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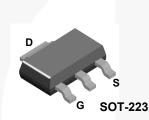
FQT4N25TF N-Channel QFET[®] MOSFET 250 V, 0.83 A, 1.75 Ω

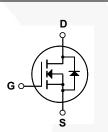
Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor®'s proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts. December 2015

Features

- 0.83 A, 250 V, R_{DS(on)}=1.75 Ω (Max.)@V_{GS}=10 V, I_D=0.415 A
- Low Gate Charge (Typ. 4.3 nC)
- Low C_{rss} (Typ. 4.8 pF)





Absolute Maximum Ratings T_c = 25°C unless otherwise noted

| Symbol | Parameter | FQT4N25TF | Unit | | |
|-----------------------------------|--|-----------|-------------|------|--|
| V _{DSS} | Drain-Source Voltage | | 250 | V | |
| I _D | Drain Current - Continuous (T _C = 25° | °C) | 0.83 | A | |
| | - Continuous (T _C = 70° | °C) | 0.66 | A | |
| l _{DM} | Drain Current - Pulsed | (Note 1) | 3.3 | А | |
| V _{GSS} | Gate-Source Voltage | | ± 30 | V | |
| E _{AS} | Single Pulsed Avalanche Energy | (Note 2) | 52 | mJ | |
| AR | Avalanche Current | (Note 1) | 0.83 | А | |
| E _{AR} | Repetitive Avalanche Energy | (Note 1) | 0.25 | mJ | |
| dv/dt | Peak Diode Recovery dv/dt | (Note 3) | 5.5 | V/ns | |
| P _D | Power Dissipation (T _C = 25°C) | | 2.5 | W | |
| | - Derate above 25°C | | 0.02 | W/°C | |
| T _J , T _{STG} | Operating and Storage Temperature Range | | -55 to +150 | °C | |
| Τ _L | Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds | | 300 | °C | |

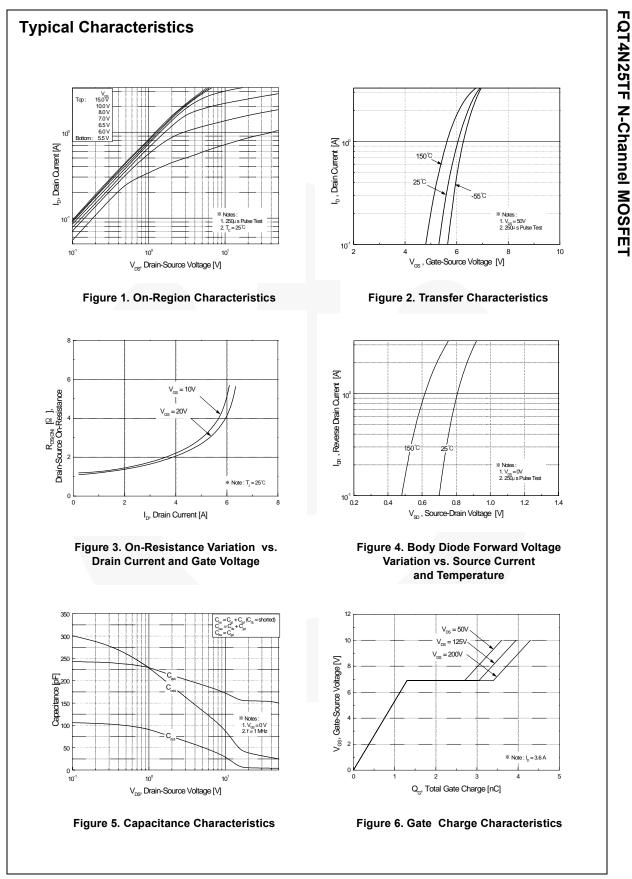
Thermal Characteristics

| Symbol | Parameter | Тур | Max | Unit |
|-----------------------|--|----------|-----|------|
| $R_{	extsf{	heta}JA}$ | Thermal Resistance, Junction-to-Ambient * | | 50 | °C/W |
| | ed on the minimum pad size recommended (PCB Mount) | <u> </u> | | |

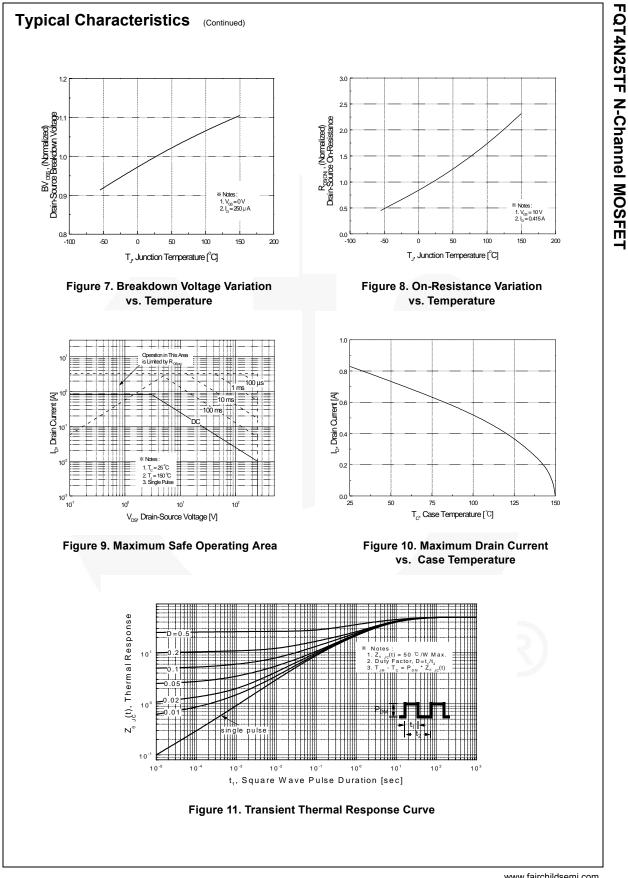
| | | Top Mark | Package | Packing Method | Reel Size | Tape Width | | Quantity | |
|--|--|--|-----------------------------|--|-------------|------------|------|------------|------|
| | | SOT-223 | 23 Tape and Reel 13" | | | 12 mm | | 2500 units | |
| Electric | cal Cha | racteristics | T _C = 25°C u | nless otherwise noted | | | | | |
| Symbol Parameter | | | Test Conditions | | Min | Тур | Max | Unit | |
| Off Cha | racteris | tics | | | | | | | |
| BV _{DSS} | | in-Source Breakdown Voltage | | V_{GS} = 0 V, I _D = 250 μ A | | 250 | | | V |
| ΔBV _{DSS} / ΔT _J | Breakdov Coefficie | down Voltage Temperature cient | | $I_D = 250 \ \mu\text{A}$, Referenced to 25°C | | | 0.22 | | V/°C |
| I _{DSS} | Zero Gate Voltage Drain Current | | ront | $V_{DS} = 250 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$ | | | | 1 | μA |
| | | | rent , | $V_{DS} = 200 \text{ V}, \text{ T}_{C} = 125^{\circ}\text{C}$ | | | | 10 | μA |
| I _{GSSF} | Gate-Boo | e-Body Leakage Current, Forward | | $V_{GS} = 30 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$ | | | | 100 | nA |
| I _{GSSR} | Gate-Boo | Body Leakage Current, Reverse | | $V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$ | | | | -100 | nA |
| On Cha | racteris | tics | | | | | | | |
| V _{GS(th)} | 1 | eshold Voltage | , | $V_{DS} = V_{GS}, I_{D} = 250 \mu A$ | | 3.0 | | 5.0 | V |
| R _{DS(on)} | Static Dra On-Resis | ain-Source stance | | V _{GS} = 10 V, I _D = 0.415 A | | | 1.38 | 1.75 | Ω |
| 9fs | Forward | Transconductance | , | V _{DS} = 50 V, I _D = 0.415 A | (Note 4) | | 1.28 | | S |
| Dvnami | ic Chara | cteristics | | | | | | | |
| C _{iss} | | pacitance | | V _{DS} = 25 V, V _{GS} = 0 V, | | | 155 | 200 | pF |
| C _{oss} | | apacitance | | f = 1.0 MHz | | | 35 | 45 | pF |
| C _{rss} | Reverse | Transfer Capacitan | | | | | 4.8 | 6.5 | pF |
| Switchi | na Char | acteristics | | | | | | | |
| t _{d(on)} | | Delay Time | | | | | 6.8 | 25 | ns |
| t _r | | Rise Time | | $V_{DD} = 125 \text{ V}, \text{ I}_{D} = 3.6 \text{ A},$ | | | 45 | 100 | ns |
| t _{d(off)} | | Delay Time | | R _G = 25 Ω | | | 6.4 | 25 | ns |
| t _f | Turn-Off | | | | (Note 4, 5) | | 22 | 55 | ns |
| Q _g | | e Charge | , | V _{DS} = 200 V, I _D = 3.6 A, | | | 4.3 | 5.6 | nC |
| Q _{gs} | | Irce Charge | | V _{DS} = 200 V, I _D = 3.0 A, V _{GS} = 10 V | | | 1.3 | | nC |
| Q _{gd} | | in Charge | | VGS - 10 V | (Note 4, 5) | | 2.1 | | nC |
| Ū. | | | | | | | | | |
| Drain-S | 1 | | | Maximum Rating | S | | | 0.83 | A |
| I _{SM} | | Maximum Continuous Drain-Source Diode Forward Current Maximum Pulsed Drain-Source Diode Forward Current | | | | | 3.3 | A | |
| V _{SD} | | urce Diode Forward | | $V_{GS} = 0 \text{ V}, \text{ I}_{S} = 0.83 \text{ A}$ | | | | 1.5 | V |
| t _{rr} | | Recovery Time | - | $V_{GS} = 0 \text{ V}, \text{ I}_{S} = 3.6 \text{ A},$ | | | 110 | | ns |
| Q _{rr} | | Recovery Charge | | dl _F / dt = 100 A/μs | (Note 4) | | 0.35 | | μC |
| L = 120mH, $I_{SD} \leq 3.6A$, Pulse Test : | I _{AS} = 0.83A, V di/dt ≤ 300A/ Pulse width ≤ | idth limited by maximum j $_{DD} = 50V$, $R_G = 25 \Omega$, Sta $_{IS}, V_{DD} \le BV_{DSS}$, Starting $_{300\mu s}$, Duty cycle $\le 2\%$ operating temperature | rting T _J = 25°C | ure | | | | | |

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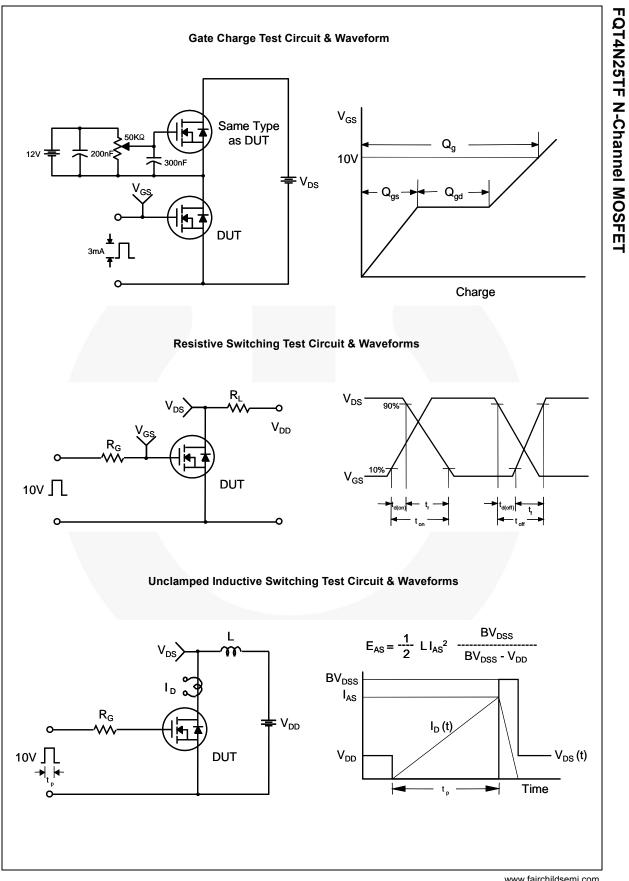
FQT4N25TF N-Channel MOSFET



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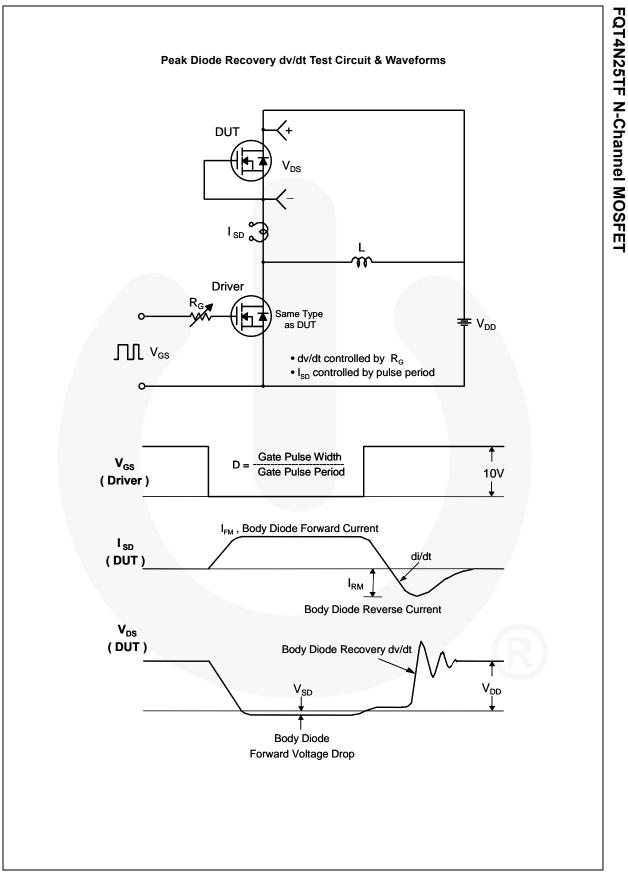


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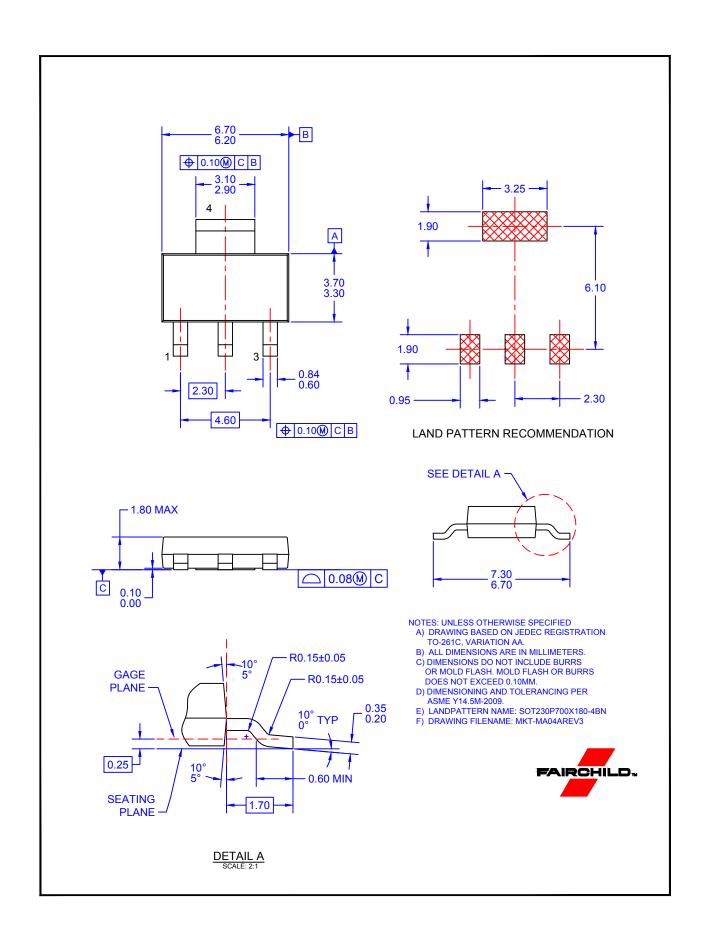


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