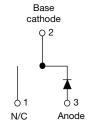


# Hyperfast Rectifier, 15 A FRED Pt®





D<sup>2</sup>PAK (TO-263AB)

PRIMARY CHARACTERISTICS								
I <sub>F(AV)</sub>	15 A							
$V_{R}$	300 V							
V <sub>F</sub> at I <sub>F</sub>	0.85 V							
t <sub>rr</sub> (typ.)	40 ns							
T <sub>J</sub> max.	175 °C							
Package	D <sup>2</sup> PAK (TO-263AB)							
Circuit configuration	Single							

#### **FEATURES**

- · Hyperfast recovery time
- Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature



- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Meets JESD 201 class 1A whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION / APPLICATIONS**

Vishay Semiconductors 300 V series are the state of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS				
Repetitive peak reverse voltage	$V_{RRM}$		300	V				
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 142 °C	15	^				
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	140	Α				
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C				

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	300	-	-					
Forward voltage	V	I <sub>F</sub> = 15 A	-	1.05	1.25	V				
	V <sub>F</sub>	I <sub>F</sub> = 15 A, T <sub>J</sub> = 125 °C	-	0.85	1.00					
Deverse legisere augment		V <sub>R</sub> = V <sub>R</sub> rated	-	0.05	40					
Reverse leakage current	I <sub>R</sub>	$T_J = 125 ^{\circ}\text{C},  V_R = V_R  \text{rated}$	-	12	400	- μΑ				
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 300 V	-	45	-	pF				
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8	-	nH				



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST C	ONDITIONS	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1.0 A, dI_F/dt$	$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$			40			
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	32	-	ns		
		T <sub>J</sub> = 125 °C	I <sub>F</sub> = 15 A	-	45	-			
Dools woods on the state of the	_	T <sub>J</sub> = 25 °C		-	2.4	-	А		
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	$dI_F/dt = -200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	6.1	-			
Poverse receivery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C	''	-	38	-	nC		
Reverse recovery charge		T <sub>J</sub> = 125 °C		-	137	-	1 IIC		

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C			
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	1.02	2.0				
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W			
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.2	-	9,11			
Weight			-	2.0	-	g			
Weight			-	0.07	-	OZ.			
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)			
Marking device		Case style D <sup>2</sup> PAK (TO-263AB)	15ETH03SH						

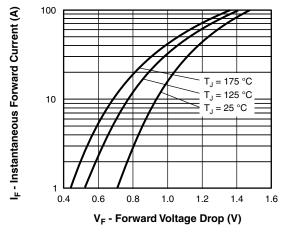


Fig. 1 - Typical Forward Voltage Drop Characteristics

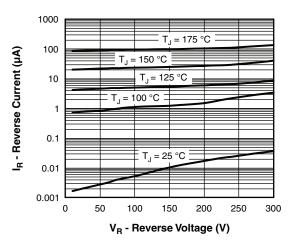


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

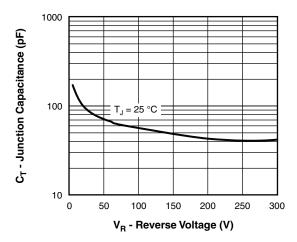


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

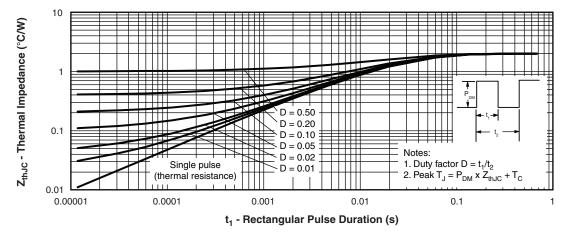


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

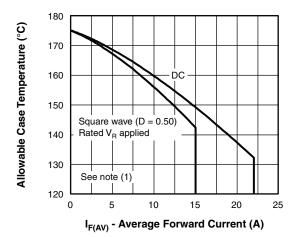
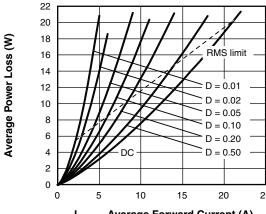


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



I<sub>F(AV)</sub> - Average Forward Current (A)

Fig. 6 - Forward Power Loss Characteristics

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# Vishay Semiconductors

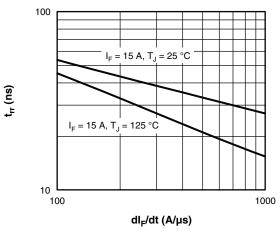


Fig. 7 - Typical Reverse Recovery Time vs.  $dI_F/dt$ 

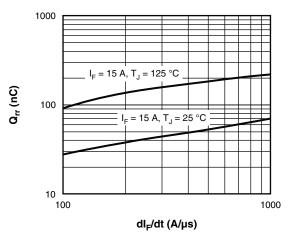
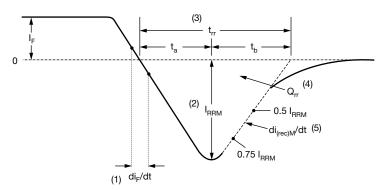


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = rated V<sub>R</sub>



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_F$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{_{\mathrm{ff}}}$  area under curve defined by  $\mathbf{t}_{_{\mathrm{ff}}}$  and  $\mathbf{I}_{\mathrm{RRM}}$

$$Q_{rr} = \frac{t_{rr} x I_{RRM}}{2}$$

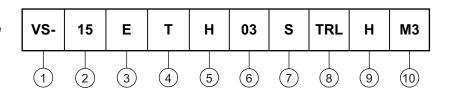
(5) di<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 9 - Reverse Recovery Waveform and Definitions



#### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating (15 A)

3 - E = single diode

4 -  $T = D^2PAK (TO-263AB)$ 

H = hyperfast rectifier

Voltage rating (03 = 300 V)

7 - S = D<sup>2</sup>PAK

8 - • None = tube (50 pieces)

• TRL = tape and reel (left oriented, for D2PAK package)

• TRR = tape and reel (right oriented, for D<sup>2</sup>PAK package)

9 - H = AEC-Q101 qualified

10 - Environmental digit:

M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION									
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-15ETH03SHM3	50	1000	Antistatic plastic tube						
VS-15ETH03STRRHM3	800	800	13" diameter reel						
VS-15ETH03STRLHM3	800	800	13" diameter reel						

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95046					
Part marking information	www.vishay.com/doc?95444					
Packaging information	www.vishay.com/doc?95032					
SPICE model	www.vishay.com/doc?96567					



### D<sup>2</sup>PAK

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIM	MILLIMETERS		INCHES		NOTES	NOTES	SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	NOTES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3	
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3	
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3	
b1	0.51	0.89	0.020	0.035	4		е	2.54 BSC 0.100 BSC					
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625		
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110		
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3	
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070		
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC		
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208		

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB

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