

OM13058,598: LPCXpresso Board for LPC11U68

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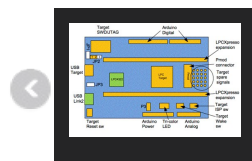
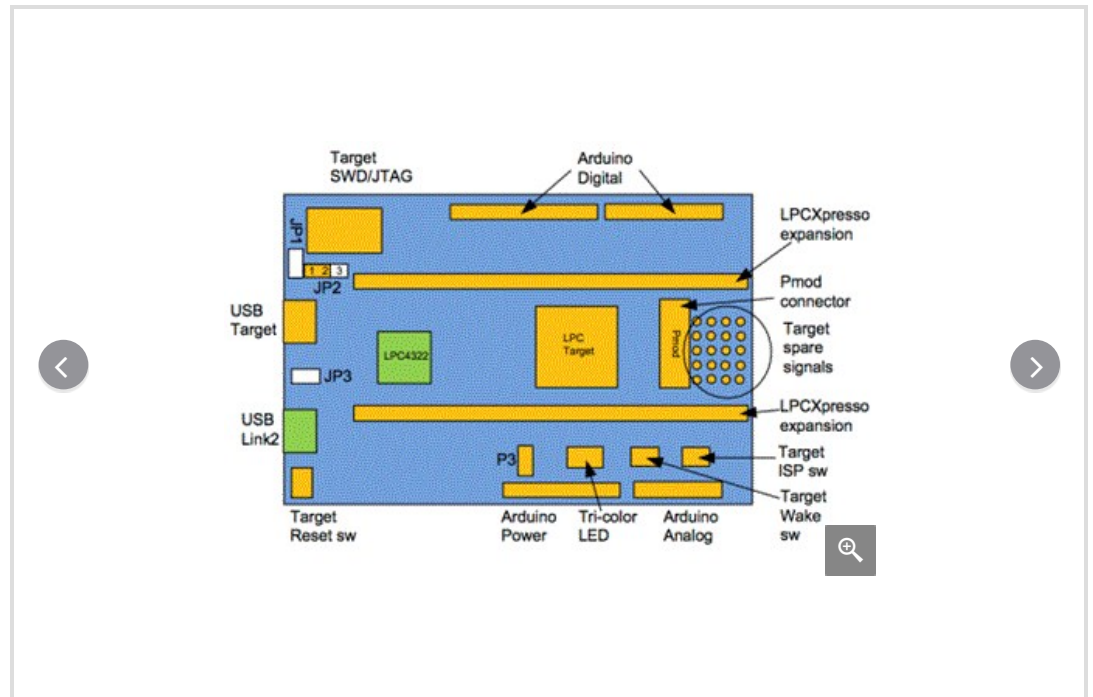
Overview

The LPC11U68 LPCXpresso board with NXP®'s LPC11U68 Cortex-M0+ microcontroller is designed to make it as easy as possible to get started with your project.

The LPC11U68 LPCXpresso board is the correct development board to use for evaluation of the LPC11E6x devices. All other LPC11Exx devices should use the appropriate LPC11Exx development boards as found on their respective product information pages.

LPCXpresso is a low-cost development platform available from NXP supporting NXP's ARM-based microcontrollers. The platform is comprised of a simplified Eclipse-based IDE and low-cost target boards which include an attached JTAG debugger. LPCXpresso is an end-to-end solution enabling embedded engineers to develop their applications from initial evaluation to final production.

Data Sheets



LPCXpresso™
Board for
LPC11U68

LPCXpresso
Version 2 Boards

Features

- Fully supported by LPCXpresso Eclipse-based IDE and GNU C/C++ toolchain, available in free and Pro versions

Supported Devices

- LPC11E67JBD48: Scalable Entry Level 32-bit Microcontroller (MCU) based on ARM® Cortex®-M0+/M0 Cores

- Integrated JTAG Debugger which can also be used to debug external target.
- LPCXpresso / mbed, PMOD and Arduino UNO expansion connectors give simple access to a wide range of available expansion boards.
- Tri-color LED, ISP & WAKE buttons for easy testing of software functionality.

Kit Contains

- LPCXpresso™ LPC11U68 Development Board

- [LPC11E68JBD100](#): Scalable Entry Level 32-bit Microcontroller (MCU) based on ARM® Cortex®-M0+/M0 Cores
- [LPC11E68JBD64](#): Scalable Entry Level 32-bit Microcontroller (MCU) based on ARM® Cortex®-M0+/M0 Cores
- [LPC11U67JBD48](#): Scalable Entry Level 32-bit Microcontroller (MCU) based on ARM® Cortex®-M0+/M0 Cores
- [LPC11U68JBD100](#): Scalable Entry Level 32-bit Microcontroller (MCU) based on ARM® Cortex®-M0+/M0 Cores
- [LPC11U68JBD48](#): Scalable Entry Level 32-bit Microcontroller (MCU) based on ARM® Cortex®-M0+/M0 Cores
- [LPC11U68JBD64](#): Scalable Entry Level 32-bit Microcontroller (MCU) based on ARM® Cortex®-M0+/M0 Cores
- [LPC11E66JBD48](#): 32-bit ARM Cortex-M0+ microcontroller; up to 64 kB flash and 12 kB SRAM; 4 kB EEPROM; 12-bit ADC
- [LPC11E67JBD100](#): Scalable Entry Level 32-bit Microcontroller (MCU) based on ARM® Cortex®-M0+/M0 Cores
- [LPC11E67JBD64](#): Scalable Entry Level 32-bit Microcontroller (MCU) based on ARM® Cortex®-M0+/M0 Cores
- [LPC11E68JBD48](#): Scalable Entry Level 32-bit Microcontroller (MCU) based on ARM® Cortex®-M0+/M0 Cores

➤ Jump Start Your Design

Debugging via the built-in debug probe

Note that you may need to install the Link2 drivers before connecting the board on a Windows platform (available on [nxp.com](#)). If you have installed LPCScript or are using LPCXpresso 7.8 or later then the drivers will already have been installed.

LPCXpresso IDE

As supplied, (JP1 - no jumper fitted, JP2 – jumper fitted across 1,2, JP3 – no jumper fitted)
[More](#) ▾

PMod™ Support

LPCXpresso V2 boards have different PMOD support depending on their target applications. Further information below:

- LPCXpresso11U68:
 - pins 1 – 6 (left side) will support Pmod

Type 2 (SPI) or Type 1 (GPIO).

- pins 7 – 12 (right side) designed to support I2C, but does not have 2.2k pull up. Will support Pmod Type 1 (GPIO).

Note: LPCXpresso11U68 (Rev C) does have a pull up on the target I2C SCL and SDA lines (these were not present on the previous revision).

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