Dual General Purpose Transistor

The MBT3906DW1 device is a spin-off of our popular SOT-23/SOT-323 three-leaded device. It is designed for general purpose amplifier applications and is housed in the SOT-363 six-leaded surface mount package. By putting two discrete devices in one package, this device is ideal for low-power surface mount applications where board space is at a premium.

Features

- h_{FE}, 100-300
- Low $V_{CE(sat)}$, $\leq 0.4 \text{ V}$
- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- Available in 8 mm, 7-inch/3,000 Unit Tape and Reel
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	V _{CEO}	-40	Vdc
Collector - Base Voltage	V _{CBO}	-40	Vdc
Emitter – Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current - Continuous	Ic	-200	mAdc
Electrostatic Discharge	ESD	HBM Class 2 MM Class B	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Package Dissipation (Note 1) T _A = 25°C	P _D	150	mW
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	833	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

Device mounted on FR4 glass epoxy printed circuit board using the minimum recommended footprint.



ON Semiconductor®

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MARKING DIAGRAM



SOT-363/SC-88 CASE 419B STYLE 1



XX = Device Code

M = Date Code

■ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

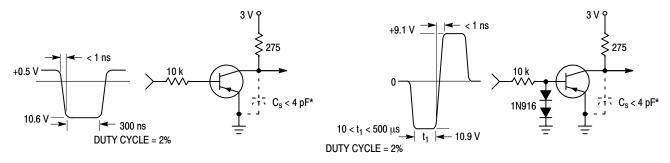
See detailed ordering, marking and shipping information in the package dimensions section on page 6 of this data sheet.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit	
OFF CHARACTE	ERISTICS					
Collector - Emitter	Breakdown Voltage (Note 2)	V _{(BR)CEO}	-40	-	Vdc	
Collector - Base B	reakdown Voltage	V _{(BR)CBO}	-40	-	Vdc	
Emitter – Base Breakdown Voltage		V _{(BR)EBO}	-5.0	-	Vdc	
Base Cutoff Curre	nt	I _{BL}	_	-50	nAdc	
Collector Cutoff Co	urrent	I _{CEX}	_	-50	nAdc	
ON CHARACTE	RISTICS (Note 2)	<u> </u>				
$(I_C = -1.0 \text{ mAdc})$ $(I_C = -10 \text{ mAdc})$ $(I_C = -50 \text{ mAdc})$	c, V _{CE} = -1.0 Vdc) c, V _{CE} = -1.0 Vdc) V _{CE} = -1.0 Vdc) V _{CE} = -1.0 Vdc) c, V _{CE} = -1.0 Vdc)	h _{FE}	60 80 100 60 30	- 300 - -	_	
Collector – Emitter Saturation Voltage (I _C = -10 mAdc, I _B = -1.0 mAdc) (I _C = -50 mAdc, I _B = -5.0 mAdc)		V _{CE(sat)}	- -	-0.25 -0.4	Vdc	
Base – Emitter Saturation Voltage ($I_C = -10$ mAdc, $I_B = -1.0$ mAdc) ($I_C = -50$ mAdc, $I_B = -5.0$ mAdc)		V _{BE(sat)}	-0.65 -	-0.85 -0.95	Vdc	
SMALL-SIGNAL	_ CHARACTERISTICS					
Current - Gain - B	andwidth Product	f _T	250	-	MHz	
Output Capacitano	ce	C _{obo}	_	4.5	pF	
Input Capacitance		C _{ibo}	-	10.0	pF	
Input Impedance (V _{CE} = -10 Vdc	, I _C = -1.0 mAdc, f = 1.0 kHz)	h _{ie}	2.0	12	kΩ	
Voltage Feedback Ratio (V _{CE} = -10 Vdc, I _C = -1.0 mAdc, f = 1.0 kHz)		h _{re}	0.1	10	X 10-4	
Small – Signal Current Gain (V _{CE} = -10 Vdc, I _C = -1.0 mAdc, f = 1.0 kHz)		h _{fe}	100	400	_	
Output Admittance $(V_{CE} = -10 \text{ Vdc}, I_C = -1.0 \text{ mAdc}, f = 1.0 \text{ kHz})$		h _{oe}	3.0	60	μmhos	
Noise Figure ($V_{CE} = -5.0 \text{ Vdc}$, $I_{C} = -100 \mu \text{Adc}$, $R_{S} = 1.0 \text{ k} \Omega$, $f = 1.0 \text{ kHz}$)		NF	-	4.0	dB	
SWITCHING CH	ARACTERISTICS					
Delay Time	$(V_{CC} = -3.0 \text{ Vdc}, V_{BE} = 0.5 \text{ Vdc})$	t _d	-	35		
Rise Time	$(I_C = -10 \text{ mAdc}, I_{B1} = -1.0 \text{ mAdc})$	t _r	-	35	ns	
Storage Time	$(V_{CC} = -3.0 \text{ Vdc}, I_{C} = -10 \text{ mAdc})$	t _s	-	225		
Fall Time	$(I_{B1} = I_{B2} = -1.0 \text{ mAdc})$	t _f	_	75	ns	

Fall Time $(I_{B1} = I_{B2} = -1.0 \text{ mAdc})$ 2. Pulse Test: Pulse Width $\leq 300 \mu \text{s}$; Duty Cycle $\leq 2.0\%$.

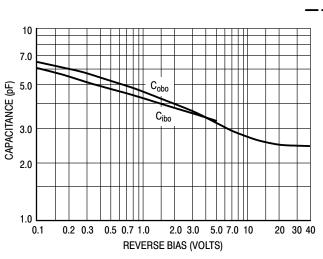


* Total shunt capacitance of test jig and connectors

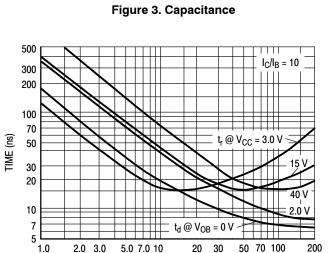
Figure 1. Delay and Rise Time **Equivalent Test Circuit**

Figure 2. Storage and Fall Time **Equivalent Test Circuit**

TYPICAL TRANSIENT CHARACTERISTICS







IC, COLLECTOR CURRENT (mA) Figure 5. Turn - On Time

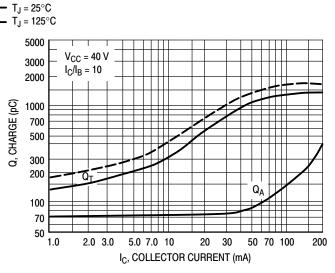


Figure 4. Charge Data

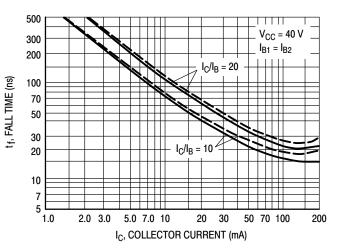
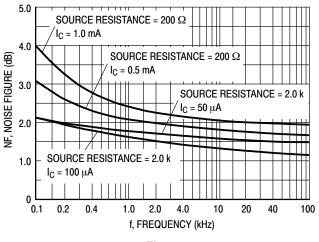


Figure 6. Fall Time

TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE VARIATIONS

 $(V_{CE} = -5.0 \text{ Vdc}, T_A = 25^{\circ}\text{C}, Bandwidth = 1.0 \text{ Hz})$



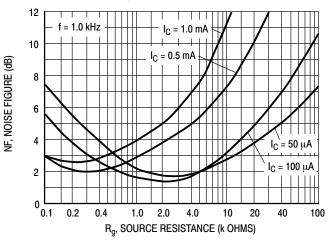
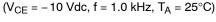
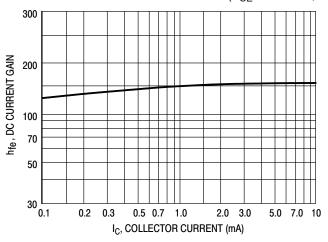


Figure 7.

Figure 8.

h PARAMETERS





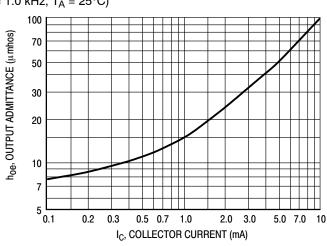
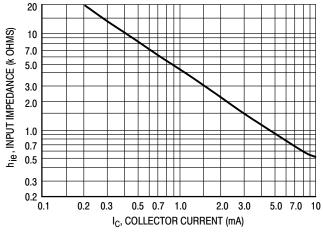


Figure 9. Current Gain

Figure 10. Output Admittance



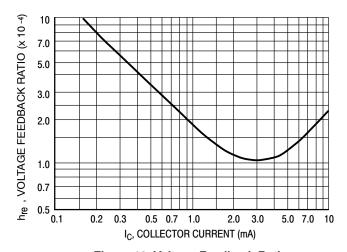


Figure 11. Input Impedance

Figure 12. Voltage Feedback Ratio

TYPICAL STATIC CHARACTERISTICS

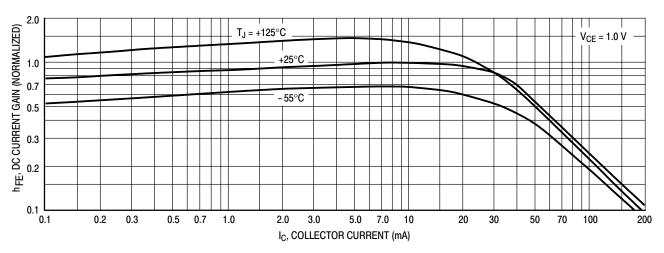


Figure 13. DC Current Gain

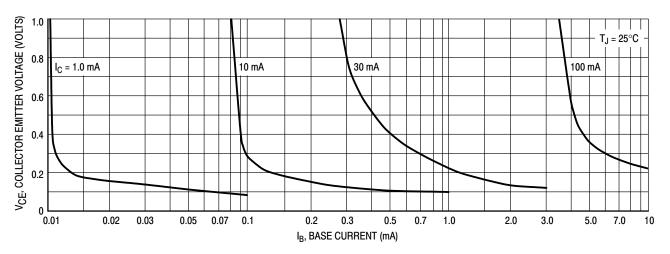


Figure 14. Collector Saturation Region

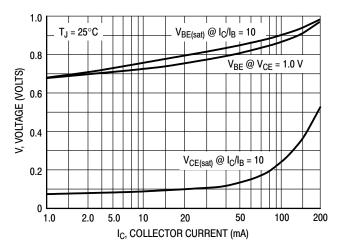


Figure 15. "ON" Voltages

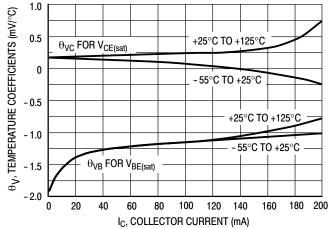


Figure 16. Temperature Coefficients

DEVICE ORDERING INFORMATION

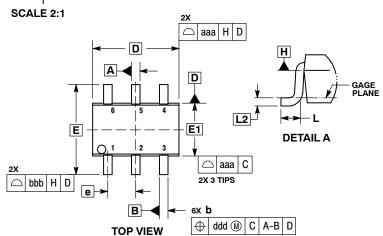
Device	Marking	Pin Out	Package	Shipping [†]
MBT3906DW1T1G	A2	6 5 4	SOT-363 (Pb-Free)	3000 / Tape & Reel
SMBT3906DW1T1G	A2	6 5 4	SOT-363 (Pb-Free)	3000 / Tape & Reel
SMBT3906DW3T1G	A3	6 5 4	SOT-363 (Pb-Free)	3000 / Tape & Reel

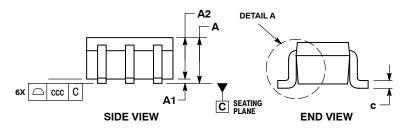
[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

SC-88/SC70-6/SOT-363 CASE 419B-02 **ISSUE Y**

NOTES:

DATE 11 DEC 2012





b 0.15 0.20 0.25 | 0.006 | 0.008 | 0.010 0.08 0.15 0.22 0.003 0.006 0.009 D E 1.80 2.00 2.20 0.070 0.078 0.086 2.10 2.20 0.078 0.082 0.086 2.00 E1 0.045 0.049 0.053 1.15 | 1.25 | 1.35 .65 BS e L L2 0.026 BSC 0.010 0.014 0.018 0.36 0.46 0.006 BSC 0.15 BSC

1.10

0.10

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS

PROTRUSIONS, OR GATE BURRS, MOLD FLASH, PROTRU-SIONS, OR GATE BURRS SHALL NOT EXCEED 0.20 PER END. DIMENSIONS D AND E1 AT THE OUTERMOST EXTREMES OF

DIMENSIONS b AND c APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.08 AND 0.15 FROM THE TIP.

DIMENSION & DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF DIMENSION 5 AT MAXIMUM MATERIAL CONDITION. THE DAMBAR CANNOT BE LOCATED ON THE LOWER

MIN

0.000

1.00 0.027 0.035 0.039

INCHES

NOM MAX

0.043

0.004

CONTROLLING DIMENSION: MILLIMETERS.
DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH,

THE PLASTIC BODY AND DATUM H.
DATUMS A AND B ARE DETERMINED AT DATUM H

RADIUS OF THE FOOT.

DIM

Α1 0.00

A2 0.70

MILLIMETERS

MIN NOM MAX

0.90

0.006 aaa bbb 0.012 ddd 0.10 0.004 0.10 0.004

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

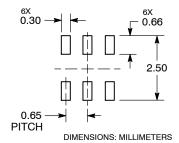
= Date Code*

= Pb-Free Package

(Note: Microdot may be in either location)

- *Date Code orientation and/or position may vary depending upon manufacturing location.
- *This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

RECOMMENDED SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

STYLES ON PAGE 2

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SC-88/SC70-6/SOT-363 CASE 419B-02 ISSUE Y

DATE 11 DEC 2012

STYLE 1: PIN 1. EMITTER 2 2. BASE 2 3. COLLECTOR 1 4. EMITTER 1 5. BASE 1 6. COLLECTOR 2	STYLE 2: CANCELLED	STYLE 3: CANCELLED	STYLE 4: PIN 1. CATHODE 2. CATHODE 3. COLLECTOR 4. EMITTER 5. BASE 6. ANODE	STYLE 5: PIN 1. ANODE 2. ANODE 3. COLLECTOR 4. EMITTER 5. BASE 6. CATHODE	STYLE 6: PIN 1. ANODE 2 2. N/C 3. CATHODE 1 4. ANODE 1 5. N/C 6. CATHODE 2
STYLE 7: PIN 1. SOURCE 2 2. DRAIN 2 3. GATE 1 4. SOURCE 1 5. DRAIN 1 6. GATE 2	STYLE 8: CANCELLED	STYLE 9: PIN 1. EMITTER 2 2. EMITTER 1 3. COLLECTOR 1 4. BASE 1 5. BASE 2 6. COLLECTOR 2	STYLE 10: PIN 1. SOURCE 2 2. SOURCE 1 3. GATE 1 4. DRAIN 1 5. DRAIN 2 6. GATE 2	STYLE 11: PIN 1. CATHODE 2 2. CATHODE 2 3. ANODE 1 4. CATHODE 1 5. CATHODE 1 6. ANODE 2	STYLE 12: PIN 1. ANODE 2 2. ANODE 2 3. CATHODE 1 4. ANODE 1 5. ANODE 1 6. CATHODE 2
STYLE 13: PIN 1. ANODE 2. N/C 3. COLLECTOR 4. EMITTER 5. BASE 6. CATHODE	STYLE 14: PIN 1. VREF 2. GND 3. GND 4. IOUT 5. VEN 6. VCC	STYLE 15: PIN 1. ANODE 1 2. ANODE 2 3. ANODE 3 4. CATHODE 3 5. CATHODE 2 6. CATHODE 1	STYLE 16: PIN 1. BASE 1 2. EMITTER 2 3. COLLECTOR 2 4. BASE 2 5. EMITTER 1 6. COLLECTOR 1	STYLE 17: PIN 1. BASE 1 2. EMITTER 1 3. COLLECTOR 2 4. BASE 2 5. EMITTER 2 6. COLLECTOR 1	STYLE 18: PIN 1. VIN1 2. VCC 3. VOUT2 4. VIN2 5. GND 6. VOUT1
STYLE 19: PIN 1. I OUT 2. GND 3. GND 4. V CC 5. V EN 6. V REF	STYLE 20: PIN 1. COLLECTOR 2. COLLECTOR 3. BASE 4. EMITTER 5. COLLECTOR 6. COLLECTOR	STYLE 21: PIN 1. ANODE 1 2. N/C 3. ANODE 2 4. CATHODE 2 5. N/C 6. CATHODE 1	STYLE 22: PIN 1. D1 (i) 2. GND 3. D2 (i) 4. D2 (c) 5. VBUS 6. D1 (c)	STYLE 23: PIN 1. Vn 2. CH1 3. Vp 4. N/C 5. CH2 6. N/C	STYLE 24: PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE 5. CATHODE 6. CATHODE
STYLE 25: PIN 1. BASE 1 2. CATHODE 3. COLLECTOR 2 4. BASE 2 5. EMITTER 6. COLLECTOR 1	STYLE 26: PIN 1. SOURCE 1 2. GATE 1 3. DRAIN 2 4. SOURCE 2 5. GATE 2 6. DRAIN 1	STYLE 27: PIN 1. BASE 2 2. BASE 1 3. COLLECTOR 1 4. EMITTER 1 5. EMITTER 2 6. COLLECTOR 2	STYLE 28: PIN 1. DRAIN 2. DRAIN 3. GATE 4. SOURCE 5. DRAIN 6. DRAIN	STYLE 29: PIN 1. ANODE 2. ANODE 3. COLLECTOR 4. EMITTER 5. BASE/ANODE 6. CATHODE	STYLE 30: PIN 1. SOURCE 1 2. DRAIN 2 3. DRAIN 2 4. SOURCE 2 5. GATE 1 6. DRAIN 1

Note: Please refer to datasheet for style callout. If style type is not called out in the datasheet refer to the device datasheet pinout or pin assignment.

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