## MBT3906DW1

## Dual General Purpose Transistor

The MBT3906DW1 device is a spin-off of our popular SOT-23/SOT-323 three-leaded device. It is designed for general purpose amplifier applications and is housed in the SOT-363 six-leaded surface mount package. By putting two discrete devices in one package, this device is ideal for low-power surface mount applications where board space is at a premium.

## Features

- $\mathrm{h}_{\mathrm{FE}}, 100-300$
- Low $\mathrm{V}_{\mathrm{CE}(\mathrm{sat})}, \leq 0.4 \mathrm{~V}$
- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- Available in $8 \mathrm{~mm}, 7$-inch/3,000 Unit Tape and Reel
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are $\mathrm{Pb}-$ Free, Halogen Free/BFR Free and are RoHS Compliant*


## MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Collector-Emitter Voltage | $\mathrm{V}_{\text {CEO }}$ | -40 | Vdc |
| Collector - Base Voltage | $\mathrm{V}_{\text {CBO }}$ | -40 | Vdc |
| Emitter-Base Voltage | $\mathrm{V}_{\text {EBO }}$ | -5.0 | Vdc |
| Collector Current - Continuous | $\mathrm{I}_{\mathrm{C}}$ | -200 | mAdc |
| Electrostatic Discharge | ESD | HBM Class 2 <br> MM Class B |  |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit |
| :---: | :---: | :---: | :---: |
| Total Package Dissipation (Note 1) <br> $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ | $\mathrm{P}_{\mathrm{D}}$ | 150 | mW |
| Thermal Resistance, <br> Junction-to-Ambient | $\mathrm{R}_{\theta \mathrm{JA}}$ | 833 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Junction and Storage <br> Temperature Range | $\mathrm{T}_{\mathrm{J}}, \mathrm{T}_{\text {stg }}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

1. Device mounted on FR4 glass epoxy printed circuit board using the minimum recommended footprint.
*For additional information on our $\mathrm{Pb}-$ Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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ORDERING INFORMATION
See detailed ordering, marking and shipping information in the package dimensions section on page 6 of this data sheet.

ELECTRICAL CHARACTERISTICS $\left(\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}\right.$ unless otherwise noted)

| Characteristic | Symbol | Min | Max | Unit |
| :---: | :---: | :---: | :---: | :---: |
| OFF CHARACTERISTICS |  |  |  |  |
| Collector - Emitter Breakdown Voltage (Note 2) | $V_{\text {(BR)CEO }}$ | -40 | - | Vdc |
| Collector-Base Breakdown Voltage | $V_{\text {(BR) }}{ }^{\text {CBO }}$ | -40 | - | Vdc |
| Emitter - Base Breakdown Voltage | $\mathrm{V}_{\text {(BR)EBO }}$ | -5.0 | - | Vdc |
| Base Cutoff Current | $\mathrm{I}_{\mathrm{BL}}$ | - | -50 | nAdc |
| Collector Cutoff Current | $I_{\text {CEX }}$ | - | -50 | nAdc |

ON CHARACTERISTICS (Note 2)

| DC Current Gain $\left(\mathrm{I}_{\mathrm{C}}=-0.1 \mathrm{mAdc}, \mathrm{V}_{\mathrm{CE}}=-1.0 \mathrm{Vdc}\right)$ ( $\mathrm{I}_{\mathrm{C}}=-1.0 \mathrm{mAdc}, \mathrm{V}_{\mathrm{CE}}=-1.0 \mathrm{Vdc}$ ) $\left(I_{C}=-10 \mathrm{mAdc}, \mathrm{V}_{\mathrm{CE}}=-1.0 \mathrm{Vdc}\right)$ ( $\mathrm{I}_{\mathrm{C}}=-50 \mathrm{mAdc}, \mathrm{V}_{\mathrm{CE}}=-1.0 \mathrm{Vdc}$ ) $\left(I_{C}=-100 \mathrm{mAdc}, \mathrm{V}_{\mathrm{CE}}=-1.0 \mathrm{Vdc}\right)$ | $h_{\text {FE }}$ | $\begin{gathered} 60 \\ 80 \\ 100 \\ 60 \\ 30 \end{gathered}$ | $\begin{gathered} - \\ - \\ 300 \\ - \\ - \end{gathered}$ | - |
| :---: | :---: | :---: | :---: | :---: |
| Collector-Emitter Saturation Voltage $\left(I_{C}=-10 \mathrm{mAdc}, \mathrm{I}_{\mathrm{B}}=-1.0 \mathrm{mAdc}\right)$ $\left(I_{C}=-50 \mathrm{mAdc}, \mathrm{I}_{\mathrm{B}}=-5.0 \mathrm{mAdc}\right)$ | $\mathrm{V}_{\text {CE(sat) }}$ | - | $\begin{aligned} & -0.25 \\ & -0.4 \end{aligned}$ | Vdc |
| Base-Emitter Saturation Voltage ( $\mathrm{I}_{\mathrm{C}}=-10 \mathrm{mAdc}, \mathrm{I}_{\mathrm{B}}=-1.0 \mathrm{mAdc}$ ) ( $\mathrm{I}_{\mathrm{C}}=-50 \mathrm{mAdc}, \mathrm{I}_{\mathrm{B}}=-5.0 \mathrm{mAdc}$ ) | $\mathrm{V}_{\mathrm{BE} \text { (sat) }}$ | $-0.65$ | $\begin{aligned} & -0.85 \\ & -0.95 \end{aligned}$ | Vdc |

SMALL-SIGNAL CHARACTERISTICS

| Current-Gain - Bandwidth Product | $\mathrm{f}_{\mathrm{T}}$ | 250 | - | MHz |
| :---: | :---: | :---: | :---: | :---: |
| Output Capacitance | $\mathrm{C}_{\text {obo }}$ | - | 4.5 | pF |
| Input Capacitance | $\mathrm{C}_{\text {ibo }}$ | - | 10.0 | pF |
| Input Impedance $\left(\mathrm{V}_{\mathrm{CE}}=-10 \mathrm{Vdc}, \mathrm{I}_{\mathrm{C}}=-1.0 \mathrm{mAdc}, \mathrm{f}=1.0 \mathrm{kHz}\right)$ | $\mathrm{h}_{\text {ie }}$ | 2.0 | 12 | $\mathrm{k} \Omega$ |
| Voltage Feedback Ratio $\left(\mathrm{V}_{\mathrm{CE}}=-10 \mathrm{Vdc}, \mathrm{I}_{\mathrm{C}}=-1.0 \mathrm{mAdc}, \mathrm{f}=1.0 \mathrm{kHz}\right)$ | $\mathrm{h}_{\text {re }}$ | 0.1 | 10 | X $10^{-4}$ |
| Small-Signal Current Gain $\left(\mathrm{V}_{\mathrm{CE}}=-10 \mathrm{Vdc}, \mathrm{I}_{\mathrm{C}}=-1.0 \mathrm{mAdc}, \mathrm{f}=1.0 \mathrm{kHz}\right)$ | $\mathrm{hfe}_{\text {fe }}$ | 100 | 400 | - |
| Output Admittance $\left(\mathrm{V}_{\mathrm{CE}}=-10 \mathrm{Vdc}, \mathrm{I}_{\mathrm{C}}=-1.0 \mathrm{mAdc}, \mathrm{f}=1.0 \mathrm{kHz}\right)$ | $\mathrm{h}_{\mathrm{oe}}$ | 3.0 | 60 | $\mu \mathrm{mhos}$ |
| Noise Figure $\left(\mathrm{V}_{\mathrm{CE}}=-5.0 \mathrm{Vdc}, \mathrm{I}_{\mathrm{C}}=-100 \mu \mathrm{Adc}, \mathrm{R}_{\mathrm{S}}=1.0 \mathrm{k} \Omega, \mathrm{f}=1.0 \mathrm{kHz}\right)$ | NF | - | 4.0 | dB |

## SWITCHING CHARACTERISTICS

| Delay Time | $\left(\mathrm{V}_{\mathrm{CC}}=-3.0 \mathrm{Vdc}, \mathrm{V}_{\mathrm{BE}}=0.5 \mathrm{Vdc}\right)$ | $\mathrm{t}_{\mathrm{d}}$ | - | 35 |  |
| :--- | :--- | :--- | :--- | :---: | :---: |
| Rise Time | $\left(\mathrm{I}_{\mathrm{C}}=-10 \mathrm{mAdc}, \mathrm{I}_{\mathrm{B} 1}=-1.0 \mathrm{mAdc}\right)$ | $\mathrm{t}_{\mathrm{r}}$ | - | 3 |  |
| Storage Time | $\left(\mathrm{V}_{\mathrm{CC}}=-3.0 \mathrm{Vdc}, \mathrm{I}_{\mathrm{C}}=-10 \mathrm{mAdc}\right)$ | $\mathrm{t}_{\mathrm{s}}$ | - | - | 225 |
| Fall Time | $\left(\mathrm{I}_{\mathrm{B} 1}=\mathrm{I}_{\mathrm{B} 2}=-1.0 \mathrm{mAdc}\right)$ | $\mathrm{t}_{\mathrm{f}}$ | - | 75 |  |

2. Pulse Test: Pulse Width $\leq 300$ us; Duty Cycle $\leq 2.0 \%$.


Figure 1. Delay and Rise Time Equivalent Test Circuit

Figure 2. Storage and Fall Time Equivalent Test Circuit

## TYPICAL TRANSIENT CHARACTERISTICS

$$
\begin{aligned}
& \text { —— } \mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C} \\
& \text { —— } \mathrm{T}_{\mathrm{J}}=125^{\circ} \mathrm{C}
\end{aligned}
$$



Figure 3. Capacitance


Figure 5. Turn-On Time


Figure 4. Charge Data


Figure 6. Fall Time

## MBT3906DW1

TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE VARIATIONS
$\left(V_{C E}=-5.0 \mathrm{Vdc}, \mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}\right.$, Bandwidth $=1.0 \mathrm{~Hz}$ )


Figure 7.


Figure 8.

## h PARAMETERS

$\left(\mathrm{V}_{\mathrm{CE}}=-10 \mathrm{Vdc}, \mathrm{f}=1.0 \mathrm{kHz}, \mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}\right)$


Figure 9. Current Gain


Figure 11. Input Impedance


Figure 10. Output Admittance


Figure 12. Voltage Feedback Ratio

TYPICAL STATIC CHARACTERISTICS


Figure 13. DC Current Gain


Figure 14. Collector Saturation Region


Figure 15. "ON" Voltages


Figure 16. Temperature Coefficients

## MBT3906DW1

DEVICE ORDERING INFORMATION

| Device | Marking | Pin Out | Package | Shipping $^{\dagger}$ |
| :--- | :---: | :---: | :---: | :---: |
| MBT3906DW1T1G | A2 | SOT-363 <br> (Pb-Free) | $3000 /$ Tape \& Reel |  |
|  |  |  |  |  |
| SMBT3906DW1T1G |  |  |  |  |

$\dagger$ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.


RECOMMENDED SOLDERING FOOTPRINT*

*For additional information on our Pb -Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: MILLIMETERS.
2. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.20 PER END.
3. DIMENSIONS D AND E1 AT THE OUTERMOST EXTREMES OF DIMENSIONS D AND E1 AT THE OUT
THE PLASTIC BODY AND DATUM H.
DATUMS A AND B ARE DETERMINED AT DATUM H
4. DIMENSIONS b AND c APPLY TO THE FLAT SECTION OF THE DIMENSIONS b AND c APPLY TO THE FLAT SEC
LEAD BETWEEN 0.08 AND 0.15 FROM THE TIP.
. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF DIMENSION b AT MAXIMUM MATERIAL CONDITION. THE DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT.

| DIM | MILLIMETERS |  |  | INCHES |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MIN | NOM | MAX | MIN | NOM | MAX |
| A | --- | --- | 1.10 | --- | --- | 0.043 |
| A1 | 0.00 | -- | 0.10 | 0.000 | --- | 0.004 |
| A2 | 0.70 | 0.90 | 1.00 | 0.027 | 0.035 | 0.039 |
| b | 0.15 | 0.20 | 0.25 | 0.006 | 0.008 | 0.010 |
| C | 0.08 | 0.15 | 0.22 | 0.003 | 0.006 | 0.009 |
| D | 1.80 | 2.00 | 2.20 | 0.070 | 0.078 | 0.086 |
| E | 2.00 | 2.10 | 2.20 | 0.078 | 0.082 | 0.086 |
| E1 | 1.15 | 1.25 | 1.35 | 0.045 | 0.049 | 0.053 |
| e | 0.65 BSC |  |  | 0.026 BSC |  |  |
| L | 0.26 | 0.36 | 0.46 | 0.010 | 0.014 | 0.018 |
| L2 | 0.15 BSC |  |  | 0.006 BSC |  |  |
| aaa | 0.15 |  |  | 0.006 |  |  |
| bbb | 0.30 |  |  | 0.012 |  |  |
| ccc | 0.10 |  |  | 0.004 |  |  |
| ddd | 0.10 |  |  | 0.004 |  |  |
|  | GENERIC |  |  |  |  |  |
|  | MARKING DIAGRAM* |  |  |  |  |  |



XXX = Specific Device Code
M = Date Code*

- = Pb-Free Package
(Note: Microdot may be in either location)
*Date Code orientation and/or position may vary depending upon manufacturing location.
*This information is generic. Please refer to device data sheet for actual part marking. $\mathrm{Pb}-\mathrm{Free}$ indicator, " G " or microdot " B ", may or may not be present. Some products may not follow the Generic Marking.


## STYLES ON PAGE 2

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## SC-88/SC70-6/SOT-363

CASE 419B-02
ISSUE Y
STYLE 1:
PIN 1. EMITTER 2
2. BASE 2
3. COLLECTOR 1
4. EMITTER 1
5. BASE 1
6. COLLECTOR 2

STYLE 7:
PIN 1. SOURCE 2
2. DRAIN 2
3. GATE 1
4. SOURCE 1
5. DRAIN 1
6. GATE 2

STYLE 13:
PIN 1. ANODE
2. N/C
3. COLLECTOR
4. EMITTER
5. BASE
6. CATHODE

STYLE 19:
PIN 1. IOUT
2. GND
3. GND
4. V CC
5. V EN
6. V REF
STYLE $25:$
PIN 1. BASE 1
2. CATHODE
3. COLLECTOR 2
4. BASE 2
5. EMITTER
6. COLLECTOR 1
STYLE 2:
CANCELLED

STYLE 8:
CANCELLED

STYLE 14:
PIN 1. VREF
2. GND
3. GND
4. IOUT
5. VEN
6. VCC

STYLE 20:
PIN 1. COLLECTOR
2. COLLECTOR
3. BASE
4. EMITTER
5. COLLECTOR
6. COLLECTOR
STYLE 26:
PIN 1. SOURCE 1
2. GATE 1
3. DRAIN 2
4. SOURCE 2
5. GATE 2
6. DRAIN 1

| STYLE $3:$ <br> CANCELLED | STYLE 4: <br> PIN 1. CATHODE <br> 2. CATHODE <br> 3. COLLECTOR <br> 4. EMITTER <br> 5. BASE <br> 6. ANODE | STYLE 5: <br> PIN 1. ANODE <br> 2. ANODE <br> 3. COLLECTOR <br> 4. EMITTER <br> 5. BASE <br> 6. CATHODE | STYLE 6: <br> PIN 1. ANODE 2 <br> 2. $\mathrm{N} / \mathrm{C}$ <br> 3. CATHODE 1 <br> 4. ANODE 1 <br> 5. N/C <br> 6. CATHODE 2 |
| :---: | :---: | :---: | :---: |
| STYLE 9: | STYLE 10: | STYLE 11: | STYLE 12: |
| PIN 1. EMITTER 2 | PIN 1. SOURCE 2 | PIN 1. CATHODE 2 | PIN 1. ANODE 2 |
| 2. EMITTER 1 | 2. SOURCE 1 | 2. CATHODE 2 | 2. ANODE 2 |
| 3. COLLECTOR 1 | 3. GATE 1 | 3. ANODE 1 | 3. CATHODE 1 |
| 4. BASE 1 | 4. DRAIN 1 | 4. CATHODE 1 | 4. ANODE 1 |
| 5. BASE 2 | 5. DRAIN 2 | 5. CATHODE 1 | 5. ANODE 1 |
| 6. COLLECTOR 2 | 6. GATE 2 | 6. ANODE 2 | 6. CATHODE 2 |
| STYLE 15: | STYLE 16: | STYLE 17: | STYLE 18: |
| PIN 1. ANODE 1 | PIN 1. BASE 1 | PIN 1. BASE 1 | PIN 1. VIN1 |
| 2. ANODE 2 | 2. EMITTER 2 | 2. EMITTER 1 | 2. VCC |
| 3. ANODE 3 | 3. COLLECTOR 2 | 3. COLLECTOR 2 | 3. VOUT2 |
| 4. CATHODE 3 | 4. BASE 2 | 4. BASE 2 | 4. VIN2 |
| 5. CATHODE 2 | 5. EMITTER 1 | 5. EMITTER 2 | 5. GND |
| 6. CATHODE 1 | 6. COLLECTOR 1 | 6. COLLECTOR 1 | 6. VOUT1 |
| STYLE 21: | STYLE 22: | STYLE 23: | STYLE 24: |
| PIN 1. ANODE 1 | PIN 1. D1 (i) | PIN 1. Vn | PIN 1. CATHODE |
| 2. N/C | 2. GND | 2. CH 1 | 2. ANODE |
| 3. ANODE 2 | 3. D2 (i) | 3. Vp | 3. CATHODE |
| 4. CATHODE 2 | 4. D2 (c) | 4. N/C | 4. CATHODE |
| 5. N/C | 5. VBUS | 5. CH 2 | 5. CATHODE |
| 6. CATHODE 1 | 6. D1 (c) | 6. N/C | 6. CATHODE |
| STYLE 27: | STYLE 28: | STYLE 29: | STYLE 30: |
| PIN 1. BASE 2 | PIN 1. DRAIN | PIN 1. ANODE | PIN 1. SOURCE 1 |
| 2. BASE 1 | 2. DRAIN | 2. ANODE | 2. DRAIN 2 |
| 3. COLLECTOR 1 | 3. GATE | 3. COLLECTOR | 3. DRAIN 2 |
| 4. EMITTER 1 | 4. SOURCE | 4. EMITTER | 4. SOURCE 2 |
| 5. EMITTER 2 | 5. DRAIN | 5. BASE/ANODE | 5. GATE 1 |
| 6. COLLECTOR 2 | 6. DRAIN | 6. CATHODE | 6. DRAIN 1 |

Note: Please refer to datasheet for style callout. If style type is not called out in the datasheet refer to the device datasheet pinout or pin assignment.

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