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FDP10N60NZ / FDPF10N60NZ N-Channel UniFETTM II MOSFET 600 V, 10 A, 750 mΩ

Features

- R_{DS(on)} = 640 mΩ (Typ.) @ V_{GS} = 10 V, I_D = 5 A
- Low Gate Charge (Typ. 23 nC)
- Low C_{rss} (Typ. 10 pF)
- · 100% Avalanche Tested
- · Improved dv/dt Capability
- · ESD Improved Capability
- RoHS Compliant

Applications

- LCD/ LED/ PDP TV
- Lighting
- Uninterruptible Power Supply

Description

TO-220F

UniFETTM II MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on advanced planar stripe and DMOS technology. This advanced MOSFET family has the smallest on-state resistance among the planar MOSFET, and also provides superior switching performance and higher avalanche energy strength. In addition, internal gate-source ESD diode allows UniFET II MOSFET to withstand over 2kV HBM surge stress. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.



D

MOSFET Maximum Ratings T_C = 25°C unless otherwise noted.

TO-220

Symbol	Parameter			FDP10N60NZ	FDPF10N60NZ	Unit	
V _{DSS}	Drain to Source Voltage			6	V		
V _{GSS}	Gate to Source Voltage		±	V			
I _D	Drain Current	- Continuous (T _C = 25 ^o C)		10	10*	٨	
		- Continuous (T _C = 100 ^o C)		6	6*	A	
I _{DM}	Drain Current	- Pulsed	(Note 1)	40	40*	Α	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		5	mJ			
I _{AR}	Avalanche Current		(Note 1)	10		Α	
E _{AR}	Repetitive Avalanche Energy		(Note 1)	18.5		mJ	
dv/dt	Peak Diode Recovery dv/dt (I		(Note 3)	10		V/ns	
P _D	Dower Dissinction	(T _C = 25°C)		185	38	W	
	Power Dissipation	- Derate Above 25°C	- Derate Above 25°C		0.3	W/ºC	
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to	°C			
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds			3	°C		

'D_S

Dran current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	FDP10N60NZ	FDPF10N60NZ	Unit
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case, Max.	0.68	3.3	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	62.5	°C/W

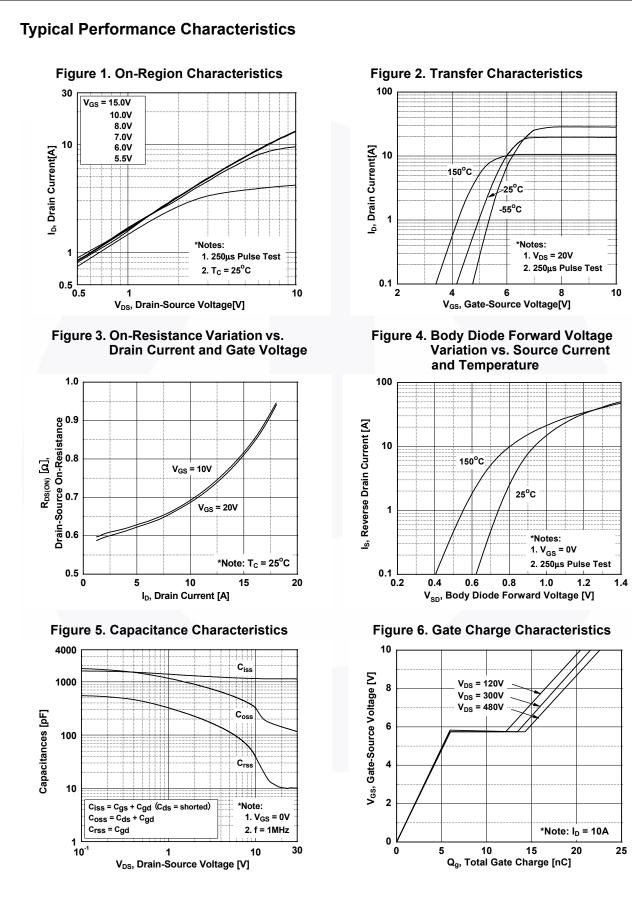
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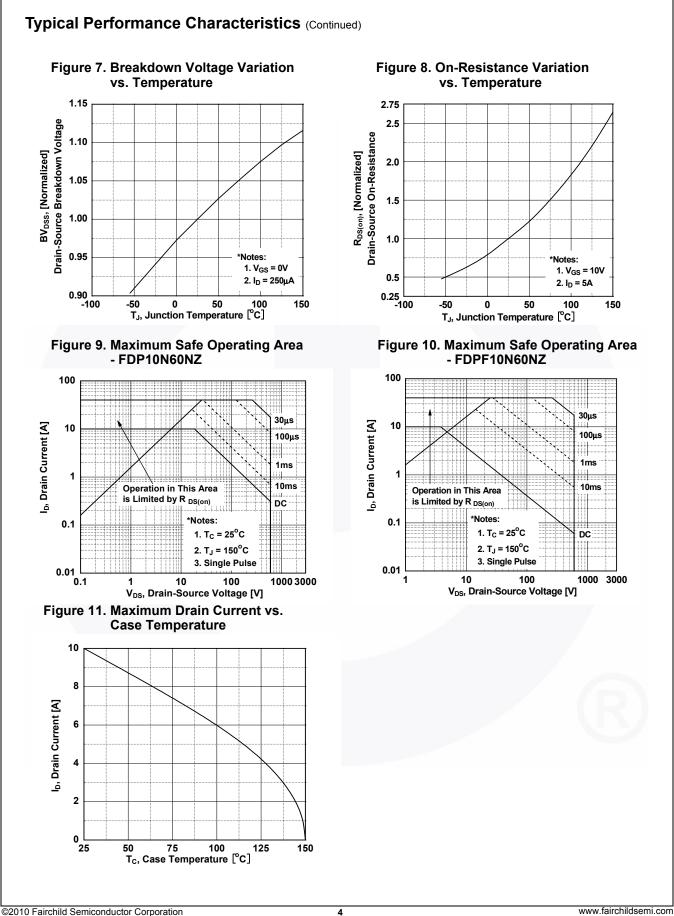
Part Number		Top Mark	Package	kage Packing Method Reel Size) Т	ape Width	Qu	antity	
		FDP10N60NZ	TO-220	Tube	N/A		N/A		50 units	
FDPF10N60NZ FDPF10N60NZ		FDPF10N60NZ	TO-220F	Tube	N/A		N/A		50 units	
Electrica	l Chara	acteristics T _C = 25°C	unless other	wise noted.						
Symbol		Parameter		Test Condition	s	Min.	Тур.	Max.	Unit	
off Charac	teristics								1	
BV _{DSS}		Source Breakdown Voltage	e lo =	250 μΑ, V _{GS} = 0 V, T	$= 25^{\circ}C$	600	-	-	V	
ΔBV_{DSS}	Breakdown Voltage Temperature									
/ΔTJ	Coefficie	U 1	I _D =	$I_D = 250 \ \mu$ A, Referenced to 25° C			0.6	-	V/ºC	
	Zero Gate Voltage Drain Current			V_{DS} = 600 V, V_{GS} = 0 V		-	-	1	μA	
DSS				V_{DS} = 480 V, T_{C} = 125°C				10	μη	
I _{GSS}	Gate to E	Body Leakage Current	V _{GS}	s = ±25 V, V _{DS} = 0 V		-	-	±10	μA	
On Charac	teristics	;								
V _{GS(th)}	Gate Thr	reshold Voltage	V _{GS}	_S = V _{DS} , I _D = 250 μA		3.0	-	5.0	V	
R _{DS(on)}		ain to Source On Resistand		_s = 10 V, I _D = 5 A		-	0.64	0.75	Ω	
9 _{FS}	Forward	Transconductance	V _{DS}	_s = 20 V, I _D = 5 A		-	14	-	S	
Dynamic C	haracte	ristics								
C _{iss}		pacitance				<u> </u>	1110	1475	pF	
C _{oss}	Output C	apacitance	$V_{\rm DS} = 25 \text{ V}, \text{ V}_{\rm GS} = 0 \text{ V},$		-	-	130	175	pF	
C _{rss}		Transfer Capacitance	t = '	f = 1 MHz			10	15	pF	
Q _g		te Charge at 10V	Vpc	V _{DS} = 480 V, I _D = 10 A, V _{GS} = 10 V		-	23	30	nC	
Q _{gs}	Gate to S	Source Gate Charge				-	6	-	nC	
Q _{gd}	Gate to D	te to Drain "Miller" Charge		(Note 4)			8	-	nC	
Switching	Charact	eristics			I.		I			
t _{d(on)}						_	25	60	ns	
t _r	Turn-On Delay Time Turn-On Rise Time Turn-Off Delay Time			$V_{DD} = 300 \text{ V}, \text{ I}_{D} = 10 \text{ A},$ $V_{GS} = 10 \text{ V}, \text{ R}_{G} = 25 \text{ W}$			50	110	ns	
t _{d(off)}			▼GS	$V_{GS} = 10 \text{ V}, N_G = 23 \text{ W}$		-	70	150	ns	
t _f		Fall Time		(Note 4)		-	50	110	ns	
		e Characteristics						-		
I _s	-	n Continuous Drain to Sour	ce Diode For	ward Current		<u> </u>	-	10	A	
I _{SM}	Maximum Pulsed Drain to Source Diode Fo					-	-	40	Α	
V _{SD}	Drain to Source Diode Forward Voltage		1	$V_{GS} = 0 \text{ V}, \text{ I}_{SD} = 10 \text{ A}$			-	1.4	V	
t _{rr}		Recovery Time	$V_{GS} = 0 V, I_{SD} = 10 A,$ $V_{GS} = 0 V, I_{SD} = 10 A,$			-	300		ns	
11	Reverse Recovery Charge			$dI_{\rm F}/dt = 100 A/\mu s$			2.0	-	μC	

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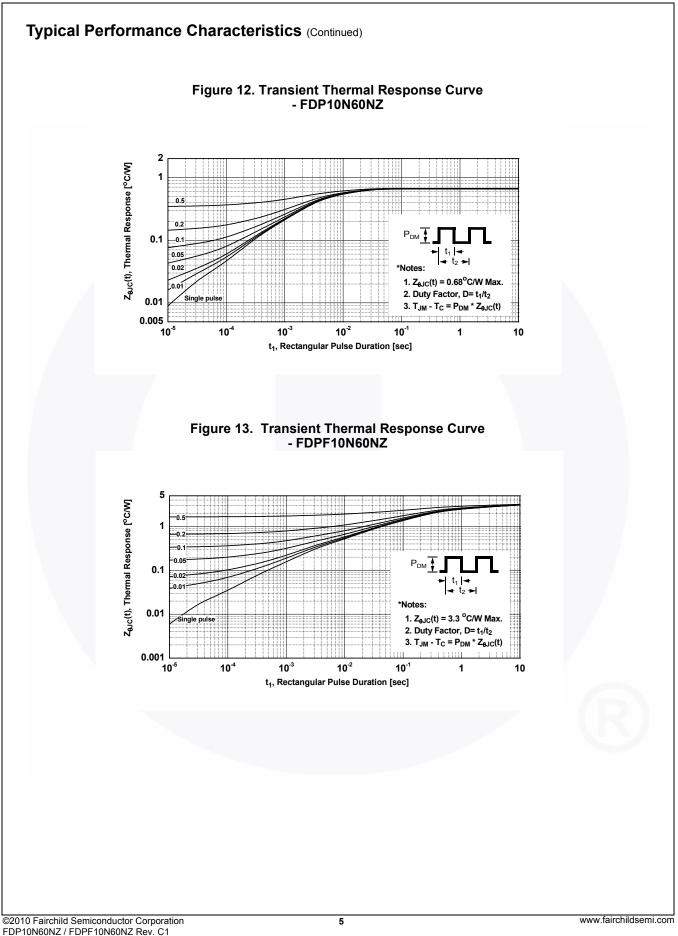


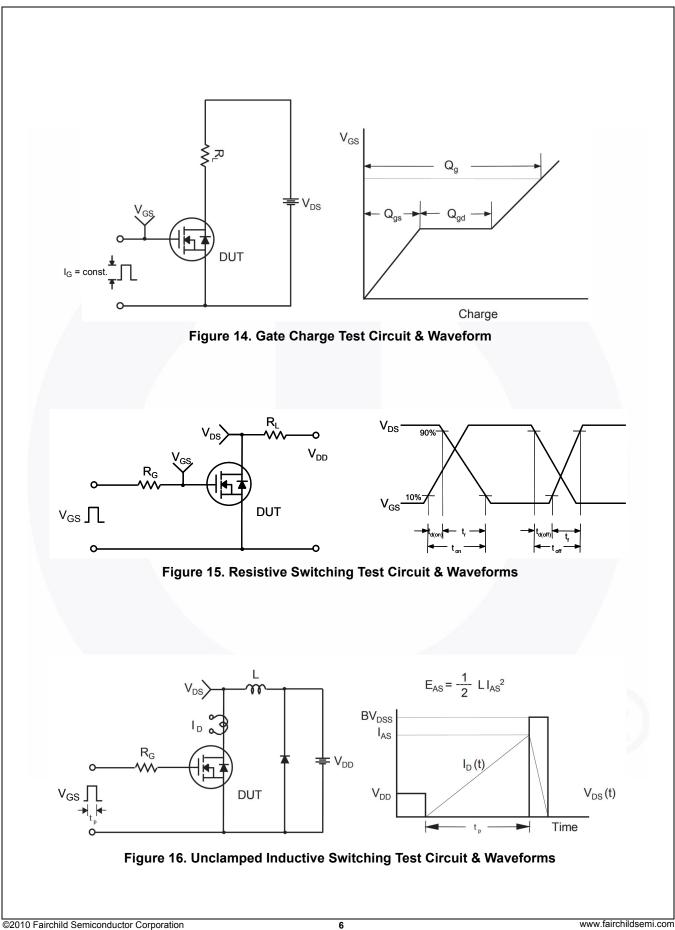
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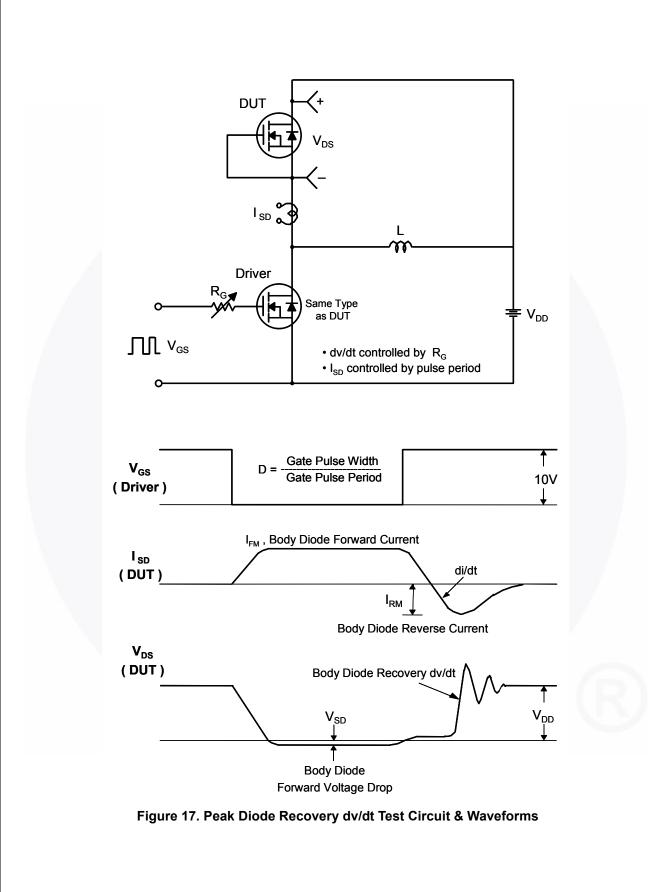
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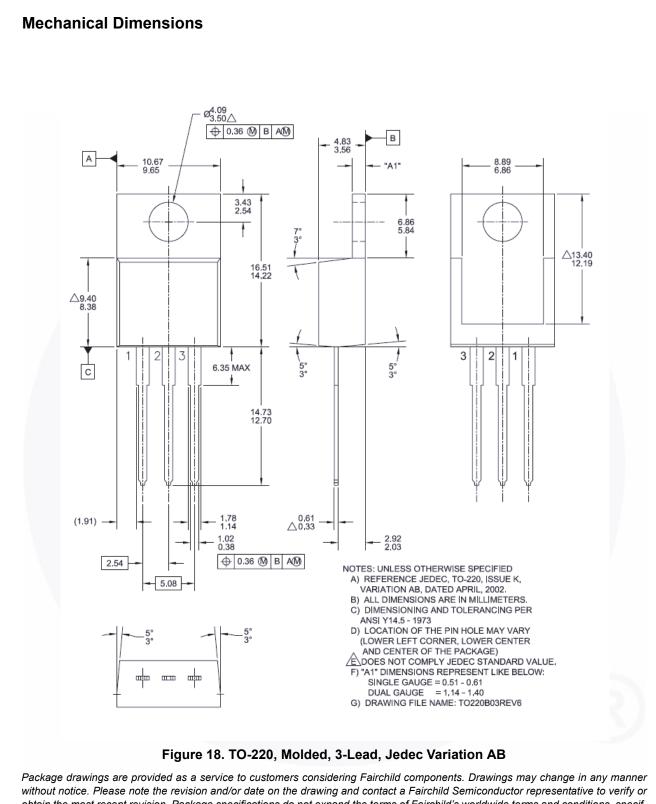




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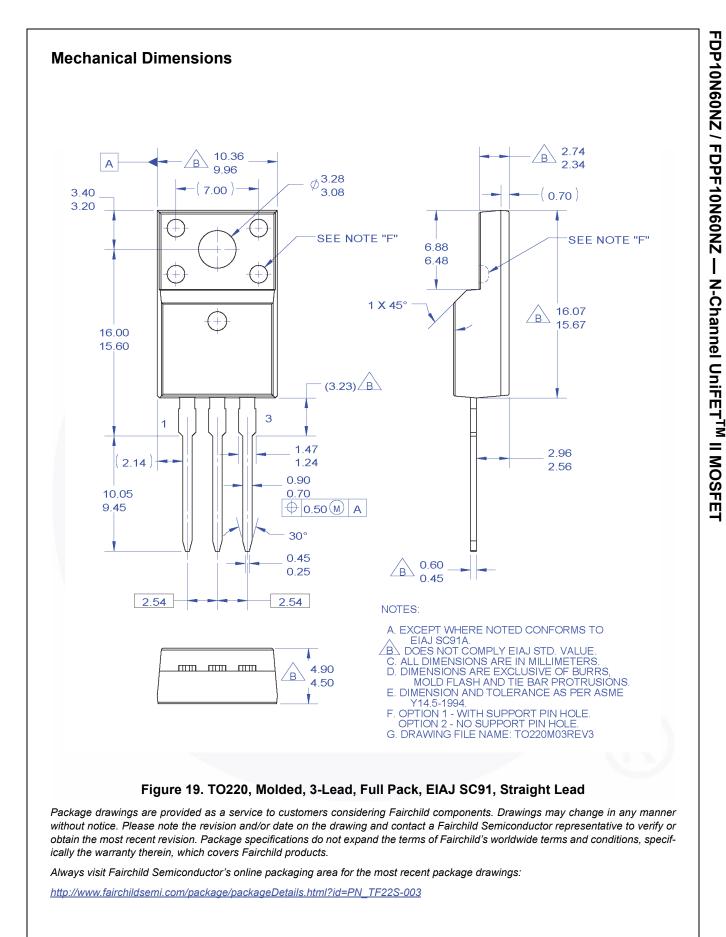
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N-Channel UniFETTM II MOSFET

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