

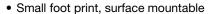
Vishay Semiconductors

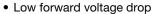
High Performance Schottky Rectifier, 1.0 A



PRODUCT SUMMARY		
Package	SMB	
I _{F(AV)}	1.0 A	
V _R	40 V	
V _F at I _F	0.49 V	
I _{RM} max.	4 mA at 125 °C	
T _J max.	150 °C	
Diode variation	Single die	
E _{AS}	3 mJ	

FEATURES







- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-10BQ040PbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	1.0	A	
V _{RRM}		40	V	
I _{FSM}	t _p = 5 μs sine	430	A	
V _F	1.0 A _{pk} , T _J = 125 °C	0.49	V	
TJ	Range	-55 to +150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-10BQ040PbF	UNITS	
Maximum DC reverse voltage	V_{R}	40	V	
Maximum working peak reverse voltage	V_{RWM}	40	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _L = 112 °C, rectangular waveform		1.0	
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	430	Α
non-repetitive surge current	I _{FSM}	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	45	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 1 \text{A}, L = 6 \text{mH}$ 3.0		mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero Frequency limited by T _J maximu		1.0	Α



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		1 A	T _ 25 °C	0.53	
Maximum forward voltage drop	V _{FM} ⁽¹⁾	2 A	T _J = 25 °C	0.70	V
See fig. 1		1 A	T _J = 125 °C	0.49	
		2 A		0.64	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.1	mA
See fig. 2		T _J = 125 °C		4	IIIA
Typical junction capacitance	C _T	$V_R = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz), 25 °C		80	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 2.0		nH	
Maximum voltage rate of charge	dV/dt	Rated V _R 10 000 V/µ:		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width $<300~\mu s,$ duty cycle <2~%

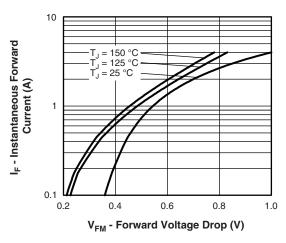
THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-55 to +150	°C
Maximum thermal resistance, junction to lead	R _{thJL} (2)	DC operation	36	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}		80	C/VV
Approximate weight			0.10	g
Approximate weight			0.003	OZ.
Marking device		Case style SMB (similar DO-214AA)	1	F

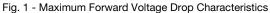
Notes

⁽¹⁾ $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

⁽²⁾ Mounted 1" square PCB







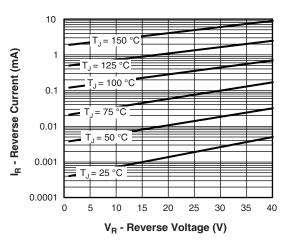


Fig. 2 - Typical Reverse Current vs. Reverse Voltage

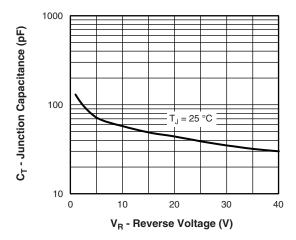


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

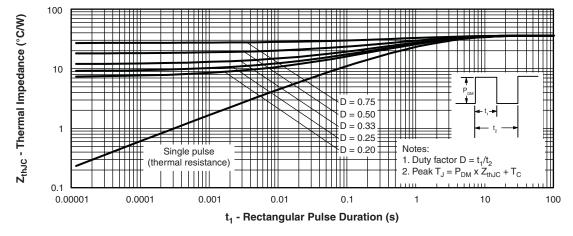


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)



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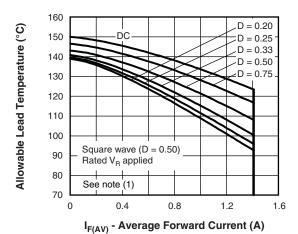
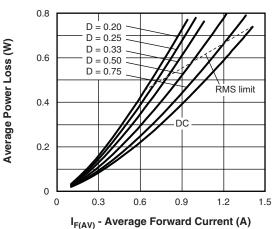


Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature



IF(AV) - Average Forward Current (A)

Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

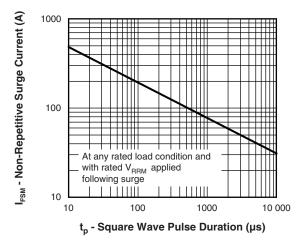


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

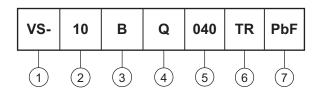
 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times \text{R}_{\text{thJC}}; \\ \text{Pd} & = \text{Forward power loss} = \text{I}_{\text{F(AV)}} \times \text{V}_{\text{FM}} \text{ at } (\text{I}_{\text{F(AV)}}/\text{D}) \text{ (see fig. 6)}; \\ \text{Pd}_{\text{REV}} & = \text{Inverse power loss} = \text{V}_{\text{R1}} \times \text{I}_{\text{R}} \text{ (1 - D); I}_{\text{R}} \text{ at } \text{V}_{\text{R1}} = 80 \text{ \% rated V}_{\text{R}} \\ \end{array}$



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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating

3 - B = single lead diode

4 - Q = Schottky "Q" series

5 - Voltage rating (040 = 40 V)

6 - TR = tape and reel

7 - PbF = lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N	RED P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTIO			
VS-10BQ040TRPbF	5BT	3200	13" diameter plastic tape and reel	

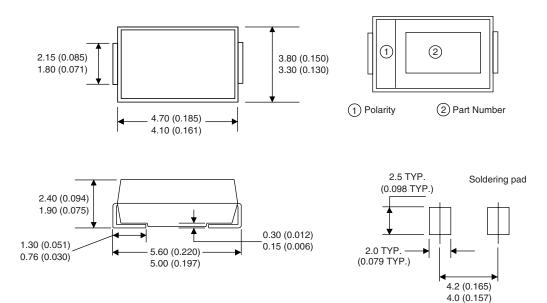
LINKS TO RELATED DOCUMENTS		
Dimensions www.vishay.com/doc?95401		
Part marking information	www.vishay.com/doc?95403	
Packaging information	www.vishay.com/doc?95404	
SPICE model	www.vishay.com/doc?95406	



Vishay High Power Products

SMB

DIMENSIONS in millimeters (inches)



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