

High Performance Schottky Rectifier, 3.0 A



SMC (DO-214AB)

PRIMARY CHARACTERISTICS				
I _{F(AV)}	3.0 A			
V_{R}	40 V			
V _F at I _F	0.46 V			
I _{RM}	30 mA at 125 °C			
T _J max.	150 °C			
E _{AS}	6.0 mJ			
Package	SMC (DO-214AB)			
Circuit configuration	Single			

FEATURES





Guard ring for enhanced ruggedness and long term reliability

ROHS COMPLIANT HALOGEN FREE

• Small foot print, surface mountable

- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-30BQ040HM3 surface-mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS VALUES UNITS			
I _{F(AV)}	Rectangular waveform	3.0	A	
V _{RRM}		40	V	
I _{FSM}	t _p = 5 μs sine	1600	A	
V _F	3.0 A _{pk} , T _J = 125 °C	0.46	V	
TJ	Range	-55 to +150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-30BQ040HM3	UNITS	
Maximum DC reverse voltage	V_{R}	40	W	
Maximum working peak reverse voltage	V _{RWM}	40	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current		50 % duty cycle at T _L = 115 °C, i	rectangular waveform	3.0	
waximum average forward current	I _{F(AV)}	50 % duty cycle at T _L = 104 °C, i	rectangular waveform	4.0	
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	1600	Α
non-repetitive surge current	IFSM	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	90	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.0 A, L = 12 mH		6.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	Α



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	eximum forward voltage drop $V_{FM}^{(1)} = \frac{6 \text{ A}}{3 \text{ A}}$	3 A	T _J = 25 °C	0.57	V
Maximum forward voltage drop		6 A		0.76	
Maximum forward voltage drop		3 A	T _J = 125 °C	0.46	
		6 A		0.64	
Maximum reverse leakage current	1	T _J = 25 °C	V _B = Rated V _B	0.5	mA
Maximum reverse leakage current	I _{RM}	T _J = 125 °C	VR = nateu VR	30	IIIA
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		230	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		3.0	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

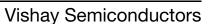
 $^{^{(1)}}$ Pulse width = 300 μ s, duty cycle = 2 %

THERMAL - MECHANICA PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
PARAMETER	STWIDUL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-55 to +150	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC encyction	12	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	46	
Annuarimeta weight			0.24	g
Approximate weight			0.008	OZ.
Marking device		Case style SMC (DO-214AB)	31	F

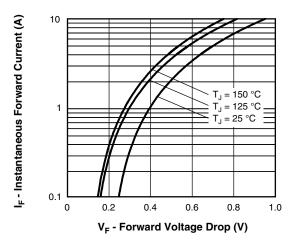
Notes

(1)
$$\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$$
 thermal runaway condition for a diode on its own heatsink

⁽²⁾ Mounted 1" square PCB







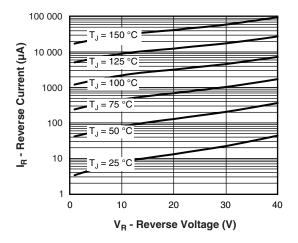


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

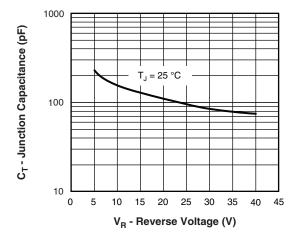


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

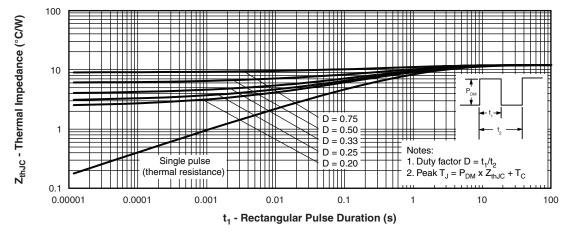


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



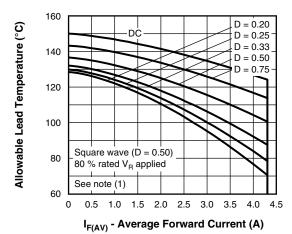


Fig. 5 - Maximum Average Forward Current vs.
Allowable Lead Temperature

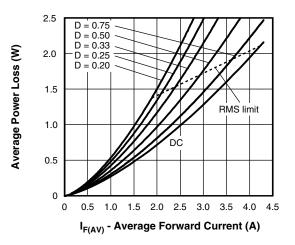


Fig. 6 - Maximum Average Forward Dissipation vs.

Average Forward Current

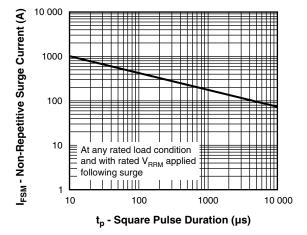


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

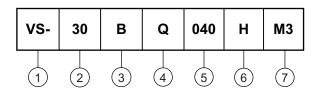
Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating

3 - B = SMC

4 - Q = Schottky "Q" series

5 - Voltage rating (040 = 40 V)

6 - H = AEC-Q101 qualified

7 - Environmental digit:

M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N PREFERRED PACKAGE CODE		MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-30BQ040HM3/9AT	9AT	3500	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95402</u>				
Part marking information	www.vishay.com/doc?95403			
Packaging information <u>www.vishay.com/doc?95404</u>				
SPICE model	www.vishay.com/doc?96601			



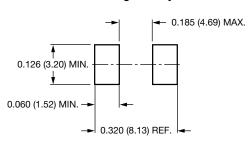
SMC

DIMENSIONS in inches (millimeters)

DO-214AB (SMC)



Mounting Pad Layout



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Vishay

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