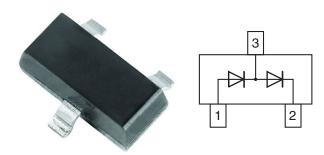
## **MMBD7000**

www.vishay.com

**Vishay Semiconductors** 

# Small Signal Switching Diode, Dual



### DESIGN SUPPORT TOOLS click logo to get started



### **MECHANICAL DATA**

Case: SOT-23

Weight: approx. 8.8 mg

#### Packaging codes / options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

- Silicon epitaxial planar diode
- Fast switching dual diode, especially suited for automatic insertion
- AEC-Q101 qualified available
- Base P/N-E3 RoHS-compliant, commercial grade RoHS
- Base P/N-HE3 RoHS-compliant, AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

PARTS TABLE					
PART	ORDERING CODE	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS	
MMBD7000	MMBD7000-E3-08 or MMBD7000-E3-18	Dual serial	M5C	Tape and reel	
	MMBD7000-HE3-08 or MMBD7000-HE3-18	Duai Selilai	IVISC		

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V <sub>R</sub>	100	V	
Forward current (continuous)		I <sub>F</sub>	200	mA	
Non-repetitive peak forward current	t = 1 s	I <sub>FSM</sub>	500	mA	
Dower dissinction on ED 5 heard		P <sub>tot</sub>	225	mW	
Power dissipation on FR-5 board	Derate above 25 °C	P <sub>tot</sub>	1.8	mW/K	
Tatal davias dissinction on alumina substrate		P <sub>tot</sub>	300	mW	
Total device dissipation on alumina substrate	Derate above 25 °C	P <sub>tot</sub>	2.4	mW/K	

<b>THERMAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Tunical thermal registence, junction to embient air		R <sub>thJA</sub> <sup>(1)</sup>	417	K/W	
Typical thermal resistance, junction to ambient air		R <sub>thJA</sub> <sup>(2)</sup>	556	K/W	
Maximum junction temperature		Тj	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C	
Operating temperature range		T <sub>op</sub>	-55 to +150	°C	

Notes

<sup>(1)</sup> Device on alumina substrate

(2) On FR-5 board

Rev. 1.7, 13-Feb-18 Document Number: 85736 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u> www.vishay.com

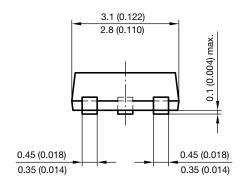
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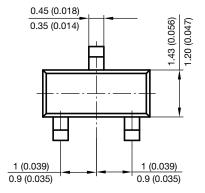
# **MMBD7000**

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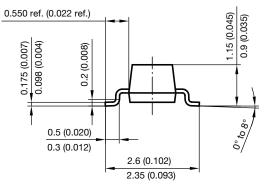
ELECTRICAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reverse breakdown voltage	I <sub>R</sub> = 100 μA	V <sub>(BR)</sub>	100			V
	V <sub>R</sub> = 50 V	I <sub>R</sub>			1000	nA
Leakage current	V <sub>R</sub> = 100 V	I <sub>R</sub>			3	μA
	$V_R = 50 \text{ V}, \text{ T}_j = 125 ^\circ\text{C}$	I <sub>R</sub>			100	μA
	I <sub>F</sub> = 1 mA	VF	0.55		0.70	V
Forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>	0.67		0.82	V
	l <sub>F</sub> = 100 mA	V <sub>F</sub>	0.75		1.10	V
Diode capacitance	$V_{R} = 0$ , f = 1 MHz	CD			1.5	pF
Reverse recovery time	$I_{\rm F} = I_{\rm R} = 10 \text{ mA}, i_{\rm R} = 1 \text{ mA}, \\ R_{\rm L} = 100 \ \Omega$	t <sub>rr</sub>			4	ns

#### PACKAGE DIMENSIONS in millimeters (inches): SOT-23

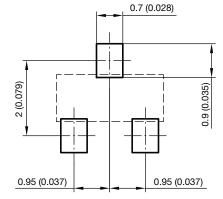




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Foot print recommendation:





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