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## **ADD-A-PAK Generation VII Power Modules Standard Diodes, 100 A**



PRODUCT SUMMARY				
I <sub>F(AV)</sub>	100 A			
Туре	Modules - Diode, High Voltage			

#### **MECHANICAL DESCRIPTION**

The ADD-A-PAK generation VII, new generation of ADD-A-PAK module, combines the excellent thermal performances obtained by the usage of exposed direct bonded copper substrate, with advanced compact simple package solution and simplified internal structure with minimized number of interfaces.

#### **FEATURES**

- High voltage
- · Industrial standard package
- UL approved file E78996



- · Low thermal resistance
- · Designed and qualified for industrial level
- · Material categorization: For definitions of compliance please see www.vishav.com/doc?99912

#### **BENEFITS**

- · Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- Up to 1600 V
- · High surge capability
- · Easy mounting on heatsink

#### **ELECTRICAL DESCRIPTION**

These modules are intended for general purpose high voltage applications such as high voltage regulated power supplies, lighting circuits, temperature and motor speed control circuits, UPS and battery charger.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	112 °C	100				
I <sub>F(RMS)</sub>		157	Α			
I <sub>FSM</sub>	50 Hz	2020	A			
	60 Hz	2115				
I <sup>2</sup> t	50 Hz	20.41	kA <sup>2</sup> s			
I <del>-</del> 1	60 Hz	18.63	KA-S			
I <sup>2</sup> √t		204.1	kA²√s			
V <sub>RRM</sub>	Range	400 to 1600	V			
T <sub>J</sub>		- 40 to 150	°C			
T <sub>Stg</sub>		- 40 (0 150	C			

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#### **ELECTRICAL SPECIFICATIONS**

VOLTAGE RATINGS							
TYPE NUMBER	VOLTAGE CODE	V <sub>RRM</sub> , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I <sub>RRM</sub> MAXIMUM AT T <sub>J</sub> = 150 °C mA			
	04	400	500				
	06	600	700				
	08	800	900				
VSK.91	10	1000	1100	10			
	12	1200	1300				
	14	1400	1500				
	16	1600	1700				

FORWARD CONDUCTION						
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS
Maximum average forward current	l=	180° condu	ction, half sine	wave	100	Α
at case temperature	I <sub>F(AV)</sub>	100 Condu	Ction, nan sine	wave	112	°C
Maximum RMS forward current	I <sub>F(RMS)</sub>	DC at 90 °C	case temperat	ure	157	
		t = 10 ms	No voltage		2020	
Maximum peak, one-cycle forward,		t = 8.3  ms	reapplied		2115	Α
non-repetitive surge current	I <sub>FSM</sub>	t = 10 ms	100 % V <sub>RRM</sub>		1700	
		t = 8.3  ms	reapplied	Sinusoidal half wave,	1780	
	l <sup>2</sup> t	t = 10 ms	No voltage	intitial $T_J = T_J$ maximum	20.41	kA <sup>2</sup> s
Maximum I <sup>2</sup> t for fusing		t = 8.3  ms	reapplied		18.63	
iviaximum i-t for fusing		t = 10 ms	100 % V <sub>RRM</sub>		14.44	
		t = 8.3  ms	reapplied		13.18	
Maximum I <sup>2</sup> √t for fusing	I <sup>2</sup> √t	t = 0.1 ms t	o 10 ms, no vol	tage reapplied	204.1	kA <sup>2</sup> √s
Low level value of threshold voltage	V <sub>F(TO)1</sub>	(16.7 % x π	$x I_{F(AV)} < I < \pi x$	$I_{F(AV)}$ , $T_J = T_J$ maximum	0.76	V
High level value of threshold voltage	V <sub>F(TO)2</sub>	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$		0.89	ľ	
Low level value of forward slope resistance	r <sub>f1</sub>	(16.7 % x $\pi$ x $I_{F(AV)}$ < I < $\pi$ x $I_{F(AV)}$ ), $T_J = T_J$ maximum		2.4	mΩ	
High level value of forward slope resistance	r <sub>f2</sub>	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$		2.05	11152	
Maximum forward voltage drop	$V_{FM}$	$I_{FM} = \pi \times I_{F(AV)}$ , $T_J = 25$ °C, $t_p = 400 \mu s$ square wave		1.55	V	

BLOCKING						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum peak reverse leakage current	I <sub>RRM</sub>	T <sub>J</sub> = 150 °C	10	mA		
Maximum RMS insulation voltage	V <sub>INS</sub>	50 Hz	3000 (1 min) 3600 (1 s)	V		



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THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Junction and storage temperature ra	ange T <sub>J</sub> , T <sub>Stg</sub>		- 40 to 150	°C		
Maximum internal thermal resistance junction to case per leg	e, R <sub>thJC</sub>	DC operation	0.22	°C/W		
Typical thermal resistance, case to heatsink per module	R <sub>thCS</sub>	R <sub>thCS</sub> Mounting surface flat, smooth and greased		C/VV		
to hear	tsink	A mounting compound is recommended and the	4	Nice		
Mounting torque ± 10 % bu	sbar	torque should be rechecked after a period of 3 hours to allow for the spread of the compound.	3	Nm		
Approximate weight			75	g		
Approximate weight			2.7	oz.		
Case style		JEDEC	ADD-A-PAK Gei	n. VII (TO-240AA)		

△R CONDUCTION PER JUNCTION											
DEVICES	5	SINE HALF WAVE CONDUCTION					RECTANGULAR WAVE CONDUCTION				UNITS
DEVICES	180°	120°	90°	60°	30°	180°	120°	90°	60°	30°	UNITS
VSK.91	0.057	0.068	0.087	0.12	0.177	0.045	0.073	0.093	0.123	0.178	°C/W

#### Note

Table shows the increment of thermal resistance R<sub>thJC</sub> when devices operate at different conduction angles than DC

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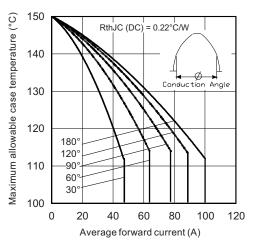


Fig. 1 - Current Ratings Characteristics

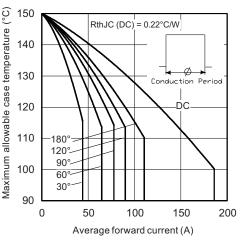


Fig. 2 - Current Ratings Characteristics

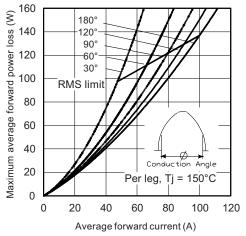


Fig. 3 - Forward Power Loss Characteristics

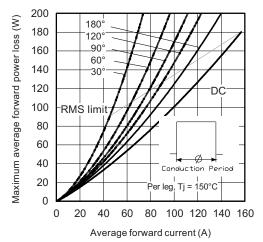
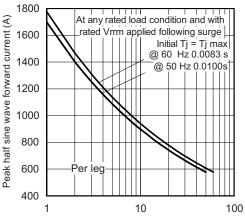


Fig. 4 - On-State Power Loss Characteristics



Number of equal amplitude half cycle current pulses (N)

Fig. 5 - Maximum Non-Repetitive Surge Current

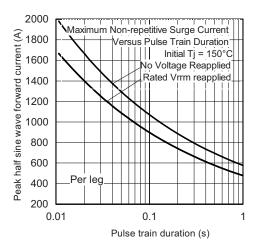


Fig. 6 - Maximum Non-Repetitive Surge Current

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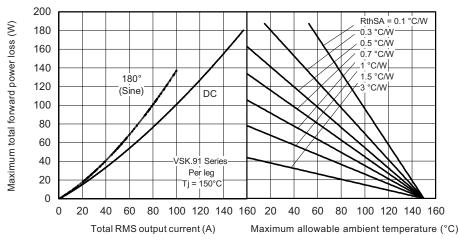


Fig. 7 - Forward Power Loss Characteristics

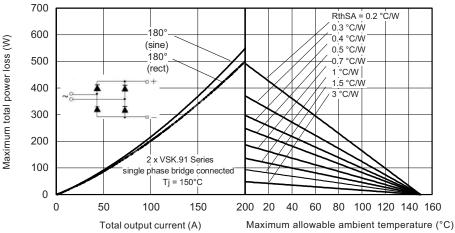


Fig. 8 - Forward Power Loss Characteristics

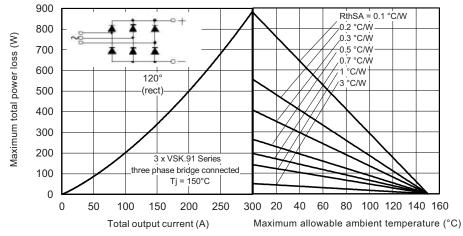


Fig. 9 - Forward Power Loss Characteristics

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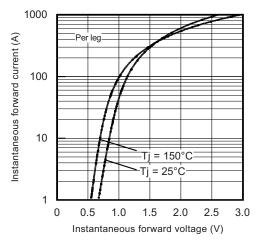


Fig. 10 - Forward Voltage Characteristics

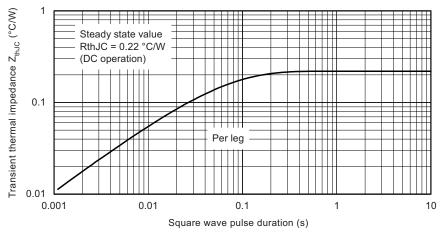
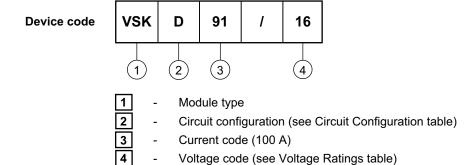


Fig. 11 - Thermal Impedance Z<sub>thJC</sub> Characteristics

#### **ORDERING INFORMATION TABLE**



#### Note

• To order the optional hardware go to <a href="www.vishay.com/doc?95172">www.vishay.com/doc?95172</a>



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CIRCUIT CONFIGURATION		
CIRCUIT DESCRIPTION	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING
Two diodes doubler circuit	D	VSKD  (1) 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
Two diodes common cathodes	С	VSKC  (1) 0
Two diodes common anodes	J	VSKJ  (1) (2) - (3)
Single diode	E	VSKE (2)    (2)   (3)   (3)

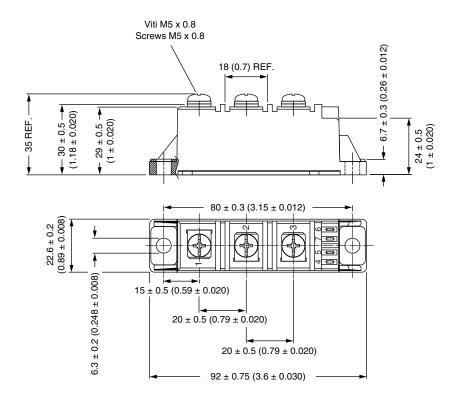
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95369		



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### **ADD-A-PAK Generation VII - Diode**

#### **DIMENSIONS** in millimeters (inches)



Document Number: 95369 Revision: 11-Nov-08

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Revision: 02-Oct-12 Document Number: 91000