

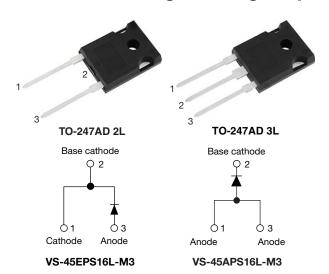
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Vishay Semiconductors

COMPLIANT HALOGEN

**FREE** 

# High Voltage Input Rectifier Diode, 45 A



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	45 A				
$V_{R}$	1600 V				
V <sub>F</sub> at I <sub>F</sub>	1.16 V				
I <sub>FSM</sub>	500 A				
T <sub>J</sub> max.	150 °C				
Package	TO-247AD 2L, TO-247AD 3L				
Circuit configuration	Single				

#### **FEATURES**

- · Very low forward voltage drop
- · Glass passivated pellet chip junction
- Designed and qualified according to JEDEC®-JESD 47
- AEC-Q101 qualified P/N available (VS-45EPS16LHM3, VS-45APS16LHM3)
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

## APPLICATIONS

- Input rectification for single and three phase bridge configurations
- Off-board EV/HEV battery chargers (AEC-Q101 qualified part for on-board chargers also available)
- Renewable energy inverters
- Input rectification for single and three phase bridge configurations
- Vishay Semiconductors switches and output rectifiers which are available in identical package outlines

#### **DESCRIPTION**

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge)

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Sinusoidal waveform	45	Α			
V <sub>RRM</sub>		1600	V			
I <sub>FSM</sub>		500	Α			
V <sub>F</sub>	45 A, T <sub>J</sub> = 25 °C	1.16	V			
$T_J$		-40 to +150	°C			

VOLTAGE RATINGS						
PART NUMBER	V <sub>RRM</sub> , MAXIMUM PEAK REVERSE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I <sub>RRM</sub> AT 150 °C mA			
VS-45EPS16L-M3	1600	1700	1			
VS-45APS16L-M3	1600	1700	'			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum average forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 109 °C, 180° conduction half sine wave	45				
Maximum peak one cycle		10 ms sine pulse, rated V <sub>RRM</sub> applied	420	Α			
non-repetitive surge current	I <sub>FSM</sub>	10 ms sine pulse, no voltage reapplied	500				
Maximum I <sup>2</sup> t for fusing	l <sup>2</sup> t	10 ms sine pulse, rated V <sub>RRM</sub> applied	884	- A <sup>2</sup> s			
Maximum I-t for fusing	1-1	10 ms sine pulse, no voltage reapplied	1250	A-S			
Maximum I <sup>2</sup> √t for fusing	I <sup>2</sup> √t	t = 0.1 ms to 10 ms, no voltage reapplied	12 500	A <sup>2</sup> √s			

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# VS-45EPS16L-M3, VS-45APS16L-M3

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CON	IDITIONS	VALUES	UNITS	
Maximum forward voltage drop	$V_{FM}$	45 A, T <sub>J</sub> = 25 °C		1.16	V	
Forward slope resistance	rt	T <sub>.1</sub> = 150 °C		7.6	mΩ	
Threshold voltage	V <sub>F(TO)</sub>	1J = 150 C		0.72	V	
Maximum reverse leakage augrent		T <sub>J</sub> = 25 °C	V - Poted V	0.1	mA	
Maximum reverse leakage current	IRM	T <sub>J</sub> = 150 °C	$V_R$ = Rated $V_{RRM}$	1.0	IIIA	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage tempera	ture range	T <sub>J</sub> , T <sub>Stg</sub>		-40 to +150	°C		
Maximum thermal resistance, junction to	case	R <sub>thJC</sub>	DC operation	0.40			
Maximum thermal resistance, junction to ambient		$R_{thJA}$		40	°C/W		
Typical thermal resistance, case to heat	sink	R <sub>thCS</sub>	Mounting surface, smooth, and greased	0.25			
Approximate weight				6	g		
Approximate weight				0.21	oz.		
Mounting torque	minimum			6 (5)	kgf · cm		
Mounting torque -	maximum			12 (10)	(lbf · in)		
Maddan dada			Case style TO-247AD 2L	45EP	S16L		
Marking device			Case style TO-247AD 3L	45AF	S16L		

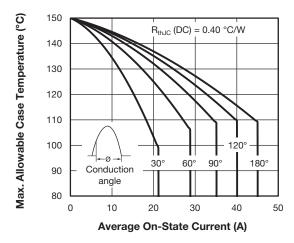


Fig. 1 - Current Rating Characteristics

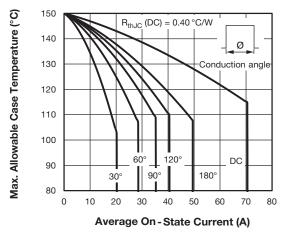


Fig. 2 - Current Rating Characteristics



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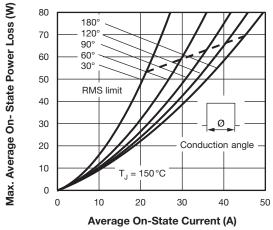


Fig. 3 - Forward Power Loss Characteristics

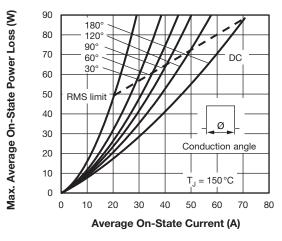


Fig. 4 - Forward Power Loss Characteristics

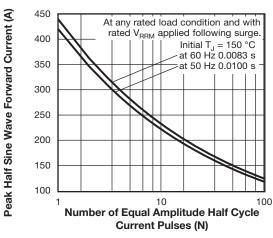


Fig. 5 - Maximum Non-Repetitive Surge Current

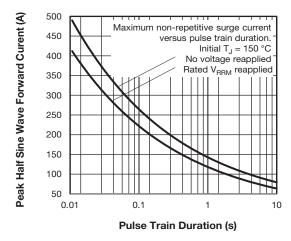


Fig. 6 - Maximum Non-Repetitive Surge Current

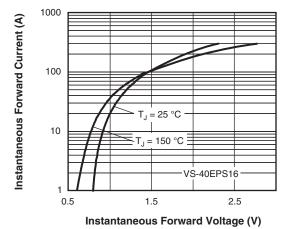


Fig. 7 - Forward Voltage Drop Characteristics

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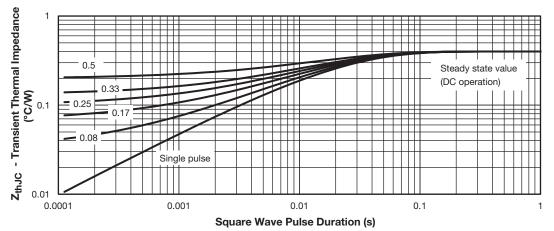
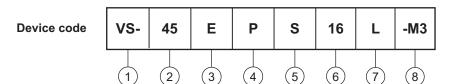


Fig. 8 - Thermal Impedance Z<sub>thJC</sub> Characteristics

#### **ORDERING INFORMATION TABLE**



- Vishay Semiconductors product
- 2 Current rating (45 = 45 A)
- Circuit configuration:

E = single diode, 2 pins

A = single diode, 3 pins

- Package:

P = TO-247

5 - Type of silicon:

S = standard recovery rectifier

7 - L = long leads

8 - Environmental digit:

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-45EPS16L-M3	25	500	Antistatic plastic tubes			
VS-45APS16L-M3	25	500	Antistatic plastic tubes			

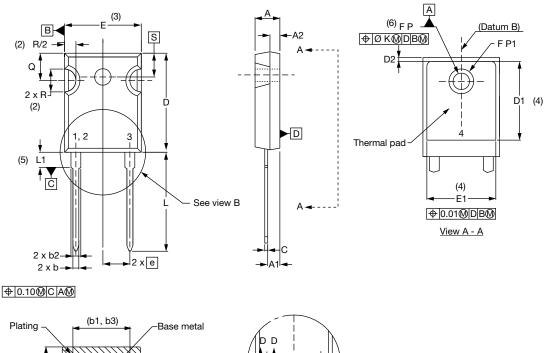
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95536			
Dimensions -	TO-247AD 3L	www.vishay.com/doc?95626		
Part marking information	TO-247AD 2L	www.vishay.com/doc?95648		
Part marking information -	TO-247AD 3L	www.vishay.com/doc?95007		

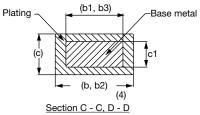


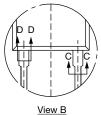
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### **TO-247AD 2L**

#### **DIMENSIONS** in millimeters and inches







SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4
D2	0.51	1.35	0.020	0.053	

SYMBOL	MILLIMETERS		INC	HES	NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Е	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	BSC	
ØK	0.2	254	0.0	10	
L	19.81	20.32	0.780	0.800	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217	BSC	

#### **Notes**

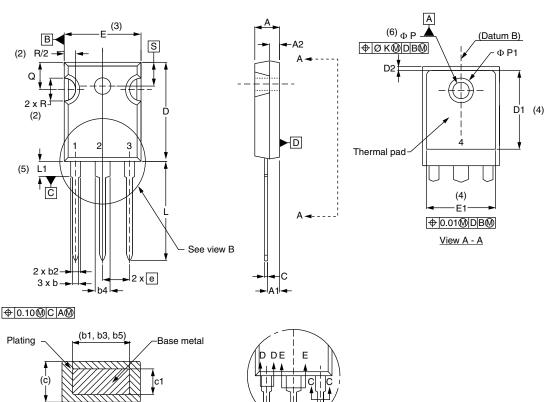
- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4



### Vishay Semiconductors

### **TO-247AD 3L**

#### **DIMENSIONS** in millimeters and inches



View B

Section C - C, D - D, E - E						
CVMPOL	MILLIM	IETERS	INC	HES	NOTES	
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.65	5.31	0.183	0.209		
A1	2.21	2.59	0.087	0.102		
A2	1.50	2.49	0.059	0.098		
b	0.99	1.40	0.039	0.055		
b1	0.99	1.35	0.039	0.053		
b2	1.65	2.39	0.065	0.094		
b3	1.65	2.34	0.065	0.092		
b4	2.59	3.43	0.102	0.135		
b5	2.59	3.38	0.102	0.133		
O	0.38	0.89	0.015	0.035		

0.015

0.776

0.515

0.033

0.815

SYMBOL	OI   MILLIMETERS   INCH		HES	NOTES	
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
Е	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	BSC	
ØK	0.2	254	0.0	)10	
L	19.81	20.32	0.780	0.800	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51	BSC	0.217	BSC	

#### Notes

с1

D

D1

(1) Dimensioning and tolerancing per ASME Y14.5M-1994

0.84

20.70

- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body

3

4

- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1

0.38

19.71

13.08

- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4

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