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Vishay General Semiconductor

# Surface-Mount TMBS® (Trench MOS Barrier Schottky) Rectifier





PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	3 A			
V <sub>RRM</sub>	100 V			
I <sub>FSM</sub>	60 A			
V <sub>F</sub> at I <sub>F</sub> = 3 A (125 °C)	0.58 V			
T <sub>J</sub> max.	175 °C			
Package	SlimSMAW (DO-221AD)			
Circuit configuration	Single			

### FEATURES

- Low-profile package
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C



RoHS

COMPLIANT HALOGEN

FREE

- AEC-Q101 qualified available
  Automotive ordering code: base P/NHM3
- Compatible to SOD-128 package case outline
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

## **TYPICAL APPLICATIONS**

For use in high frequency inverters, freewheeling, DC/DC converters, and polarity protection in commercial, industrial, and automotive applications.

### **MECHANICAL DATA**

Case: SlimSMAW (DO-221AD)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

H3 and HM3 suffix meets JESD 201 class 2 whisker test

Polarity: color band denotes cathode end

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	VSS8D3M10	UNIT		
Device marking code		3M10			
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	100	V		
Maximum average forward restified averant (is 1)		3	^		
Maximum average forward rectified current (fig.1)	I <sub>F</sub> <sup>(2)</sup>	2.1	A		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	60	А		
Operating junction temperature range		-40 to +175	°C		
Storage temperature range	T <sub>STG</sub>	-55 to +175	C		

#### Notes

(1) Mounted on 30 mm x 30 mm AL PCB pad area

<sup>(2)</sup> Free air, mounted on recommended copper pad area

 $^{(3)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient: dP<sub>D</sub>/dT<sub>J</sub> < 1/R<sub>0JA</sub>

VSS8D3M10



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 1.5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.55	-	V
	I <sub>F</sub> = 3 A			0.67	0.75	
	I <sub>F</sub> = 1.5 A	- T <sub>A</sub> = 125 °C		0.48	-	
	I <sub>F</sub> = 3 A			0.58	0.66	
Reverse current	V <sub>R</sub> = 70 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> (2)	0.01	-	
	v <sub>R</sub> = 70 v	T <sub>A</sub> = 125 °C		0.7	-	- mA
	V <sub>R</sub> = 100 V	T <sub>A</sub> = 25 °C		-	0.2	
	v <sub>R</sub> = 100 v	T <sub>A</sub> = 125 °C		1.5	4	
Typical junction capacitance	4.0 V, 1 M⊦	4.0 V, 1 MHz		340	-	pF

Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise specified)					
PARAMETER	SYMBOL	TYP.	MAX.	UNIT	
Typical thermal resistance	R <sub>0JA</sub> <sup>(1)(2)</sup>	120	150	°C/W	
	R <sub>0JM</sub> <sup>(3)</sup>	12	15	0/10	

#### Notes

<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{0JA}$ 

(2) Thermal resistance junction-to-ambient to follow JEDEC® 51-2A, device mounted on FR4 PCB, 2 oz., standard footprint

<sup>(3)</sup> Thermal resistance junction-to-mount to follow JEDEC 51-14 transient dual interface test method (TDIM)

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
VSS8D3M10-M3/H	0.033	Н	3500	7" diameter plastic tape and reel		
VSS8D3M10-M3/I	0.033	I	14 000	13" diameter plastic tape and reel		
VSS8D3M10HM3/H (1)	0.033	Н	3500	7" diameter plastic tape and reel		
VSS8D3M10HM3/I (1)	0.033	I	14 000	13" diameter plastic tape and reel		

#### Note

(1) AEC-Q101 qualified



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## **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

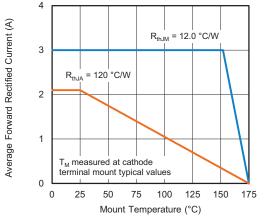


Fig. 1 - Maximum Forward Current Derating Curve

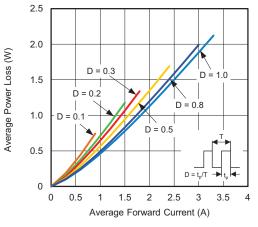


Fig. 2 - Forward Power Loss Characteristics

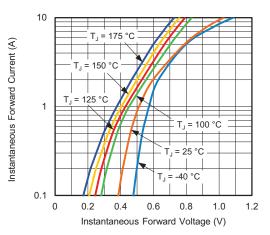


Fig. 3 - Typical Instantaneous Forward Characteristics

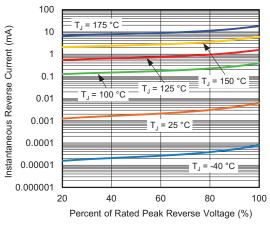


Fig. 4 - Typical Reverse Leakage Characteristics

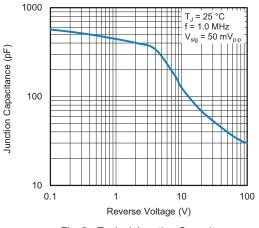


Fig. 5 - Typical Junction Capacitance

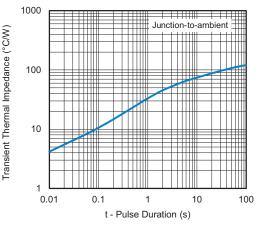


Fig. 6 - Typical Transient Thermal Impedance

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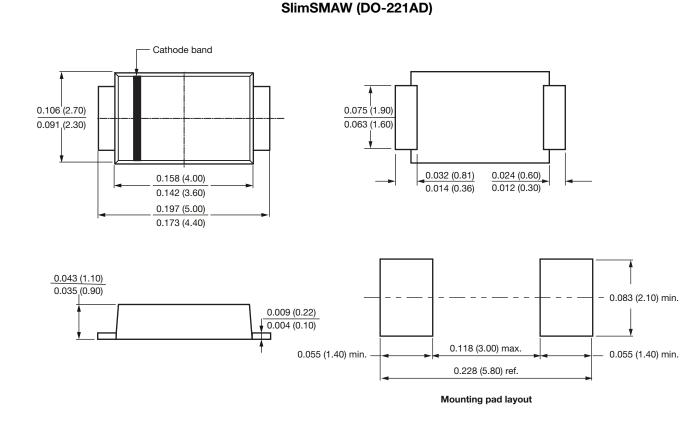
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# VSS8D3M10

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### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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