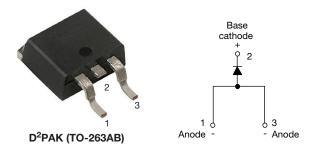
Vishay Semiconductors

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ISHA

Surface Mount Fast Soft Recovery Rectifier Diode, 10 A



PRIMARY CHARACTERISTICS							
I _{F(AV)}	10 A						
V _R	1200 V						
V _F at I _F	1.33 V						
I _{FSM}	155 A						
t _{rr}	80 ns						
T _J max.	150 °C						
Package	D ² PAK (TO-263AB)						
Circuit configuration	Single						
Snap factor	0.6						

FEATURES

- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Glass passivated pellet chip junction
- AEC-Q101 qualified
- Meets JESD 201 class 1A whisker test
- Flexible solution for reliable AC power rectification
- High surge, low V_F rugged blocking diode for DC charging stations
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Input rectification
- On-board and off-board EV / HEV battery chargers

DESCRIPTION

The VS-10ETF12SLHM3 fast soft recovery rectifier series has been optimized for combined short reverse recovery time and low forward voltage drop.

The glass passivation ensures stable reliable operation in the most severe temperature and power cycling conditions.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Sinusoidal waveform	10	A						
V _{RRM}		1200	V						
I _{FSM}		155	A						
V _F	10 A, T _J = 25 °C	1.33	V						
t _{rr}	1 A, 100 A/µs	80	ns						
TJ	Range	-40 to +150	°C						

VOLTAGE RATINGS			
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA
VS-10ETF12SLHM3	1200	1300	4

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Maximum average forward current	I _{F(AV)}	T_C = 125 °C, 180° conduction half sine wave	10						
Maximum peak one cycle non-repetitive	1	10 ms sine pulse, rated V _{RRM} applied	130	А					
surge current	IFSM	10 ms sine pulse, no voltage reapplied	155						
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	85	A ² s					
Maximum -t for fusing	1-1	10 ms sine pulse, no voltage reapplied	120	A-2					
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied	1200	A²√s					

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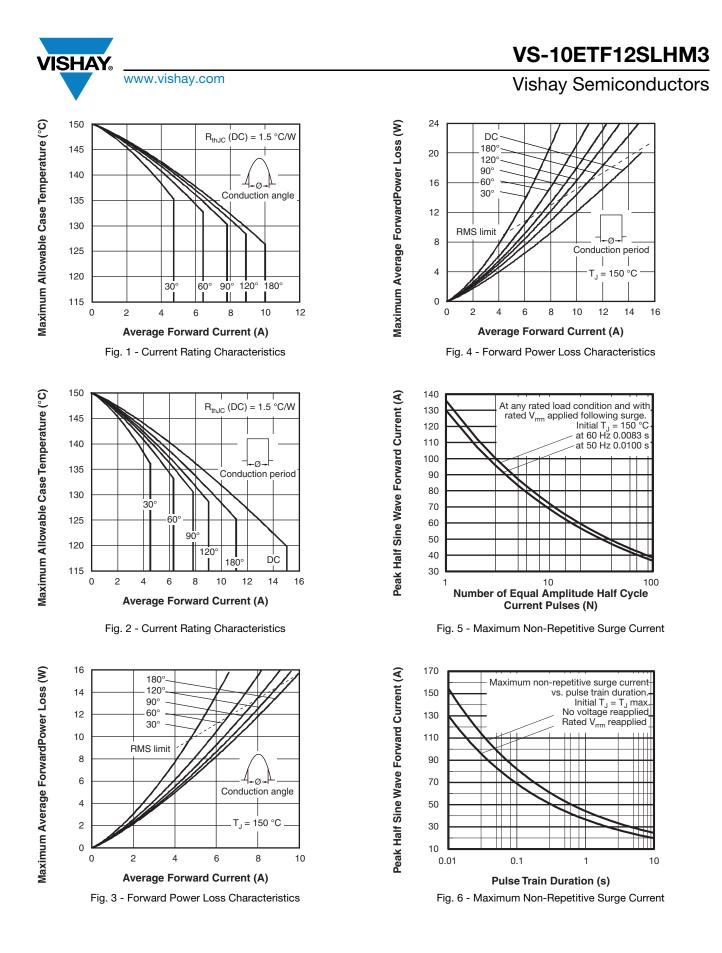
ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS					
Maximum forward voltage drop	V _{FM}	10 A, T _J = 25 °C		1.33	V				
Forward slope resistance	r _t	T.I = 150 °C	22.9	mΩ					
Threshold voltage	V _{F(TO)}	1) = 150 C	0.96	V					
		T _J = 25 °C	V_{B} = rated V_{BBM}	0.1	mA				
Maximum reverse leakage current	IRM	T _J = 150 °C	$v_{\rm R}$ = rated $v_{\rm RRM}$	4	ША				

RECOVERY CHARACTERISTICS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	· •			
Reverse recovery time	t _{rr}	I= at 10.4	310	ns	I _{FM}			
Reverse recovery current	I _{rr}	I _F at 10 A _{pk} 25 A/μs	4.7	А				
Reverse recovery charge	Q _{rr}	25 °C	1.05	μC	dir/ dt/Qrr			
Typical snap factor	S		0.6		I IRM(REC)			

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}		-40 to +150	°C				
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	1.5	°C/W				
Maximum thermal resistance, junction to ambient (PCB mount)	R _{thJA} ⁽¹⁾		62	C/W				
Approximate weight			2	g				
Approximate weight			0.07	oz.				
Marking device		Case style D ² PAK (TO-263AB)	10ETF	12SH				

Note

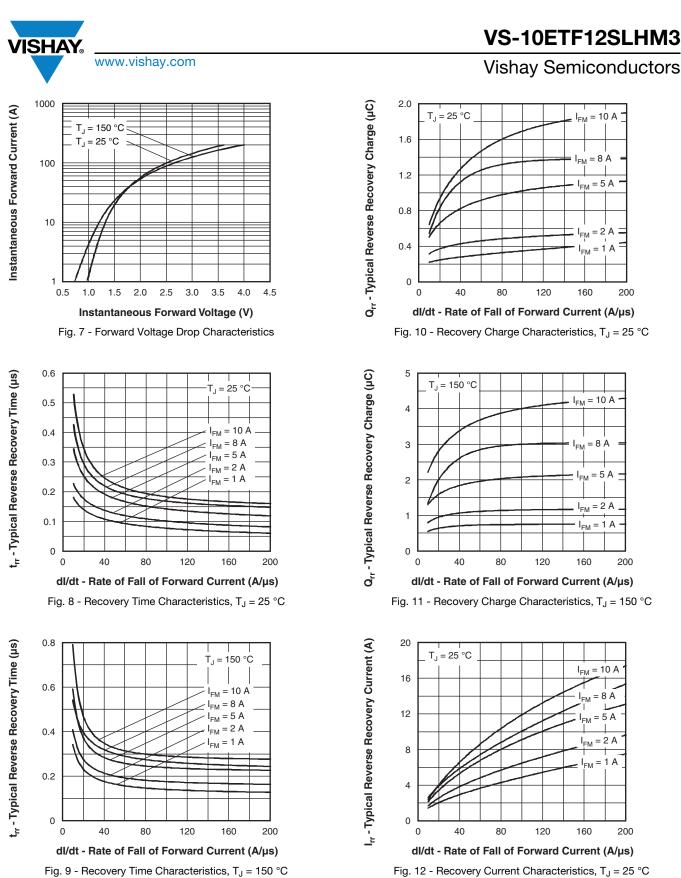
(1) When mounted on 1" square (650 mm²) PCB of FR-4 or G-10 material 4 oz. (140 µm) copper 40 °C/W



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VS-10ETF12SLHM3

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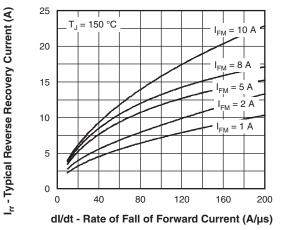
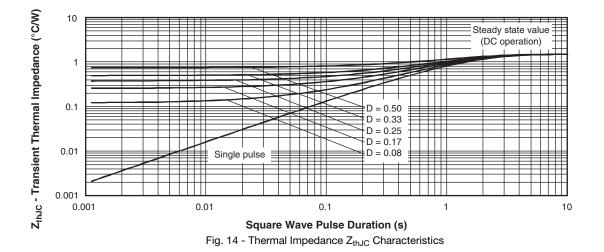


Fig. 13 - Recovery Current Characteristics, T_J = 150 °C



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ORDERING INFORMATION TABLE

Device code	VS-	10	Е	т	F	12	S	L	н	М3	
		2	3	4	5	6	7	8	9	10	
	1	- Vis	hay Sen	niconduo	ctors pro	oduct					
	2 - Current rating (10 = 10 A)										
	3	- Circuit configuration:									
		E =	E = single								
	4	- Pad	kage:								
		T =	D ² PAK	(TO-26	3AB)						
	5	- Тур	e of silio	con:							
		F =	fast sof	t recove	ry rectif	ier					
	6	- Vol	tage coo	de x 100	= V _{RRN}	ı ——	12 = 12	200 V			
	E			mounta							
	8		L = tape and reel (left oriented), for different orientation, contact factory								
	9	- H=	H = AEC-Q101 qualified								
	10			ntal digit							
				•		complia	int, and	termina	ations le	ad (Pb)-	

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-10ETF12SLHM3	800	800	13" diameter reel				

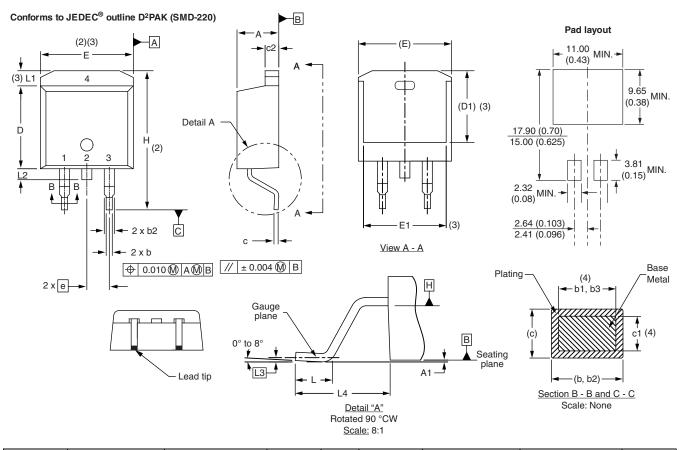
LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95046					
Part marking information	www.vishay.com/doc?95444					
Packaging information	www.vishay.com/doc?96317					

Outline Dimensions



D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INC	HES	NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100) BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

(4) Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 08-Jul-15

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