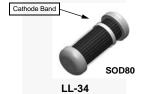
April 2013

FDH / FDLL 300 / A / 333 High Contraction Low Leakage Diode





THE PLACEMENT OF THE EXPANSION GAP HAS NO RELATIONSHIP TO THE LOCATION OF THE CATHODE TERMINAL

LL-34 COLOR BAND MARKING

DEVICE 1ST BAND

FDLL300 WHITE
FDLL300A WHITE
FDLL333 WHITE

-1st band denotes cathode terminal and has wider width

Absolute Maximum Ratings(1)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_{\Delta} = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Units	
W _{IV}	Working Inverse Voltage		125	V
Io	Average Rectified Forward Current	200	mA	
I _F	DC Forward Current	500	mA	
i _f	Recurrent Peak Forward Current		600	mA
I _{FSM}	Non-repetitive Peak Forward Surge Current	Pulse Width = 1.0 s	1.0	Α
	Non-repetitive Feak Forward Surge Current	Pulse Width = 1.0 μs	4.0	Α
T _{STG}	Storage Temperature Range		-65 to +200	°C
TJ	Operating Junction Temperature		175	°C

Note:

1. These ratings are limiting values above which the serviceability of the diode may be impaired.

These ratings are bansed on a maximum junction temperature of 200 $^{\circ}$ C.

These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Max.	Units
Cymbol	i didilicici	FDH / FDLL 400	
В	Power Dissipation	500	mW
P _D	Derate above 25°C	3.33	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	300	°C/W

1

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Electrical Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter		Test Conditions	Min.	Max.	Units
V _R	Breakdown Voltage	е	I _R = 100 μA	150		V
	ForwardVoltage	FDH / FDLL 300 / A	I _F = 1.0 mA		680	mV
		FDH / FDLL 300	I _F = 5.0 mA		750	mV
		FDH / FDLL 300A	I _F = 5.0 mA		760	mV
		FDH / FDLL 300 / A	I _F = 10 mA		800	mV
V_{F}		FDH / FDLL 300	I _F = 50 mA		880	mV
		FDH / FDLL 300A	I _F = 50 mA		890	mV
		FDH / FDLL 300 / A	I _F = 100 mA		920	mV
		FDH / FDLL 300 / A	I _F = 200 mA		1.0	V
		FDH / FDLL 333	I _F = 50 mA	800	890	mV
			I _F = 100 mA	830	940	mV
			I _F = 150 mA	860	970	mV
			I _F = 200 mA	0.87	1.05	V
			I _F = 250 mA	0.88	1.08	V
			I _F = 300 mA	0.9	1.15	V
I _R	Reverse Leakage	FDH / FDLL 300 / A	V _R = 125 V		1.0	nA
			V _R = 125 V, T _A = 150°C		3.0	μA
		FDH / FDLL 333	V _R = 125 V		3.0	nA
			V _R = 125 V, T _A = 100°C		500	nA
Co	Diode Capacitance)	V _R = 0, f = 1.0 MHz		6.0	pF

Physical Dimensions

SOD-80

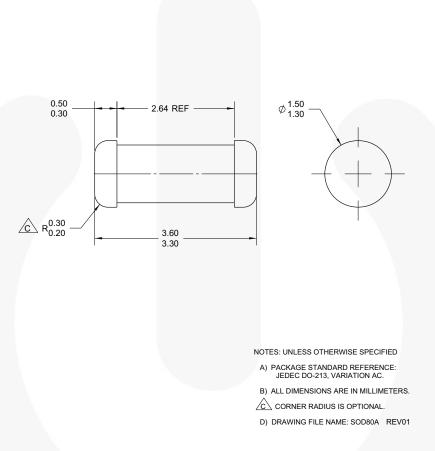


Figure 1. 2-TERMINAL, SOD-80, JEDEC DO-213AC, MINI-MELF

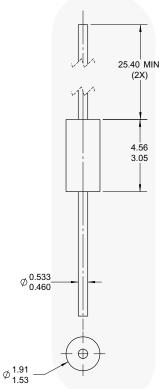
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Physical Dimensions (Continued)





NOTES: UNLESS OTHERWISE SPECIFIED

- PACKAGE STANDARD REFERENCE:
 JEDEC DO-204, VARIATION AH.
 HERMETICALLY SEALED GLASS PACKAGE.
 PACKAGE WEIGHT IS 0.137 GRAM.
 J ALL DIMENSIONS ARE IN MILLIMETERS.
 DRAWING FILE NAME: DO35AREV02

Figure 2. AXIAL LEADED, GLASS, JEDEC DO204, VARIATION AH (ACTIVE)

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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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