

#### Hex Schmitt inverter

Datasheet - production data

#### **Features**

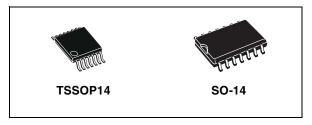
- High speed:  $t_{PD} = 5.5$  ns (typ.) at  $V_{CC} = 5$  V
- Low power dissipation
  - I<sub>CC</sub> = 2  $\mu$ A (max.) at T<sub>A</sub> = 25 °C
- Typical hysteresis: V<sub>h</sub> = 1 V at V<sub>CC</sub> = 4.5 V
- Power-down protection on inputs
  - Symmetrical output impedance
     II<sub>OH</sub>I = I<sub>OL</sub> = 8 mA (min.)
- Balanced propagation delay: t<sub>PLH</sub> ≅ t<sub>PHL</sub>
- Operating voltage range
  - $V_{CC}$  (opr.) = 2 V to 5.5 V
- Pin and function compatible with 74 series 14
- Improved latch-up immunity
- Low noise
  - $V_{OIP} = 0.8 V (max.)$
- ESD performance

HBM: 2000 VMM: 200 V

- CDM: 1000 V

### **Applications**

- Automotive
- Industrial
- Computer
- Consumer



### **Description**

The 74VHC14 device is an advanced high-speed CMOS hex Schmitt inverter manufactured with sub-micron silicon gate and double-layer metal wiring C<sup>2</sup>MOS technology. The internal circuit is composed of 3 stages including a buffer output, which provides high noise immunity and stable output.

Power-down protection is provided on all inputs and 0 to 7 V can be accepted on inputs regardless of the supply voltage. This device can be used to interface from 5 V to 3 V.

Pin configuration and function are the same as those of the 74VHC04 device but the 74VHC14 device has hysteresis.

This along with its Schmitt trigger function allows the device to be used on line receivers with slow rise/fall input signals.

All inputs and outputs are equipped with protection circuits against static discharge, giving them 2 kV ESD immunity and transient excess voltage.

Table 1. Device summary

Order code	Temperature range	Package	Packaging	Marking
74VHC14TTR	-55/+125 °C	TSSOP14	Tape and reel	VHC14
74VHC14YTTR <sup>(1)</sup>	-40/+125 °C	TSSOP14 (automotive grade)	Tape and reel	VHC14Y
74VHC14MTR	-55/+125 °C	SO-14	Tape and reel	74VHC14
74VHC14YMTR <sup>(1)</sup>	-40/+125 °C	SO-14 (automotive grade)	Tape and reel	74VHC14Y

Qualification and characterization according to AEC Q100 and Q003 or equivalent, advanced screening according to AEC Q001 and Q002 or equivalent.

Contents 74VHC14

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## 1 Logic symbols and I/O equivalent circuit

Figure 1. IEC logic symbols

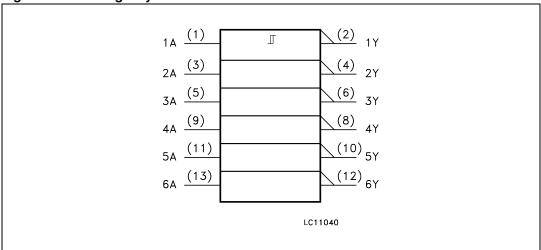
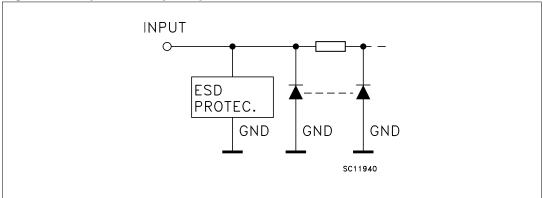


Figure 2. Input and output equivalent circuit



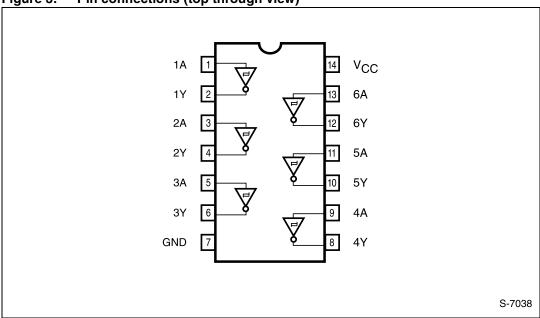
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Pin settings 74VHC14

# 2 Pin settings

#### 2.1 Pin connections

Figure 3. Pin connections (top through view)



### 2.2 Pin description

Table 2. Pin description

Pin number	Symbol	Name and function
1, 3, 5, 9, 11, 13	1A to 6A	Data inputs
2, 4, 6, 8, 10, 12	1Y to 6Y	Data outputs
7	GND	Ground (0 V)
14	V <sub>CC</sub>	Positive supply voltage

### 2.3 Truth table

Table 3. Truth table

Input	Output
Α	Υ
L	Н
Н	L

74VHC14 Maximum ratings

### 3 Maximum ratings

Stressing the device above the rating listed in *Table 4: Absolute maximum ratings* may cause permanent damage to the device. These are stress ratings only and operation of the device at these or any other conditions above those indicated in *Table 5: Recommended operating conditions* of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Table 4. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>CC</sub>	Supply voltage	-0.5 to +7.0	V
VI	DC input voltage	-0.5 to +7.0	V
Vo	DC output voltage	-0.5 to V <sub>CC</sub> + 0.5	V
I <sub>IK</sub>	DC input diode current	- 20	mA
I <sub>OK</sub>	DC output diode current	± 20	mA
Io	DC output current	±25	mA
I <sub>CC</sub>	DC supply current per supply pin	±50	mA
I <sub>GND</sub>	DC ground current per supply pin	±50	mA
T <sub>stg</sub>	Storage temperature	-65 to +150	°C
T <sub>L</sub>	Lead temperature (10 sec.)	300	°C

### **Recommended operating conditions**

Table 5. Recommended operating conditions

Symbol	F	Value	Unit	
V <sub>CC</sub>	Supply voltage	2 to 5.5	V	
V <sub>I</sub>	Input voltage	0 to 5.5	V	
V <sub>O</sub>	Output voltage		0 to V <sub>CC</sub>	V
т	Operating temperature	TSSOP14, SO-14	-55 to +125	°C
T <sub>op</sub>	Operating temperature	-40 to +125	°C	

Electrical characteristics 74VHC14

### 4 Electrical characteristics

Table 6. DC specifications

		T	est condition	Value							
Symbol	Parameter	v <sub>cc</sub>		T	<sub>A</sub> = 25°	С	-40 to	85 °C	-55 to	125 °C	Unit
		(V)		Min.	Тур.	Max.	Min.	Max.	Min.	Max.	,
		3.0				2.2		2.2		2.2	
$V_{t+}$	High level threshold voltage	4.5				3.15		3.15		3.15	V
	5.5				3.85		3.85		3.85		
		3.0		0.9			0.9		0.9		
$V_{t-}$	Low level threshold voltage	4.5		1.35			1.35		1.35		V
in control voltage	5.5		1.65			1.65		1.65			
		3.0		0.3		1.2	0.3	1.2	0.3	1.2	
$V_h$	V <sub>h</sub> Hysteresis voltage	4.5		0.4		1.4	0.4	1.4	0.4	1.4	V
	5.5		0.5		1.6	0.5	1.6	0.5	1.6		
		2.0	I <sub>O</sub> = -50 μA	1.9	2.0		1.9		1.9		
		3.0	I <sub>O</sub> = -50 μA	2.9	3.0		2.9		2.9		
$V_{OH}$	High level output voltage	4.5	I <sub>O</sub> = -50 μA	4.4	4.5		4.4		4.4		V
	J	3.0	$I_O = -4 \text{ mA}$	2.58			2.48		2.4		
		4.5	I <sub>O</sub> = -8 mA	3.94			3.8		3.7		
		2.0	I <sub>O</sub> = 50 μA		0.0	0.1		0.1		0.1	
		3.0	I <sub>O</sub> = 50 μA		0.0	0.1		0.1		0.1	
$V_{OL}$	Low level output voltage	4.5	I <sub>O</sub> = 50 μA		0.0	0.1		0.1		0.1	٧
	J	3.0	I <sub>O</sub> = 4 mA			0.36		0.44		0.55	
		4.5	I <sub>O</sub> = 8 mA			0.36		0.44		0.55	
I <sub>I</sub>	Input leakage current	0 to 5.5	V <sub>I</sub> = 5.5 V or GND			±0.1		±1		±1	μА
I <sub>CC</sub>	Quiescent supply current	5.5	$V_I = V_{CC}$ or GND			2		20		20	μΑ

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**Test condition** Value **Symbol**  $T_A = 25 \, ^{\circ}C$ -40 to 85 °C -55 to 125 °C Unit **Parameter** CL V<sub>CC</sub> (V) (pF) Тур. Max. Min. Max. Min. Max.  $3.3^{(1)}$ 15 8.3 12.8 1.0 15.0 1.0 15.0  $3.3^{(1)}$ 50 16.3 10.8 1.0 18.5 1.0 18.5 Propagation delay  $t_{PLH}$ ns 5.0<sup>(2)</sup> time  $t_{PHL}$ 15 5.5 8.6 1.0 10.0 1.0 10.0  $5.0^{(2)}$ 50 7.0 10.6 1.0 12.0 1.0 12.0

Table 7. AC electrical characteristics (input  $t_r = t_f = 3 \text{ ns}$ )

- 1. Voltage range is  $3.3 \text{ V} \pm 0.3 \text{ V}$ .
- 2. Voltage range is  $5.0 \text{ V} \pm 0.5 \text{ V}$ .

Table 8. Capacitive characteristics

		Test condition		Value						
Symbol	Parameter		T,	<sub>A</sub> = 25 °	C.	-40 to	85 °C	-55 to	125°C	Unit
			Min.	Тур.	Max.	Min.	Max.	Min.	Max.	
C <sub>IN</sub>	Input capacitance			6	10		10		10	pF
C <sub>PD</sub>	Power dissipation capacitance <sup>(1)</sup>			14						pF

C<sub>PD</sub> is defined as the value of the IC's internal equivalent capacitance which is calculated from the operating current consumption without load. (Refer to Section 5: Test circuit). Average operating current can be obtained by the following equation. I<sub>CC</sub>(opr) = C<sub>PD</sub> x V<sub>CC</sub> x f<sub>IN</sub> + I<sub>CC</sub>/6 (per gate).

Table 9. Dynamic switching characteristics

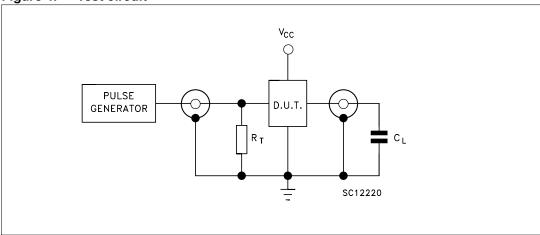
Symbol Parameter		Test condition		Value								
		V <sub>CC</sub>		T <sub>A</sub> = 25 °C			-40 to 85 °C		-55 to 125 °C		Unit	
	V <sub>CC</sub> (V)		Min.	Min. Typ.	Max.	Min.	Max.	Min.	Max.			
V <sub>OLP</sub>	Dynamic low					0.3	0.8					.,
V <sub>OLV</sub>	voltage quiet output <sup>(1)</sup> , <sup>(2)</sup>	5.0	.0	-0.8	-0.3						V	
V <sub>IHD</sub>	Dynamic high voltage input <sup>(1)</sup> , <sup>(3)</sup>	5.0	C <sub>L</sub> = 50 pF	3.5							V	
V <sub>ILD</sub>	Dynamic low voltage input (1), (3)	5.0				1.5					V	

- 1. Worst case package.
- 2. Max number of outputs defined as (n). Data inputs are driven 0 V to 5.0 V, (n-1) outputs switching and one output at GND.
- 3. Max number of data inputs (n) switching. (n-1) switching 0 V to 5.0 V. Inputs under test switching: 5.0 V to threshold ( $V_{ILD}$ ), 0 V to threshold ( $V_{IHD}$ ), f = 1 MHz.

Test circuit 74VHC14

### 5 Test circuit

Figure 4. Test circuit



Note:

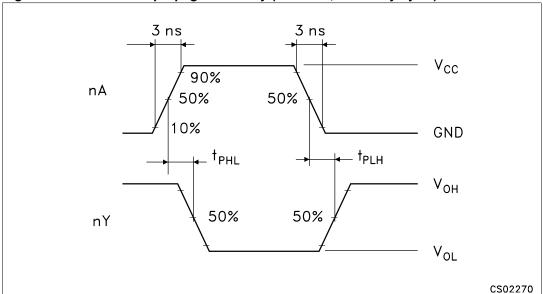
 $C_L = 15/50 pF$  or equivalent (includes jig and probe capacitance)

 $R_T = Z_{OUT}$  of pulse generator (typically 50  $\Omega$ ).

74VHC14 Waveform

## 6 Waveform





Package information 74VHC14

## 7 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: <a href="www.st.com">www.st.com</a>. ECOPACK is an ST trademark.



74VHC14 Package information

Figure 6. SO-14 package outline

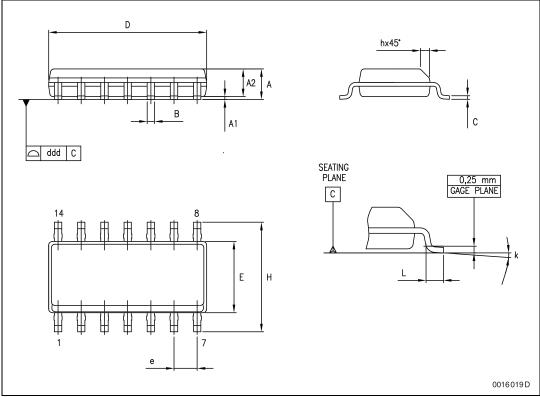


Table 10. SO-14 package mechanical data

			Dime	nsions		
Symbol		mm			inch	
	Min.	Тур.	Max.	Min.	Тур.	Max.
А	1.35		1.75	0.053		0.069
A1	0.1		0.25	0.004		0.010
A2	1.10		1.65	0.043		0.065
В	0.33		0.51	0.013		0.020
С	0.19		0.25	0.007		0.010
D	8.55		8.75	0.337		0.344
E	3.8		4.0	0.150		0.157
е		1.27			0.050	
Н	5.8		6.2	0.228		0.244
h	0.25		0.50	0.010		0.020
L	0.4		1.27	0.016		0.050
k	0°		8°	0°		8°
ddd			0.100			0.004

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Package information 74VHC14

Figure 7. TSSOP14 package outline

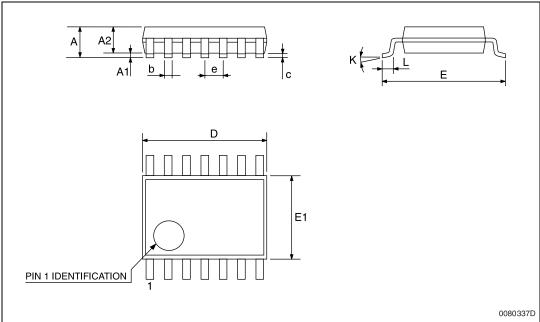
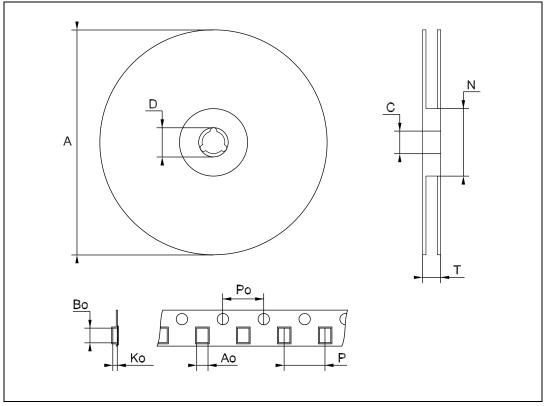


Table 11. TSSOP14 package mechanical data

		ickage meen		nsions				
Symbol		mm		inch				
	Min.	Тур.	Max.	Min.	Тур.	Max.		
Α			1.2			0.047		
A1	0.05		0.15	0.002	0.004	0.006		
A2	0.8	1	1.05	0.031	0.039	0.041		
b	0.19		0.30	0.007		0.012		
С	0.09		0.20	0.004		0.0089		
D	4.9	5	5.1	0.193	0.197	0.201		
Е	6.2	6.4	6.6	0.244	0.252	0.260		
E1	4.3	4.4	4.48	0.169	0.173	0.176		
е		0.65 BSC			0.0256 BSC			
K	0°		8°	0°		8°		
L	0.45	0.60	0.75	0.018	0.024	0.030		

74VHC14 Package information

Figure 8. Tape and reel SO-14 outline



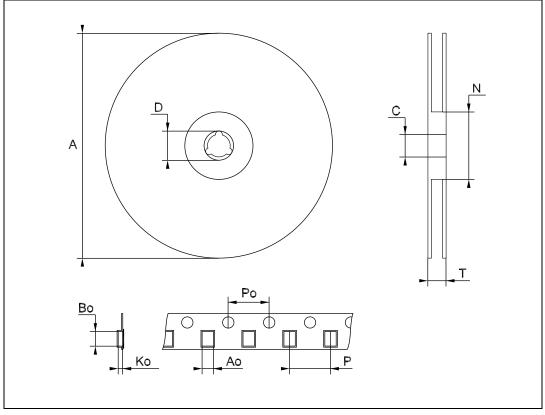
1. Drawing is not in scale.

Table 12. Tape and reel SO-14 mechanical data

			Dim	ensions					
Symbol		mm			inch				
	Min.	Тур.	Max.	Min.	Тур.	Max.			
А			330			12.992			
С	12.8		13.2	0.504		0.519			
D	20.2			0.795					
N	60			2.362					
Т			22.4			0.882			
Ao	6.4		6.6	0.252		0.260			
Во	9		9.2	0.354		0.362			
Ko	2.1		2.3	0.082		0.090			
Po	3.9		4.1	0.153		0.161			
Р	7.9		8.1	0.311		0.319			

Package information 74VHC14

Figure 9. Tape and reel TSSOP14 outline



1. Drawing is not in scale.

Table 13. Tape and reel TSSOP14 mechanical data

Symbol	Dimensions					
	mm			inch		
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α			330			12.992
С	12.8		13.2	0.504		0.519
D	20.2			0.795		
N	60			2.362		
T			22.4			0.882
Ao	6.7		6.9	0.264		0.272
Во	5.3		5.5	0.209		0.217
Ko	1.6		1.8	0.063		0.071
Po	3.9		4.1	0.153		0.161
Р	7.9		8.1	0.311		0.319

74VHC14 Revision history

# 8 Revision history

Table 14. Document revision history

Date	Revision	Changes	
12-Nov-2004	6	Ordering codes revision - pag. 1	
02-May-2007	7	Document reformatted, updated Table 6 on page 6	
15-Jun-2012	8	Added Applications on page 1 Updated Table 1: Device summary on page 1 Updated Top in Table 5: Recommended operating conditions Updated ECOPACK® text in Section 7: Package information Minor textual updates	
03-Oct-2012	9	Added ESD performance into <i>Features</i> .  Added 74VHC14YMTR device and "Marking" to <i>Table 1</i> , updated note 1 below <i>Table 1</i> .  Updated <i>Section 3: Maximum ratings</i> (added cross-references)  Reformatted <i>Section 7: Package information</i> .  Minor corrections throughout document.	

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