



# SB01-15C

## Schottky Barrier Diode 150V, 0.1A, Low IR, Single CP

ON Semiconductor®

<http://onsemi.com>

### Applications

- High frequency rectification (switching regulators, converters, choppers)

### Features

- Low forward voltage ( $V_F$  max=0.75V)
- Low switching noise
- Low leakage current and high reliability due to highly reliable planar structure
- Fast reverse recovery time ( $t_{rr}$  max=10ns)

### Specifications

#### Absolute Maximum Ratings at $T_a=25^\circ\text{C}$

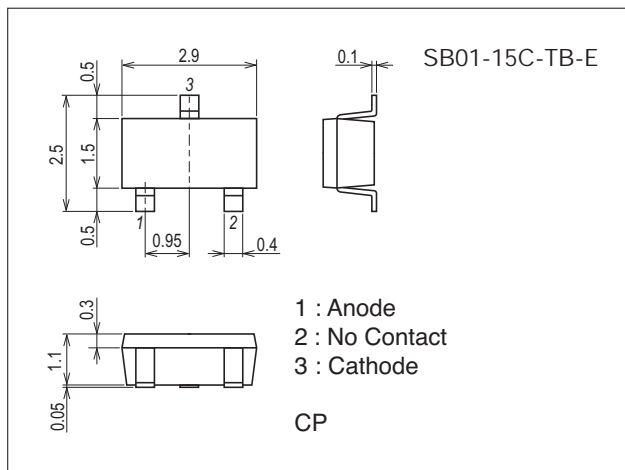
Parameter	Symbol	Conditions	Ratings	Unit
Repetitive Peak Reverse Voltage	$V_{RRM}$		150	V
Nonrepetitive Peak Reverse Surge Voltage	$V_{RSM}$		155	V
Average Output Current	$I_O$		100	mA
Surge Forward Current	$I_{FSM}$	50Hz sine wave, 1 cycle	5	A
Junction Temperature	$T_j$		-55 to +125	$^\circ\text{C}$
Storage Temperature	$T_{stg}$		-55 to +125	$^\circ\text{C}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### Package Dimensions

unit : mm (typ)

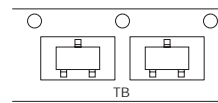
7013A-004



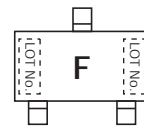
### Product & Package Information

- Package : CP
- JEITA, JEDEC : SC-59, TO-236, SOT-23, TO-236AB
- Minimum Packing Quantity : 3,000 pcs./reel

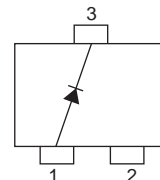
### Packing Type: TB



### Marking



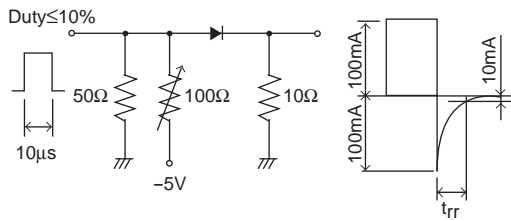
### Electrical Connection



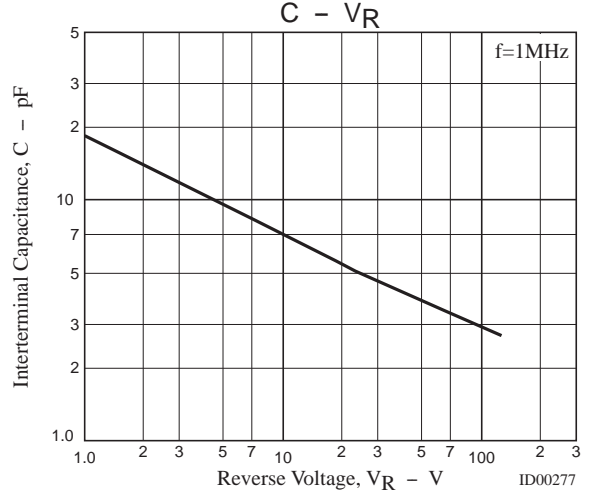
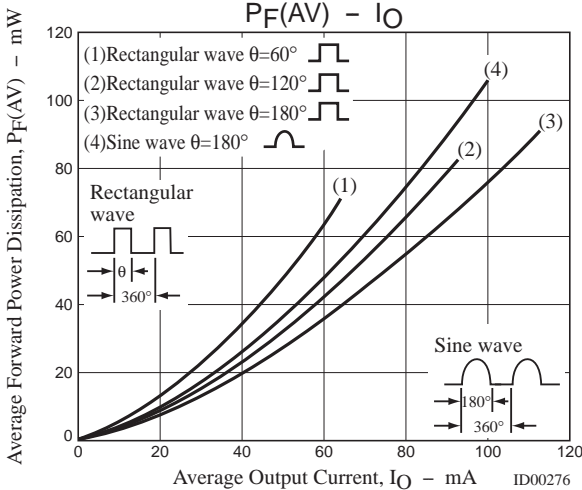
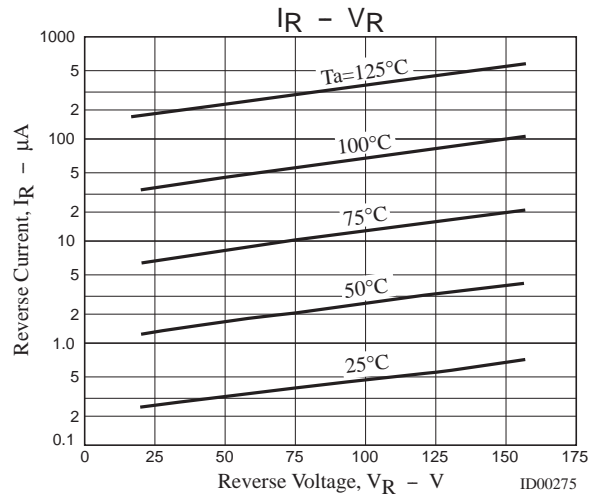
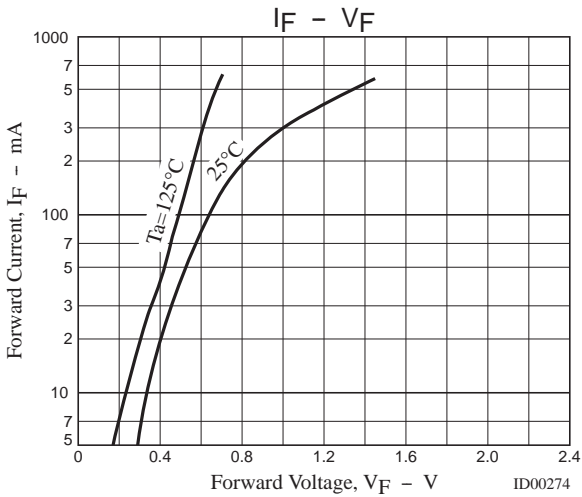
Electrical Characteristics at Ta=25°C

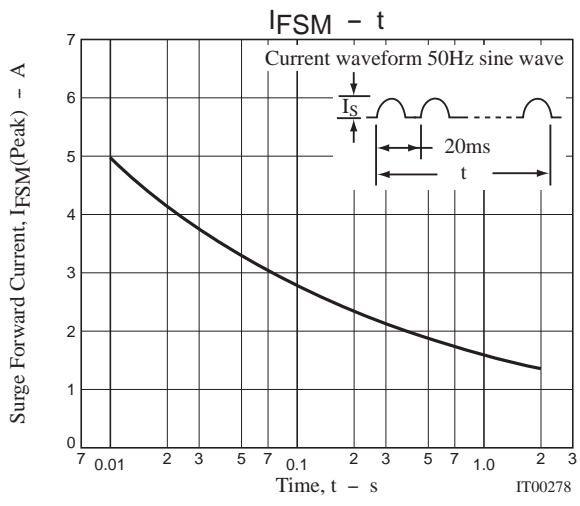
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Reverse Voltage	V <sub>R</sub>	I <sub>R</sub> =200μA	150			V
Forward Voltage	V <sub>F</sub>	I <sub>F</sub> =100mA			0.75	V
Reverse Current	I <sub>R</sub>	V <sub>R</sub> =75V			50	μA
Interterminal Capacitance	C	V <sub>R</sub> =10V, f=1MHz		7		pF
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =I <sub>R</sub> =100mA, See specified Test Circuit.			10	ns
Thermal Resistance	R <sub>th(j-a)1</sub>			420		°C / W
	R <sub>th(j-a)2</sub>	Mounted in Cu-foiled area of 16mm <sup>2</sup> ×0.2mm on glass epoxy board		330		°C / W

t<sub>rr</sub> Test Circuit



Ordering Information





Embossed Taping Specification

SB01-15C-TB-E

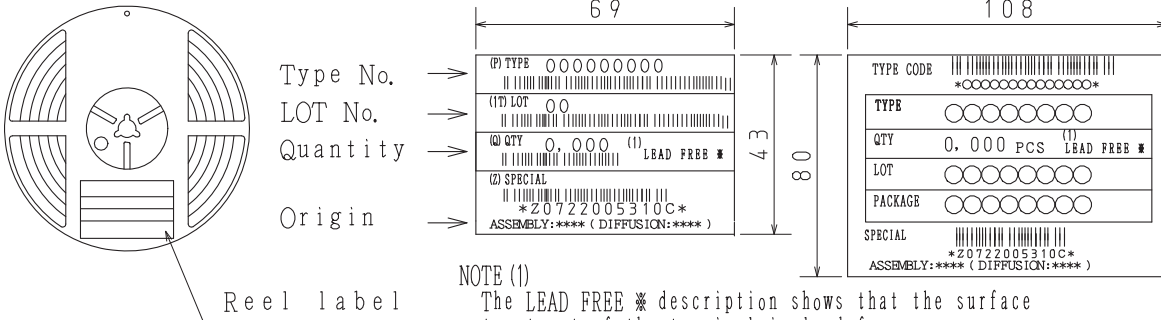
1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
CP	CP	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Packing method

Reel label, Inner box label (unit:mm)      Outer box label

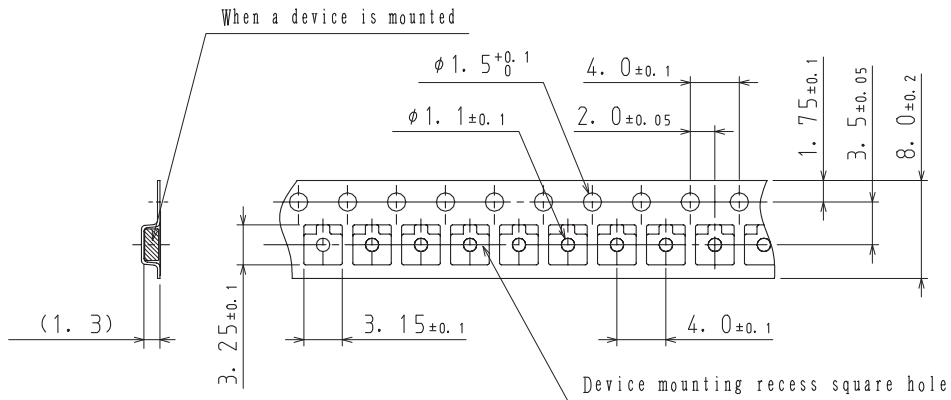
It is a label at the time of factory shipments. The form of a label may change in physical distribution process.



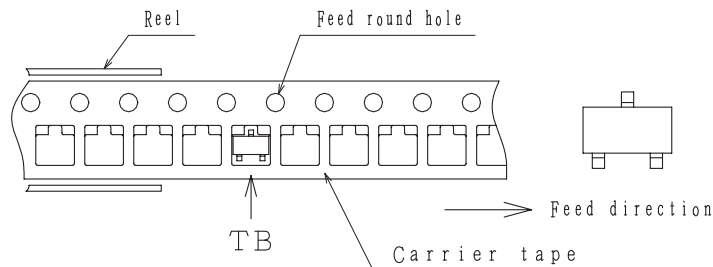
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



2-2. Device placement direction

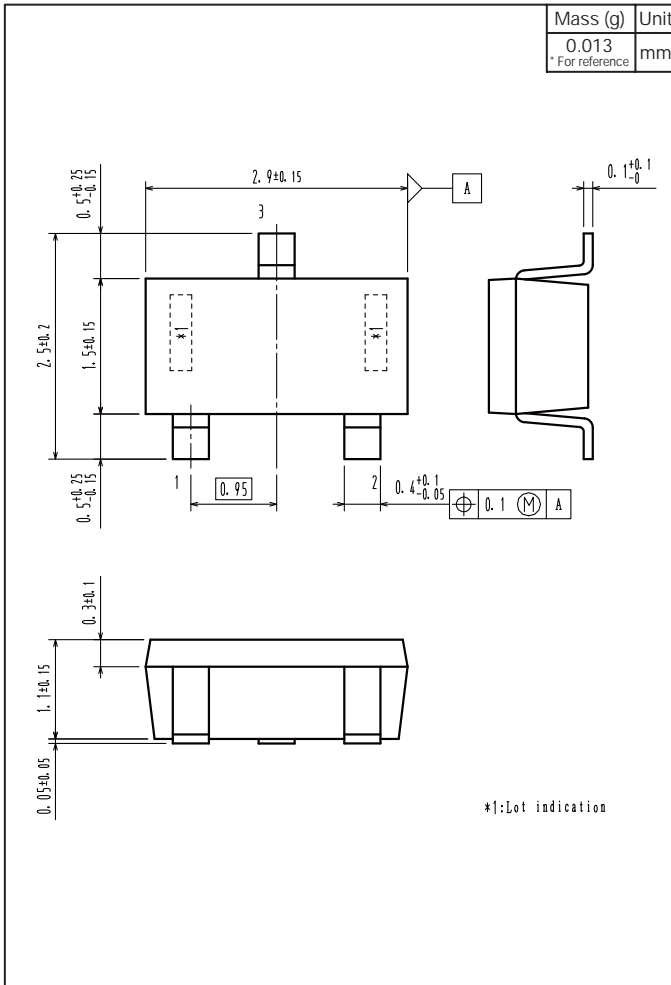


Those with one electrode terminal on the feed hole side.....TB

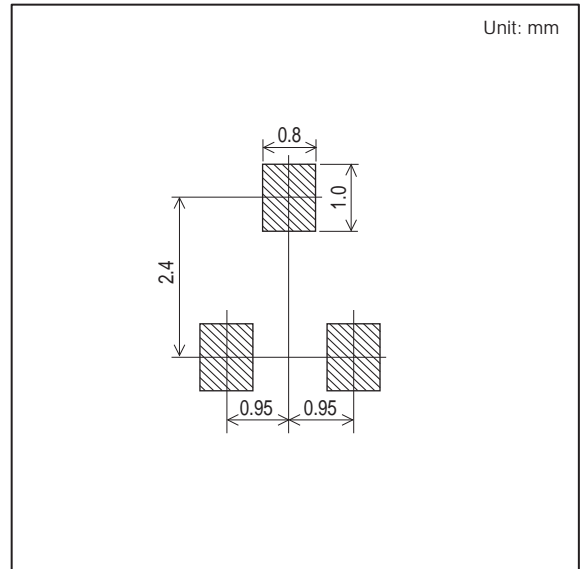
# SB01-15C

## Outline Drawing

SB01-15C-TB-E



## Land Pattern Example



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