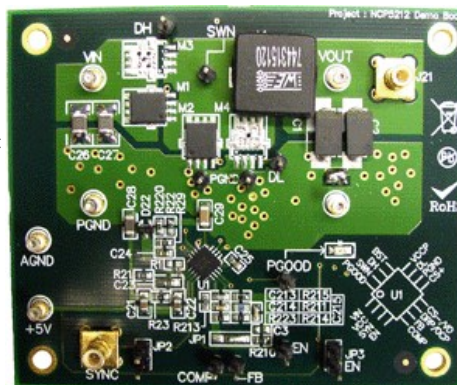




# NCP5212AGEVB: Synchronous Step-Down Controller Evaluation Board

The NCP5212A/NCP5212T is a synchronous step-down controller for high performance systems battery-power systems. The NCP5212A/NCP5212T includes a high efficiency PWM controller. A pin is provided to allow two devices in interleaved operation. An internal power good voltage monitor tracks the SMPS output. NCP5212A/NCP5212T also features soft-start sequence, UVLO for Vcc and switcher, over-voltage protection, over-current protection, under-voltage protection and thermal shutdown. The IC is packaged in QFN16.



## Features and Applications

### Features

- 0.8% accuracy 0.8 V Reference
- 4.5 V to 27 V Battery/Adapter Voltage Range
- Adjustable Output Voltage Range: 0.8 V to 3.3 V
- Synchronization Interleaving between Two NCP5212A/NCP5212Ts
- Skip Mode for Power Saving Operation at Light Load
- Lossless Inductor Current Sensing
- Programmable Transient-Response-Enhancement (TRE) Control
- Programmable Adaptive Voltage Positioning (AVP)
- Input Supply Feedforward Control
- Internal Soft-Start
- Integrated Output Discharge (Soft-Stop)
- Build-in Adaptive Gate Drivers
- PGOOD Indication
- Over-voltage, Under-voltage and Over-current Protections
- Thermal Shutdown
- These Devices are Pb-Free and are RoHS Compliant

### Applications

- Notebook Computers
- Tablet Computers
- System Power

## Evaluation/Development Tool Information

Product	Status	Compliance	Short Description	Parts Used	Action
NCP5212AGEVB	Active	Pb-free	Synchronous Step-Down Controller Evaluation Board	NCP5212AMNTXG	<a href="#">» Contact Local Sales Office</a>

## Technical Documents

Type	Document Title	Document ID/Size	Rev
Eval Board: BOM	NCP5212AGEVB Bill of Materials ROHS Compliant	NCP5212AGEVB_BOM_ROHS.PDF - 174.0 KB	0
Eval Board: Gerber	NCP5212AGEVB Gerber Layout Files (Zip Format)	NCP5212AGEVB_GERBERS.ZIP - 288.0 KB	0
Eval Board: Schematic	NCP5212AGEVB Schematic	NCP5212AGEVB_SCHEMATIC.PDF - 138.0 KB	0
Eval Board: Test Procedure	NCP5212AGEVB Test Procedure	NCP5212AGEVB_TEST_PROCEDURE.PDF - 106.0 KB	0

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