

## MC33063DFBSTGEVB: Monolithic Control Circuit Evaluation Board

The MC34063A Series is a monolithic control circuit containing the primary functions required for DC-to-DC converters. These devices consist of an internal temperature compensated reference, comparator, controlled duty cycle oscillator with an active current limit circuit, driver and high current output switch. This series was specifically designed to be incorporated in Step-Down and Step-Up and Voltage-Inverting applications with a minimum number of external components. Refer to Application Note AN920/D for additional design information. The excel spread sheet for thermal considerations is available too. The MC33063 is an extended temperature version. The NCV33063 is an extended temp version for automotive. The one layer demoboard in step down topology contend additional output filter. Size of demoboard: 50 x 50 mm



### Features and Applications

#### Features

- Operation from 3.0 V to 40 V Input
- Low Standby Current
- Current Limiting
- Output Switch Current to 1.5 A
- Output Voltage Adjustable
- Frequency Operation to 100 kHz
- Precision 2% Reference
- Pb-Free Packages are Available

#### Applications

- High Power LED Lighting
- Battery Chargers

### Evaluation/Development Tool Information

Product	Status	Compliance	Short Description	Parts Used	Action
<a href="#">MC33063DFBSTGEVB</a>	Active	Pb-free	Monolithic Control Circuit Evaluation Board	<a href="#">MC33063MNTXG</a>	<a href="#">» Contact Local Sales Office</a> <a href="#">» Inventory</a>

### Technical Documents

Type	Document Title	Document ID/Size	Rev
Eval Board: BOM	MC33063DFBSTGEVB Bill of Materials ROHS Compliant	<a href="#">MC33063DFBSTGEVB_BOM_ROHS.PDF</a> - 82.0 KB	0
Eval Board: Gerber	MC33063DFBSTGEVB Gerber Layout Files (Zip Format)	<a href="#">MC33063DFBSTGEVB_GERBER.ZIP</a> - 73.0 KB	0
Eval Board: Schematic	MC33063DFBSTGEVB Schematic	<a href="#">MC33063DFBSTGEVB_SCHEMATIC.PDF</a> - 21.0 KB	0
Eval Board: Test Procedure	MC33063DFBSTGEVB Test Procedure	<a href="#">MC33063DFBSTGEVB_TEST_PROCEDURE.PDF</a> - 220.0 KB	0

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