	REVISIONS	1	
LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
Α	Add device type 02. Add case outline Y. Change to note in section 1.5rrp	16-10-12	C. SAFFLE

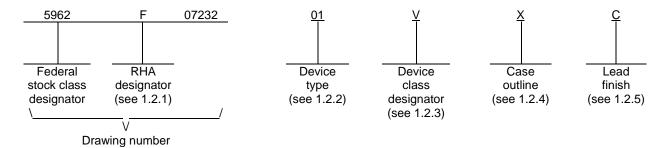


REV																				
SHEET																				
REV	Α	Α	Α	Α	Α															
SHEET	15	16	17	18	19															
REV STATUS	•			REV	,		Α	Α	Α	Α	Α	Α	Α	Α	Α	Α	Α	Α	Α	Α
OF SHEETS				SHE	ET		1	2	3	4	5	6	7	8	9	10	11	12	13	14
PMIC N/A					PARED ICK OF		1			DLA LAND AND MARITIME										
STAM MICRO DRA		CUIT		0	CKED AJESH		ADIA			COLUMBUS, OHIO 43218-3990 <a href="http://www.landandmaritime.dla.mil">http://www.landandmaritime.dla.mil</a>										
THIS DRAWIN FOR US DEPAF AND AGEN DEPARTMEN	SE BY A RTMEN ICIES (	ALL ITS OF THE	=	С	ROVEI HARLE WING	S F. S				MICROCIRCUIT, LINEAR, SINGLE, HIGH SPEED CURRENT FEEDBACK OPERATIONAL AMPLIFIER, MONOLITHIC SILICON						:D				
AMSC N/A		REV	ISION		A				ZE A		GE CC	3	OF 1	5962-07232						
										SHEET 1 OF 19										

DSCC FORM 2233 APR 97

# 1. SCOPE

- 1.1 <u>Scope</u>. This drawing documents two product assurance class levels consisting of high reliability (device class Q) and space application (device class V). A choice of case outlines and lead finishes are available and are reflected in the Part or Identifying Number (PIN). When available, a choice of Radiation Hardness Assurance (RHA) levels is reflected in the PIN.
  - 1.2 PIN. The PIN is as shown in the following example:



- 1.2.1 RHA designator. Device classes Q and V RHA marked devices meet the MIL-PRF-38535 specified RHA levels and are marked with the appropriate RHA designator. A dash (-) indicates a non-RHA device.
  - 1.2.2 <u>Device type(s)</u>. The device type(s) identify the circuit function as follows:

Device type	Generic number	<u>Circuit function</u>
01	RHF350	Radiation hardened, single, high speed, current feedback, operational amplifier
02	RHF350A	Radiation hardened, single, high speed, current feedback, operational amplifier

1.2.3 <u>Device class designator</u>. The device class designator is a single letter identifying the product assurance level as follows:

Device class

**Device requirements documentation** 

Q or V

Certification and qualification to MIL-PRF-38535

1.2.4 Case outline(s). The case outline(s) are as designated in MIL-STD-1835 and as follows:

Outline letter	Descriptive designator	<u>Terminals</u>	Package style
X	See figure 1	8	Flat pack <u>1</u> /
Y	See figure 1	8	Flat pack <u>1</u> / <u>2</u> /

1.2.5 Lead finish. The lead finish is as specified in MIL-PRF-38535 for device classes Q and V.

- $\underline{1}/$  Al<sub>2</sub>O<sub>3</sub> ceramic header with metalized bottom side and pullback of 0.01 x 0.02 inches.
- <u>2</u>/ Lid is connected to the ground.

STANDARD MICROCIRCUIT DRAWING	SIZE <b>A</b>		5962-07232
DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990		REVISION LEVEL A	SHEET 2

## 1.3 Absolute maximum ratings. 3/

Supply voltage:

- 11 7 3 -	
Single supply mode	VCC with respect to GND = 6 V
Dual supply voltage	+VCC = +3 V
Input voltage range (VIN)	±2.5 V
Differential input voltage (VID)	±0.5 V <u>4</u> /
Power dissipation (PD) at TA = +25°C for TJ = 150°C	830 mW <u>5</u> /
Maximum junction temperature (TJ)	+150°C
Lead temperature (soldering, 10 seconds)	+260°C <u>6</u> /
Storage temperature range	65°C to +150°C
	0000111

1.4 Recommended operating conditions.

Supply voltage (VCC)	4.5 V to 5.5 V
Common mode input voltage range (VICM)	-VCC + 1.5 V to +VCC - 1.5 V
Ambient operating temperature range (TA)	-55°C to +125°C

1.5 Radiation features.

Maximum total dose available (dose rate  $\leq$  10 mrads(Si)/s) ......300K Rad (Si)  $\frac{7}{2}$ 

The manufacturer supplying RHA device types 01 and 02 on this drawing has performed characterization test to demonstrate that the parts do not exhibit enhanced low dose rate sensitivity (ELDRS) in accordance with MIL-STD-883, method 1019, paragraph 3.13.1.1.

The manufacturer supplying device types 01 and 02 has performed ELDRS characterization testing in accordance with MIL-STD-883 method 1019 paragraph 3.13.1.1 and the parts exhibited no enhanced low dose rate sensitivity (ELDRS) at total ionizing dose level of 300K rad (Si). The radiation end point limits for the noted parameters are guaranteed only for the conditions as specified in MIL-STD-883, method 1019, condition A and condition D to a maximum total dose of 300K rad (Si).

STANDARD MICROCIRCUIT DRAWING DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		5962-07232
		REVISION LEVEL A	SHEET 3

Stresses above the absolute maximum rating may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.

Differential voltages are the non-inverting input terminal with respect to the inverting input terminal.

Short circuits can cause excessive heating. Destructive dissipation can result from short circuit on amplifiers.

Distance of not less than 1.5 mm from the device body and the same lead shall not be resoldered until 3 minutes have elapsed.

#### 2. APPLICABLE DOCUMENTS

2.1 <u>Government specification, standards, and handbooks</u>. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

#### DEPARTMENT OF DEFENSE SPECIFICATION

MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification for.

#### DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard Microcircuits.

MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

#### DEPARTMENT OF DEFENSE HANDBOOKS

MIL-HDBK-103 - List of Standard Microcircuit Drawings.

MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at <a href="http://quicksearch.dla.mil">http://quicksearch.dla.mil</a> or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 <u>Order of precedence</u>. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

#### 3. REQUIREMENTS

- 3.1 <u>Item requirements</u>. The individual item requirements for device classes Q and V shall be in accordance with MIL-PRF-38535 as specified herein, or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.
  - 3.1.1 Microcircuit die. For the requirements of microcircuit die, see appendix A to this document.
- 3.2 <u>Design, construction, and physical dimensions</u>. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein for device classes Q and V.
  - 3.2.1 Case outlines. The case outlines shall be in accordance with 1.2.4 herein and figure 1.
  - 3.2.2 <u>Terminal connections</u>. The terminal connections shall be as specified on figure 2.
  - 3.2.3 Block diagram. The block diagram shall be as specified on figure 3.
- 3.2.4 Radiation exposure circuit. The radiation exposure circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing and acquiring activity upon request.
- 3.3 <u>Electrical performance characteristics and postirradiation parameter limits</u>. Unless otherwise specified herein, the electrical performance characteristics and postirradiation parameter limits are as specified in table I and shall apply over the full ambient operating temperature range.
- 3.4 <u>Electrical test requirements</u>. The electrical test requirements shall be the subgroups specified in table IIA. The electrical tests for each subgroup are defined in table I.
- 3.5 <u>Marking</u>. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked. For packages where marking of the entire SMD PIN number is not feasible due to space limitations, the manufacturer has the option of not marking the "5962-" on the device. For RHA product using this option, the RHA designator shall still be marked. Marking for device classes Q and V shall be in accordance with MIL-PRF-38535.

STANDARD MICROCIRCUIT DRAWING	SIZE <b>A</b>		5962-07232
DLA LAND AND MARITIME		REVISION LEVEL	SHEET
COLUMBUS, OHIO 43218-3990		A	4

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions <u>1</u> / -55°C ≤ TA ≤ +125°C	Group A subgroups	Device type	Lir	nits	Unit
		+VCC = +2.5 V, -VCC = -2.5 V unless otherwise specified			Min	Max	
Input offset voltage	VIO		1,2,3	01, 02	-4	4	mV
Input bias current	+liB		1,2,3	01, 02		35	μА
	-liB		1			20	
			2,3			25	
Common mode rejection	CMRR	$\Delta VIC = \pm 1 V$	1	01, 02	54		dB
ratio			2,3		50		
Supply voltage rejection ratio	SVRR	ΔVCC = 3.5 V to 5 V	1	01, 02	68		dB
			2,3		55		
Supply current	ICC	No load	1,2,3	01, 02		4.9	mA
Transimpedance	ROL	Vout = $\pm 1$ V, RL = $100 \Omega$	1	01, 02	170		kΩ
			2,3		150		
Output sink current	ISINK	Output to GND	1,2,3	01, 02	135		mA
Output source current	ISOURCE	Output to GND	1,2,3	01, 02		-140	mA
Low level output voltage	VOL	RL = 100 Ω	1	01, 02		-1.44	V
			2,3			-1.3	
High level output voltage	Vон	RL = 100 Ω	1	01, 02	1.44		V
			2,3		1.3		
-3 dB bandwidth	BW	RL = 100 Ω, AV = -2	4,5,6	01	250		MHz
Slew rate 2/	+SR	VOUT = 2 VPP, AV = +2,	4,5,6	01, 02	700		V/μs
	-SR	RL = 100 Ω			700		

- 1/ RHA devices supplied to this drawing have been characterized through all levels M, D, P, L, R, and F of irradiation. However, this device is only tested at the "F" level for high and low dose rates. Pre and post irradiation values are identical unless otherwise specified in table I. When performing post irradiation electrical measurements for any RHA level, TA = +25°C. This part has been tested and does not demonstrate low dose rate sensitivity at 300 krads(Si). The radiation end points limits for the noted parameters are guaranteed for the conditions specified in MIL-STD-883, method 1019, condition A for high dose rate and condition D for low dose rate.
- 2/ Controlled via design or process and is not directly tested. Characterized on initial design release and upon design or process changes which affect this parameter.

STANDARD MICROCIRCUIT DRAWING	SIZE <b>A</b>		5962-07232
DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990		REVISION LEVEL A	SHEET 5

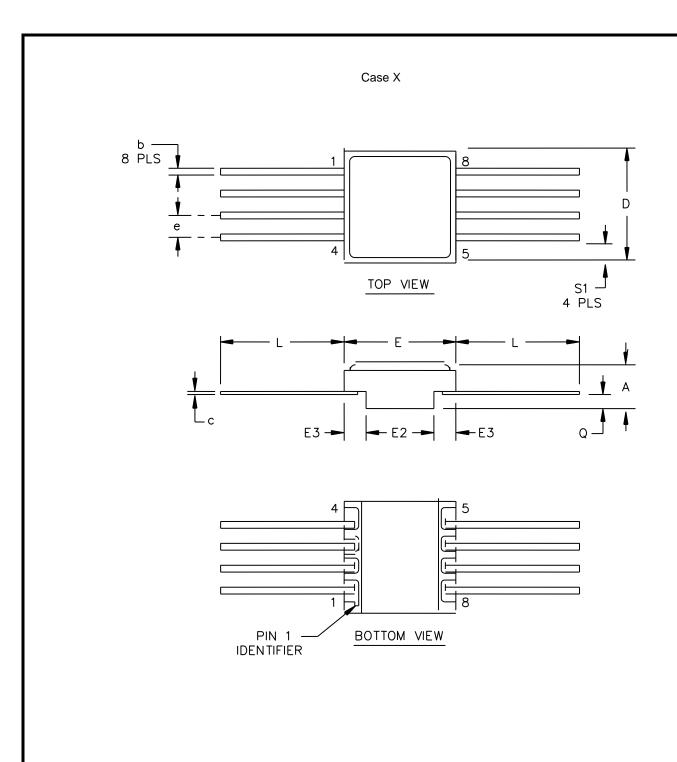


FIGURE 1. Case outline.

STANDARD MICROCIRCUIT DRAWING	SIZE <b>A</b>		5962-07232
DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990		REVISION LEVEL A	SHEET 6

# Case X – continued.

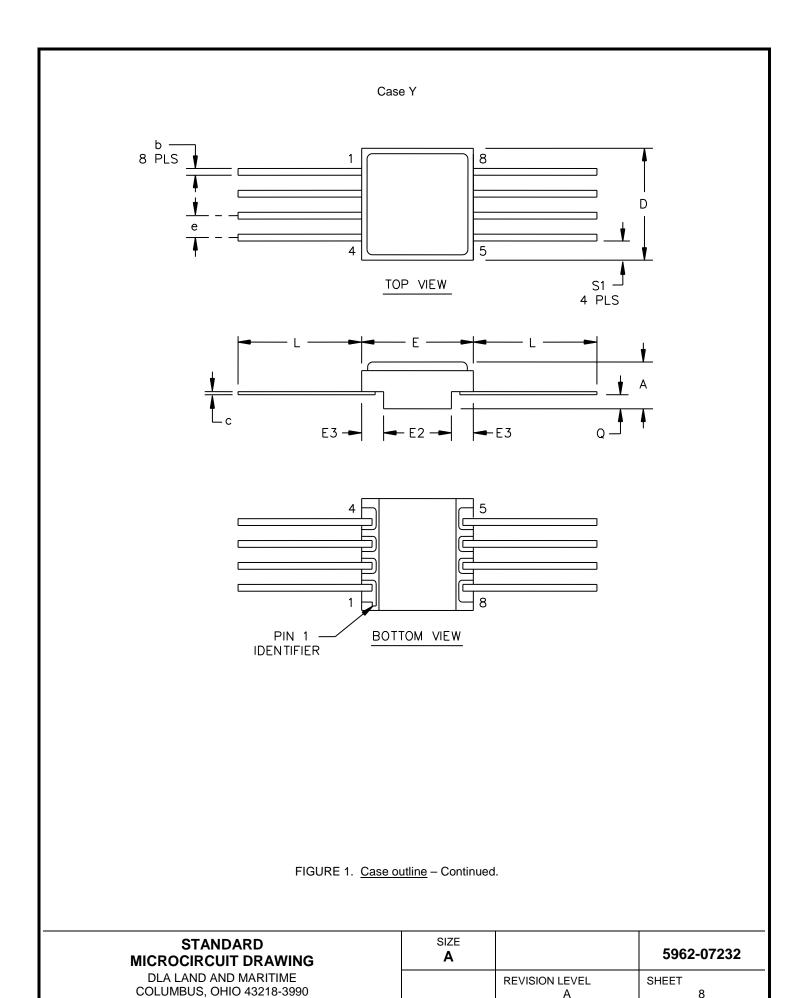
	Dimensions			
Symbol	Inches		Millimeters	
	Min	Max	Min	Max
А	.088	.104	2.24	2.64
b	.015	.019	0.38	0.48
С	.004	.006	0.10	0.16
D	.250	.260	6.35	6.61
E	.250	.260	6.35	6.61
E2	.170	.180	4.32	4.58
E3	.035	.045	0.88	1.14
е	.050	BSC	1.27	BSC
L	.118	BSC	3.00	BSC
Q	.026	.036	0.66	0.92
S1	.036	.052	0.92	1.32
N		8		8

## NOTES:

- 1. The US government preferred system of measurement is the metric SI system. However, since this item was originally designed using inch-pound units of measurement, in the event of conflict between the metric and inch pound units, the inch pound units shall take precedence.
- 2. N is the maximum number of terminal positions.

FIGURE 1. Case outline - Continued.

STANDARD MICROCIRCUIT DRAWING	SIZE <b>A</b>		5962-07232
DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990		REVISION LEVEL A	SHEET 7



8

Case Y - continued.

Symbol		Inches		М	illimeters	
	Min	Тур	Max	Min	Тур	Max
Α	.088	.096	.104	2.24	2.44	2.64
b	.015	.017	.019	0.38	0.43	0.48
С	.004	.005	.006	0.10	0.13	0.16
D	.250	.255	.260	6.35	6.48	6.61
E	.250	.255	.260	6.35	6.48	6.61
E2	.170	.175	.180	4.32	4.45	4.58
E3	.035	.040	.045	0.88	1.01	1.14
е		.050 BSC		1	.27 BSC	
L	.256		.291	6.51		7.38
Q	.026	.031	.036	0.66	0.79	0.92
S1	.036	.044	.052	0.92	1.12	1.32
N		8			8	

# NOTES:

- 1. The U.S. government preferred system of measurement is the metric SI system. However, since this item was originally designed using inch-pound units of measurement, in the event of conflict between the metric and inch-pound units, the inch-pound units shall take precedence.
- 2. N is the maximum number of terminal positions.

FIGURE 1. <u>Case outline</u> – Continued.

STANDARD MICROCIRCUIT DRAWING	SIZE <b>A</b>		5962-07232
DLA LAND AND MARITIME		REVISION LEVEL	SHEET
COLUMBUS, OHIO 43218-3990		A	9

Device type	01	02
Case outline	Х	Υ
Terminal number	Termina	symbol
1	NC	NC
2	-INPUT	-INPUT
3	+INPUT	+INPUT
4	-Vcc	-Vcc
5	NC	NC
6	OUTPUT	OUTPUT
7	+Vcc	+Vcc
8	NC	NC

FIGURE 2. Terminal connections.

STANDARD MICROCIRCUIT DRAWING	SIZE <b>A</b>		5962-07232
DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990		REVISION LEVEL A	SHEET 10

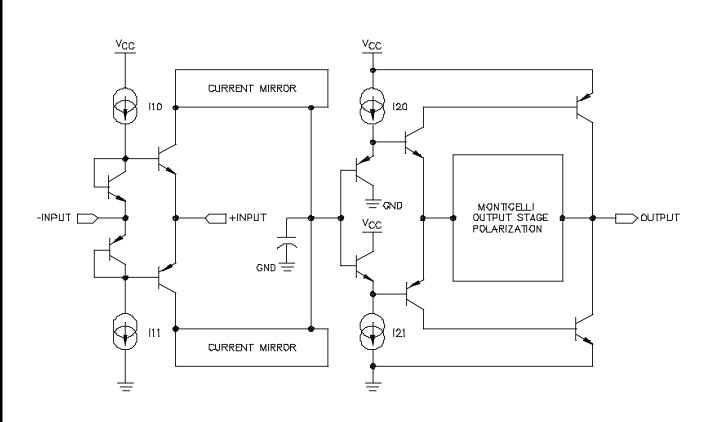


FIGURE 3. Block diagram.

STANDARD MICROCIRCUIT DRAWING	SIZE <b>A</b>		5962-07232
DLA LAND AND MARITIME		REVISION LEVEL	SHEET
COLUMBUS, OHIO 43218-3990		A	11

- 3.5.1 <u>Certification/compliance mark</u>. The certification mark for device classes Q and V shall be a "QML" or "Q" as required in MIL-PRF-38535.
- 3.6 <u>Certificate of compliance</u>. For device classes Q and V, a certificate of compliance shall be required from a QML-38535 listed manufacturer in order to supply to the requirements of this drawing (see 6.6.1 herein). The certificate of compliance submitted to DLA Land and Maritime-VA prior to listing as an approved source of supply for this drawing shall affirm that the manufacturer's product meets, for device classes Q and V, the requirements of MIL-PRF-38535 and herein.
  - 3.7 Certificate of conformance. A certificate of conformance as required for device classes Q and V in MIL-PRF-38535.
  - 4. VERIFICATION
- 4.1 <u>Sampling and inspection</u>. For device classes Q and V, sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.
- 4.2 <u>Screening</u>. For device classes Q and V, screening shall be in accordance with MIL-PRF-38535, and shall be conducted on all devices prior to qualification and technology conformance inspection.
  - 4.2.1 Additional criteria for device classes Q and V.
    - a. The burn-in test duration, test condition and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document revision level control of the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.
    - b. Interim and final electrical test parameters shall be as specified in table IIA herein.
    - c. Additional screening for device class V beyond the requirements of device class Q shall be as specified in MIL-PRF-38535, appendix B.
- 4.3 <u>Qualification inspection for device classes Q and V.</u> Qualification inspection for device classes Q and V shall be in accordance with MIL-PRF-38535. Inspections to be performed shall be those specified in MIL-PRF-38535 and herein for groups A, B, C, D, and E inspections (see 4.4.1 through 4.4.4).
- 4.4 <u>Conformance inspection</u>. Technology conformance inspection for classes Q and V shall be in accordance with MIL-PRF-38535 including groups A, B, C, D, and E inspections, and as specified herein.
  - 4.4.1 Group A inspection.
    - a. Tests shall be as specified in table IIA herein.
    - b. Subgroups 7, 8, 9, 10, and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.
  - 4.4.2 Group C inspection. The group C inspection end-point electrical parameters shall be as specified in table IIA herein.
- 4.4.2.1 Additional criteria for device classes Q and V. The steady-state life test duration, test condition and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The test circuit shall be maintained under document revision level control by the device manufacturer's TRB in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.

STANDARD MICROCIRCUIT DRAWING	SIZE <b>A</b>		5962-07232
DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990		REVISION LEVEL A	SHEET 12

# TABLE IIA Electrical test requirements.

Test requirements	Subgroups (in accordance with MIL-PRF-38535, table III)		
	Device class Q	Device class V	
Interim electrical parameters (see 4.2)	1	1	
Final electrical parameters (see 4.2)	1,2,3,4,5,6 <u>1</u> /	1,2,3,4,5,6 <u>1</u> / <u>2</u> /	
Group A test requirements (see 4.4)	1,2,3,4,5,6	1,2,3,4,5,6	
Group C end-point electrical parameters (see 4.4)	1,2,3,4,5,6	1,2,3,4,5,6 <u>2</u> /	
Group D end-point electrical parameters (see 4.4)	1,2,3,4,5,6	1,2,3,4,5,6	
Group E end-point electrical parameters (see 4.4)	1	1	

<sup>1/</sup> PDA applies to subgroup 1.

TABLE IIB. <u>Burn-in and operating life test delta parameters</u>. <u>1/ 2/</u>

Parameters	Symbol	Test conditions	Device types	Delta limits	Units
Change in input offset voltage	ΔVΙΟ	+VCC = 2.5 V, -VCC = -2.5 V	01, 02	±2.5	mV
Change in supply current	ΔICC	+VCC = 2.5 V, -VCC = -2.5 V	01, 02	±0.05	mA
Change in non-inverting input bias current	Δ+IIB	+VCC = 2.5 V, -VCC = -2.5 V	01, 02	±1.5	μΑ
Change in inverting input bias current	Δ-IIB	+VCC = 2.5 V, -VCC = -2.5 V	01, 02	±1.5	μΑ

 $<sup>\</sup>underline{1}$ / Deltas are performed at room temperature, TA = +25°C.

STANDARD MICROCIRCUIT DRAWING	SIZE <b>A</b>		5962-07232
DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990		REVISION LEVEL A	SHEET 13

Delta limits as specified in table IIB shall be required where specified, and the delta limits shall be computed with reference to the zero hour electrical parameters (see Table I).

<sup>2/ 240</sup> hour burn-in and 1,000 hour operating group C life test.

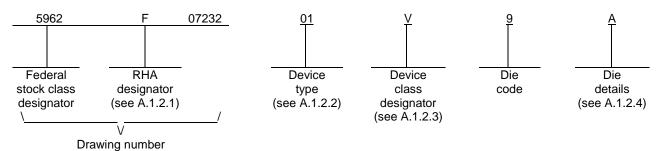
- 4.4.3 Group D inspection. The group D inspection end-point electrical parameters shall be as specified in table IIA herein.
- 4.4.4 <u>Group E inspection</u>. Group E inspection is required only for parts intended to be marked as radiation hardness assured (see 3.5 herein).
  - a. End-point electrical parameters shall be as specified in table IIA herein.
  - b. For device classes Q and V, the devices or test vehicle shall be subjected to radiation hardness assured tests as specified in MIL-PRF-38535 for the RHA level being tested. All device classes must meet the postirradiation end-point electrical parameter limits as defined in table I at T<sub>A</sub> = +25°C ±5°C, after exposure, to the subgroups specified in table IIA herein.
- 4.4.4.1 <u>Total dose irradiation testing</u>. Total dose irradiation testing shall be performed in accordance with MIL-STD-883 method 1019, condition A and condition D, as specified herein.
- 4.4.4.1.1 <u>Accelerated annealing test</u>. Accelerated annealing tests shall be performed on all devices requiring a RHA level greater than 5 krads(Si). The post-anneal end-point electrical parameter limits shall be as specified in table I herein and shall be the pre-irradiation end-point electrical parameter limit at 25°C ±5°C. Testing shall be performed at initial qualification and after any design or process changes which may affect the RHA response of the device.
  - 5. PACKAGING
- 5.1 <u>Packaging requirements</u>. The requirements for packaging shall be in accordance with MIL-PRF-38535 for device classes Q and V.
  - 6. NOTES
- 6.1 <u>Intended use</u>. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.
- 6.1.1 <u>Replaceability</u>. Microcircuits covered by this drawing will replace the same generic device covered by a contractor prepared specification or drawing.
- 6.2 <u>Configuration control of SMD's</u>. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished using DD Form 1692, Engineering Change Proposal.
- 6.3 <u>Record of users</u>. Military and industrial users should inform DLA Land and Maritime when a system application requires configuration control and which SMD's are applicable to that system. DLA Land and Maritime will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DLA Land and Maritime-VA, telephone (614) 692-8108.
- 6.4 <u>Comments</u>. Comments on this drawing should be directed to DLA Land and Maritime-VA, Columbus, Ohio 43218-3990, or telephone (614) 692-0540.
- 6.5 <u>Abbreviations, symbols, and definitions</u>. The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535 and MIL-HDBK-1331.
  - 6.6 Sources of supply.
- 6.6.1 <u>Sources of supply for device classes Q and V</u>. Sources of supply for device classes Q and V are listed in MIL-HDBK-103 and QML-38535. The vendors listed in QML-38535 have submitted a certificate of compliance (see 3.6 herein) to DLA Land and Maritime-VA and have agreed to this drawing.

STANDARD MICROCIRCUIT DRAWING	SIZE <b>A</b>		5962-07232
DLA LAND AND MARITIME		REVISION LEVEL	SHEET
COLUMBUS, OHIO 43218-3990		A	14

## A.1 SCOPE

A.1.1 <u>Scope</u>. This appendix establishes minimum requirements for microcircuit die to be supplied under the Qualified Manufacturers List (QML) Program. QML microcircuit die meeting the requirements of MIL-PRF-38535 and the manufacturers approved QM plan for use in monolithic microcircuits, multi-chip modules (MCMs), hybrids, electronic modules, or devices using chip and wire designs in accordance with MIL-PRF-38534 are specified herein. Two product assurance classes consisting of military high reliability (device class Q) and space application (device class V) are reflected in the Part or Identification Number (PIN). When available, a choice of Radiation Hardiness Assurance (RHA) levels are reflected in the PIN.

A.1.2 PIN. The PIN is as shown in the following example:



A.1.2.1 RHA designator. Device classes Q and V RHA identified die meet the MIL-PRF-38535 specified RHA levels. A dash (-) indicates a non-RHA die.

A.1.2.2 <u>Device type(s)</u>. The device type(s) identify the circuit function as follows:

Device type	Generic number	<u>Circuit function</u>
01	RHF350	Radiation hardened, single, high speed,
		current feedback operational amplifier

A.1.2.3 Device class designator.

Device class

Device requirements documentation

Q or V

Certification and qualification to the die requirements of MIL-PRF-38535

STANDARD MICROCIRCUIT DRAWING	SIZE <b>A</b>		5962-07232
DLA LAND AND MARITIME		REVISION LEVEL	SHEET
COLUMBUS, OHIO 43218-3990		A	15

A.1.2.4 <u>Die details</u>. The die details designation is a unique letter which designates the die's physical dimensions, bonding pad location(s) and related electrical function(s), interface materials, and other assembly related information, for each product and variant supplied to this appendix.

A.1.2.4.1 Die physical dimensions.

<u>Die type</u> <u>Figure number</u>

01 A-1

A.1.2.4.2 Die bonding pad locations and electrical functions.

<u>Die type</u> <u>Figure number</u>

01 A-1

A.1.2.4.3 Interface materials.

<u>Die type</u> <u>Figure number</u>

01 A-1

A.1.2.4.4 Assembly related information.

<u>Die type</u> <u>Figure number</u>

01 A-1

- A.1.3 Absolute maximum ratings. See paragraph 1.3 herein for details.
- A.1.4 Recommended operating conditions. See paragraph 1.4 herein for details.
- A.2 APPLICABLE DOCUMENTS.
- A.2.1 <u>Government specification, standards, and handbooks</u>. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATION

MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification for.

DEPARTMENT OF DEFENSE STANDARD

MIL-STD-883 - Test Method Standard Microcircuits.

DEPARTMENT OF DEFENSE HANDBOOKS

MIL-HDBK-103 - List of Standard Microcircuit Drawings.

MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at <a href="http://quicksearch.dla.mil">http://quicksearch.dla.mil</a> or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

A.2.2 <u>Order of precedence</u>. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

STANDARD MICROCIRCUIT DRAWING	SIZE <b>A</b>		5962-07232
DLA LAND AND MARITIME		REVISION LEVEL	SHEET
COLUMBUS, OHIO 43218-3990		A	16

## A.3 REQUIREMENTS

- A.3.1 <u>Item requirements</u>. The individual item requirements for device classes Q and V shall be in accordance with MIL-PRF-38535 and as specified herein or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.
- A.3.2 <u>Design, construction and physical dimensions</u>. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein and the manufacturer's QM plan for device classes Q and V.
  - A.3.2.1 <u>Die physical dimensions</u>. The die physical dimensions shall be as specified in A.1.2.4.1 and on figure A-1.
- A.3.2.2 <u>Die bonding pad locations and electrical functions</u>. The die bonding pad locations and electrical functions shall be as specified in A.1.2.4.2 and on figure A-1.
  - A.3.2.3 Interface materials. The interface materials for the die shall be as specified in A.1.2.4.3 and on figure A-1.
  - A.3.2.4 <u>Assembly related information</u>. The assembly related information shall be as specified in A.1.2.4.4 and on figure A-1.
  - A.3.2.5 Radiation exposure circuit. The radiation exposure circuit shall be as defined in paragraph 3.2.4 herein.
- A.3.3 <u>Electrical performance characteristics and post-irradiation parameter limits</u>. Unless otherwise specified herein, the electrical performance characteristics and post-irradiation parameter limits are as specified in table I of the body of this document.
- A.3.4 <u>Electrical test requirements</u>. The wafer probe test requirements shall include functional and parametric testing sufficient to make the packaged die capable of meeting the electrical performance requirements in table I.
- A.3.5 <u>Marking</u>. As a minimum, each unique lot of die, loaded in single or multiple stack of carriers, for shipment to a customer, shall be identified with the wafer lot number, the certification mark, the manufacturer's identification and the PIN listed in A.1.2 herein. The certification mark shall be a "QML" or "Q" as required by MIL-PRF-38535.
- A.3.6 <u>Certification of compliance</u>. For device classes Q and V, a certificate of compliance shall be required from a QML-38535 listed manufacturer in order to supply to the requirements of this drawing (see A.6.4 herein). The certificate of compliance submitted to DLA Land and Maritime -VA prior to listing as an approved source of supply for this appendix shall affirm that the manufacturer's product meets, for device classes Q and V, the requirements of MIL-PRF-38535 and the requirements herein.
- A.3.7 <u>Certificate of conformance</u>. A certificate of conformance as required for device classes Q and V in MIL-PRF-38535 shall be provided with each lot of microcircuit die delivered to this drawing.

## A.4 VERIFICATION

- A.4.1 <u>Sampling and inspection</u>. For device classes Q and V, die sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modifications in the QM plan shall not affect the form, fit, or function as described herein.
- A.4.2 <u>Screening</u>. For device classes Q and V, screening shall be in accordance with MIL-PRF-38535, and as defined in the manufacturer's QM plan. As a minimum, it shall consist of:
  - a. Wafer lot acceptance for class V product using the criteria defined in MIL-STD-883, method 5007.
  - b. 100% wafer probe (see paragraph A.3.4 herein).
  - c. 100% internal visual inspection to the applicable class Q or V criteria defined in MIL-STD-883, method 2010 or the alternate procedures allowed in MIL-STD-883, method 5004.

STANDARD MICROCIRCUIT DRAWING	SIZE <b>A</b>		5962-07232
DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990		REVISION LEVEL A	SHEET 17

## A.4.3 Conformance inspection.

A.4.3.1 <u>Group E inspection</u>. Group E inspection is required only for parts intended to be identified as radiation assured (see A.3.5 herein). RHA levels for device classes Q and V shall be as specified in MIL-PRF-38535. End point electrical testing of packaged die shall be as specified in table IIA herein. Group E tests and conditions are as specified in paragraphs 4.4.4, 4.4.4.1, and 4.4.4.1.1 herein.

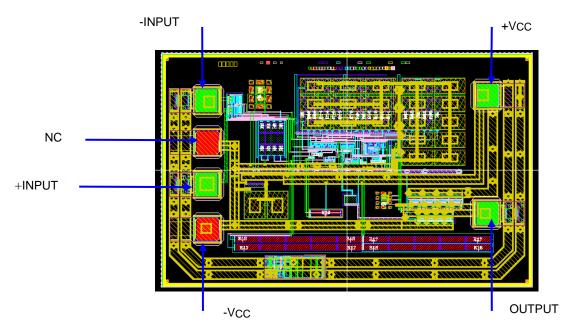
## A.5 DIE CARRIER

A.5.1 <u>Die carrier requirements</u>. The requirements for the die carrier shall be accordance with the manufacturer's QM plan or as specified in the purchase order by the acquiring activity. The die carrier shall provide adequate physical, mechanical and electrostatic protection.

#### A.6 NOTES

- A.6.1 <u>Intended use</u>. Microcircuit die conforming to this drawing are intended for use in microcircuits built in accordance with MIL-PRF-38535 or MIL-PRF-38534 for government microcircuit applications (original equipment), design applications, and logistics purposes.
- A.6.2 <u>Comments</u>. Comments on this appendix should be directed to DLA Land and Maritime -VA, Columbus, Ohio, 43218-3990 or telephone (614) 692-0540.
- A.6.3 <u>Abbreviations, symbols, and definitions</u>. The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535 and MIL-HDBK-1331.
- A.6.4 <u>Sources of supply for device classes Q and V</u>. Sources of supply for device classes Q and V are listed in QML-38535. The vendors listed within QML-38535 have submitted a certificate of compliance (see A.3.6 herein) to DLA Land and Maritime VA and have agreed to this drawing.

STANDARD MICROCIRCUIT DRAWING DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>		5962-07232
		REVISION LEVEL A	SHEET 18



Die bonding pad locations and electrical functions

Die physical dimensions.

Die size: 1350  $\mu$ m x 890  $\mu$ m Die thickness: 375  $\mu$ m ± 25  $\mu$ m

Pad size: 80 μm x 80 μm

Pad placements

 $\begin{array}{lll} + VCC & X = 470 \; \mu m - Y = 249.6 \; \mu m \\ OUTPUT & X = 467.3 \; \mu m - Y = -144.65 \; \mu m \\ - VCC & X = -467.3 \; \mu m - Y = -198.1 \; \mu m \\ + INPUT & X = -467.3 \; \mu m - Y = -48.1 \; \mu m \\ - INPUT & X = -467.3 \; \mu m - Y = 232.8 \; \mu m \\ NC & X = -467.3 \; \mu m - Y = 92.8 \; \mu m \end{array}$ 

Interface materials.

Top metallization: Metal 1 W = 600 nm ( $\pm$  60 nm)

Metal 2 Al-Cu = 608 nm ( $\pm$  60 nm) Metal 3 Al-Cu = 608 nm ( $\pm$  60 nm) Metal 4 Al-Cu = 608 nm ( $\pm$  60 nm) Metal thick Al-Cu = 2.5  $\mu$ m ( $\pm$  0.255  $\mu$ m)

Backside metallization: bare silicon

Glassivation.

 $PSG = 0.5 \ \mu m$   $Si3N4 = 0.6 \ \mu m$ 

Substrate: Silicon

FIGURE A-1. Die bonding pad locations and electrical functions.

STANDARD MICROCIRCUIT DRAWING	SIZE <b>A</b>		5962-07232
DLA LAND AND MARITIME		REVISION LEVEL	SHEET
COLUMBUS, OHIO 43218-3990		A	19

#### STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 16-10-12

Approved sources of supply for SMD 5962-07232 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38535 during the next revision. MIL-HDBK-103 and QML-38535 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DLA Land and Maritime-VA. This information bulletin is superseded by the next dated revision of MIL-HDBK-103 and QML-38535. DLA Land and Maritime maintains an online database of all current sources of supply at <a href="https://landandmaritimeapps.dla.mil/programs/smcr/">https://landandmaritimeapps.dla.mil/programs/smcr/</a>.

Standard	Vendor	Vendor
microcircuit drawing	CAGE	similar
PIN <u>1</u> /	number	PIN <u>2</u> /
5962F0723201VXC	F8859	RHF350K-01V
5962F0723201V9A	F8859	RHF350DIE2V
5962F0723202VYC	F8859	RHF350AK01V

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.
- <u>2</u>/ <u>Caution</u>. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGEVendor namenumberand address

F8859 ST Microelectronics 3 rue de Suisse

CS 60816

35208 RENNES Cedex 2-FRANCE

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in the information bulletin.