# MBRA160T3

# Surface Mount **Schottky Power Rectifier**

# **SMA Power Surface Mount Package**

This device employs the Schottky Barrier principle in a large area metal-to-silicon power diode. State of the art geometry features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency rectification, or as free wheeling and polarity diodes in surface mount applications where compact size and weight are critical to the system.

#### **Features**

- Small Compact Surface Mountable Package with J-Bent Leads
- Rectangular Package for Automated Handling
- Highly Stable Oxide Passivated Junction
- Very Low Forward Voltage Drop
- Guard-ring for Stress Protection
- Pb-Free Package is Available

#### **Mechanical Characteristics:**

- Case: Epoxy, Molded
- Weight: 70 mg (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Shipped in 12 mm tape, 5000 units per 13 inch reel
- Polarity: Cathode Lead Indicated by Polarity Band
- ESD Ratings: Machine Model = C Human Body Model = 3B



## **ON Semiconductor®**

http://onsemi.com

### SCHOTTKY BARRIER RECTIFIER **1.0 AMPERES** 60 VOLTS



SMA CASE 403D PLASTIC

#### **MARKING DIAGRAM**



= Specific Device Code B16 А

- = Assembly Location
- = Year

ww = Work Week

= Pb-Free Package

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MBRA160T3	SMA	5000/Tape & Reel
MBRA160T3G	SMA (Pb-Free)	5000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	60	V
Average Rectified Forward Current (At Rated V <sub>R</sub> , T <sub>C</sub> = 105°C)	Ι <sub>Ο</sub>	1.0	A
Average Rectified Forward Current (At Rated $V_R$ , $T_C = 70^{\circ}C$ )	IO	2.1	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I <sub>FSM</sub>	60	A
Storage/Operating Case Temperature	T <sub>stg</sub> , T <sub>C</sub>	-55 to +150	°C
Operating Junction Temperature (Note 1)	TJ	-55 to +150	°C
Voltage Rate of Change (Rated V <sub>R</sub> , T <sub>J</sub> = 25°C)	dv/dt	10,000	V/μs

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. The heat generated must be less than the thermal conductivity from Junction-to-Ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ .

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction-to-Lead (Note 2)	$R_{ heta JL}$	35	°C/W
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	86	

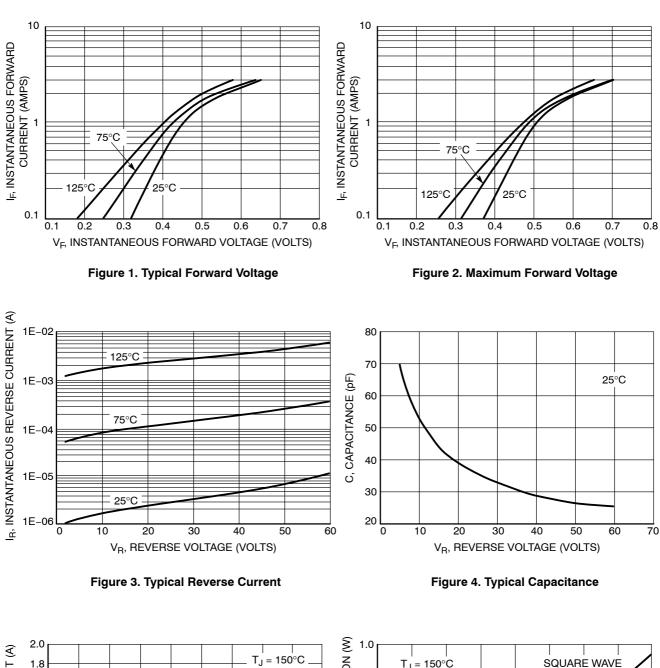
#### **ELECTRICAL CHARACTERISTICS**

Maximum Instantaneous Forward Voltage (Note 3)		V <sub>F</sub>	T <sub>J</sub> = 25°C	T <sub>J</sub> = 125°C	V
	(I <sub>F</sub> = 1.0 A)		0.510	0.475	
Maximum Instantaneous Reverse Current		I <sub>R</sub>	T <sub>J</sub> = 25°C	T <sub>J</sub> = 125°C	mA
	(V <sub>R</sub> = 60 V)		0.2	20	

2. Mounted on 2" Square PC Board with 1" Square Total Pad Size, PC Board FR4.

3. Pulse Test: Pulse Width  $\leq$  250  $\mu$ s, Duty Cycle  $\leq$  2.0%.

## MBRA160T3



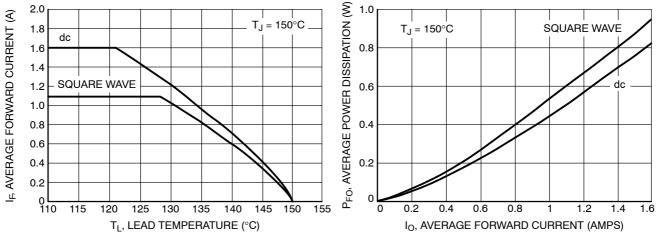


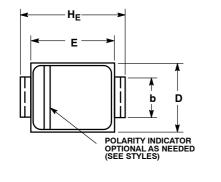
Figure 5. Current Derating – Junction-to-Lead

Figure 6. Forward Power Dissipation

### **MBRA160T3**

#### PACKAGE DIMENSIONS

SMA CASE 403D-02 ISSUE F



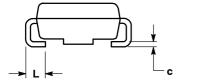
NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

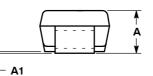
2. CONTROLLING DIMENSION: INCH.

STYLE 1: PIN 1. CATHODE (POLARITY BAND)

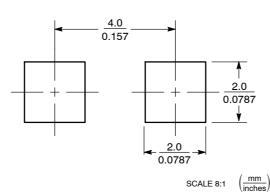
3. 403D-01 OBSOLETE, NEW STANDARD IS 403D-02.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	1.97	2.10	2.20	0.078	0.083	0.087
A1	0.05	0.10	0.15	0.002	0.004	0.006
b	1.27	1.45	1.63	0.050	0.057	0.064
с	0.15	0.28	0.41	0.006	0.011	0.016
D	2.29	2.60	2.92	0.090	0.103	0.115
E	4.06	4.32	4.57	0.160	0.170	0.180
HE	4.83	5.21	5.59	0.190	0.205	0.220
L	0.76	1.14	1.52	0.030	0.045	0.060





# SOLDERING FOOTPRINT\*



\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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