Product Preview

Power MOSFET

30 V, 21 m Ω , 6.9 A, Single N–Channel, 1.6x1.6x0.55 mm μ Cool UDFN6 Package

Features

- UDFN Package with Exposed Drain Pads for Excellent Thermal Conduction
- Low Profile UDFN 1.6 x 1.6 x 0.55 mm for Board Space Saving
- Ultra Low R_{DS(on)}
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- Power Load Switch
- Wireless Charging
- DC-DC Converters
- Motor Drive

MAXIMUM RATINGS ($T_J = 25^{\circ}C$ unless otherwise stated)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V_{DSS}	30	V
Gate-to-Source Volt	age		V_{GS}	±20	V
Continuous Drain		T _A = 25°C	I _D	6.9	Α
Current R _{0JA} (Note 1, 3)	Steady	T _A = 85°C		5.0	
Power Dissipation R _{0JA} (Note 1, 3)			P _D	1.49	W
Continuous Drain		T _A = 25°C	I _D	4.5	Α
Current R _{0JA} (Note 2, 3)	Steady	T _A = 85°C		3.2	
Power Dissipation R _{0JA} (Note 2, 3)	State	T _A = 25°C	P _D	0.64	W
Pulsed Drain Current $t_p = 10 \mu s$		I _{DM}	20	Α	
Operating Junction and Storage Temperature			T _J , T _{STG}	-55 to 150	°C
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			T _L	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. Surface-mounted on FR4 board using 1 in² pad size, 2 oz Cu pad.
- 2. Surface-mounted on FR4 board using the min pad size, 2 oz Cu pad.
- 3. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions

This document contains information on a product under development. ON Semiconductor reserves the right to change or discontinue this product without notice.

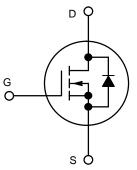


ON Semiconductor®

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	MOSFEI	
V _{(BR)DSS}	R _{DS(on)} MAX	I _D MAX
20.1/	21 mΩ @ 10 V	604

30 mΩ @ 4.5 V



N-CHANNEL MOSFET

S D

UDFN6 (μCOOL) CASE 517AU



MARKING DIAGRAM

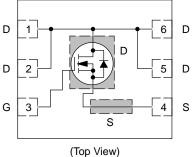
XX = Specific Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

PIN CONNECTIONS



(TOP VICW)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

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THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Ambient – Steady State (Note 1, 3)	$R_{\theta JA}$	83.7	°C/W
Junction-to-Ambient – Steady State min Pad (Note 2, 3)	$R_{\theta JA}$	196.6	-C/VV

ELECTRICAL CHARACTERISTICS (T_{.1} = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Units
OFF CHARACTERISTICS		•		•	•		
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 V$,	I _D = 250 μA	30			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	I _D = 250 μA	$I_D = 250 \mu\text{A}$, ref to 25°C		TBD		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$,	T _J = 25°C			1.0	μΑ
		V _{DS} = 24 V	T _J = 125°C			10	
Gate-to-Source Leakage Current	I _{GSS}	$V_{DS} = 0 V$	V _{GS} = ±20 V			±100	nA
ON CHARACTERISTICS (Note 4)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}$, I _D = 250 μA	1.2		2.2	V
Negative Threshold Temp. Coefficient	V _{GS(TH)} /T _J	I _D = 250 μA	A, ref to 25°C		TBD		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 10	V, I _D = 6.0 A		16.8	21	mΩ
		V _{GS} = 4.5	V, I _D = 5.0 A		24	30	
Forward Transconductance	9FS	V _{DS} = 1.5	V, I _D = 5.0 A		TBD		S
CHARGES & CAPACITANCES		•		•	•		•
Input Capacitance	C _{ISS}				393		pF
Output Capacitance	C _{OSS}	$V_{GS} = 0 V_{GS}$	$V_{GS} = 0 \text{ V, } f = 1 \text{ MHz,}$		218		
Reverse Transfer Capacitance	C _{RSS}	V _{DS} = 15 V			10		
Total Gate Charge	Q _{G(TOT)}				2.8		nC
Threshold Gate Charge	Q _{G(TH)}	$V_{GS} = 4.5 \text{ V}, V_{DS} = 15 \text{ V};$ $I_{D} = 5.0 \text{ A}$			TBD		
Gate-to-Source Charge	Q _{GS}				TBD		
Gate-to-Drain Charge	Q _{GD}				TBD		
Total Gate Charge	Q _{G(TOT)}				TBD		nC
SWITCHING CHARACTERISTICS, VG		L		1	<u>. </u>		
Turn-On Delay Time	t _{d(ON)}				TBD		ns
Rise Time	t _r	V45V	\/ _ 1 5 \/		TBD		
Turn-Off Delay Time	t _{d(OFF)}	$I_{D} = 5.0 \text{ A}$	$V_{GS} = 4.5 \text{ V}, V_{DD} = 15 \text{ V}, I_{D} = 5.0 \text{ A}, R_{G} = 6 \Omega$		TBD		
Fall Time	t _f				TBD		
SWITCHING CHARACTERISTICS, VG	· · · · · · · · · · · · · · · · · · ·	1		1	1		I
Turn-On Delay Time	t _{d(ON)}				TBD		ns
Rise Time	t _r	· · · · · · · · · · · · · · · · · · ·	V 40.V.V 45.V		TBD		
Turn-Off Delay Time	t _{d(OFF)}	$V_{GS} = 10 \text{ V}, V_{DD} = 15 \text{ V},$ $I_{D} = 5.0 \text{ A}, R_{G} = 6 \Omega$		 	TBD		
Fall Time	t _f			 	TBD		
DRAIN-SOURCE DIODE CHARACTER	<u>-</u>	l		1	1		l .
Forward Diode Voltage	V _{SD}	$V_{GS} = 0 \text{ V},$ $T_{J} = 25^{\circ}\text{C}$ $I_{S} = 5.0 \text{ A}$ $T_{J} = 125^{\circ}\text{C}$		1	0.8	1.1	V
=	- 30			1	0.0		I ,

^{4.} Pulse Test: pulse width \leq 300 μ s, duty cycle \leq 2%. 5. Switching characteristics are independent of operating junction temperatures.

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Units
DRAIN-SOURCE DIODE CHARACTERISTICS						
Reverse Recovery Time	t _{RR}			TBD		ns
Charge Time	t _a	$V_{GS} = 0 \text{ V, dIs/dt} = 100 \text{ A/us,}$		TBD		
Discharge Time	t _b	$V_{GS} = 0 \text{ V, dls/dt} = 100 \text{ A/}\mu\text{s,}$ $I_{S} = 5.0 \text{ A}$		TBD		
Reverse Recovery Charge	Q _{RR}			TBD		nC

DEVICE ORDERING INFORMATION

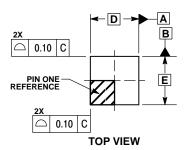
Device	Package	Shipping [†]
NTLUS030N03CTAG	UDFN6 (Pb-Free)	3000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{4.} Pulse Test: pulse width ≤ 300 μs, duty cycle ≤ 2%.
5. Switching characteristics are independent of operating junction temperatures.

PACKAGE DIMENSIONS

UDFN6 1.6x1.6, 0.5P CASE 517AU **ISSUE O**



DETAIL B

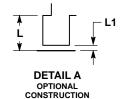
SIDE VIEW

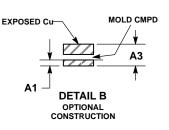
0.05 C

0.05 C

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NOTE 4

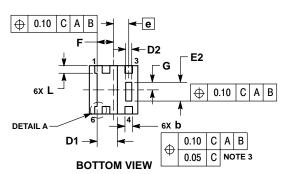




NOTES

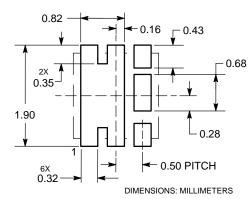
- 1. DIMENSIONING AND TOLERANCING PER
- ASME Y14.5M, 1994.
 CONTROLLING DIMENSION: MILLIMETERS.
 DIMENSION b APPLIES TO PLATED TERMINAL
 AND IS MEASURED BETWEEN 0.15 AND
- 0.30 mm FROM TERMINAL. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

	MILLIMETERS			
DIM	MIN	MAX		
Α	0.45	0.55		
A1	0.00	0.05		
А3	0.13	REF		
b	0.20	0.30		
D	1.60	BSC		
Е	1.60 BSC			
е	0.50	BSC		
D1	0.62	0.72		
D2	0.15	0.25		
E2	0.57	0.67		
F	0.55 BSC			
G	0.25 BSC			
L	0.20	0.30		
L1		0.15		



SEATING PLANE

SOLDERMASK DEFINED MOUNTING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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