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November 2013

FQAF13N80 — N-Channel QFET<sup>®</sup> MOSFET

## FQAF13N80

### N-Channel QFET<sup>®</sup> MOSFET

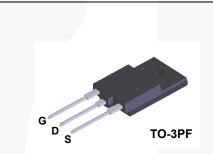
 $800~V,\,8.0~A,\,750~m\Omega$ 

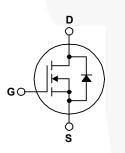
### Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

#### Features

- 8.0 A, 800 V,  $R_{DS(on)}$  = 750 m $\Omega$  (Max.) @  $V_{GS}$  = 10 V,  $I_{D}$  = 4.0 A
- Low Gate Charge (Typ. 68 nC)
- Low Crss (Typ. 30 pF)
- 100% Avalanche Tested





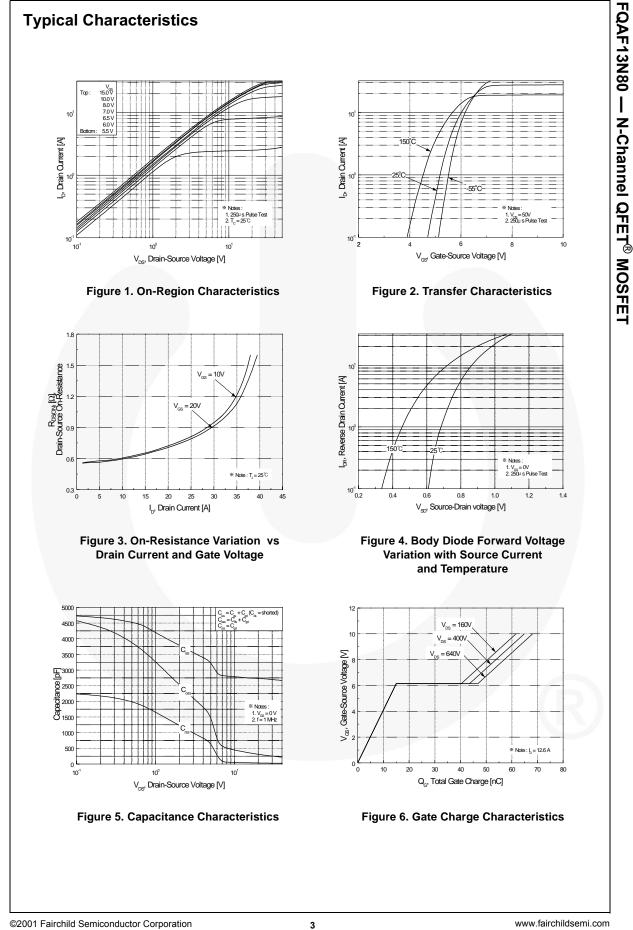
#### Absolute Maximum Ratings T<sub>c</sub> = 25°C unless otherwise noted.

Symbol	Parameter		FQAF13N80	Unit	
V <sub>DSS</sub>	Drain-Source Voltage		800	V	
I <sub>D</sub>	Drain Current - Continuous ( $T_C = 25^\circ$	C)	8.0	А	
	- Continuous (T <sub>C</sub> = 100	5.1	A		
DМ	Drain Current - Pulsed	(Note 1)	32	A	
V <sub>GSS</sub>	Gate-Source Voltage		± 30	V	
E <sub>AS</sub>	Single Pulsed Avalanche Energy	(Note 2)	1100	mJ	
AR	Avalanche Current	(Note 1)	8.0	Α	
E <sub>AR</sub>	Repetitive Avalanche Energy	(Note 1)	12	mJ	
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.0	V/ns	
PD	Power Dissipation ( $T_C = 25^{\circ}C$ )	120	W		
	- Derate above 25°C	0.96	W/°C		
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +150	°C	
TL	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300	°C	

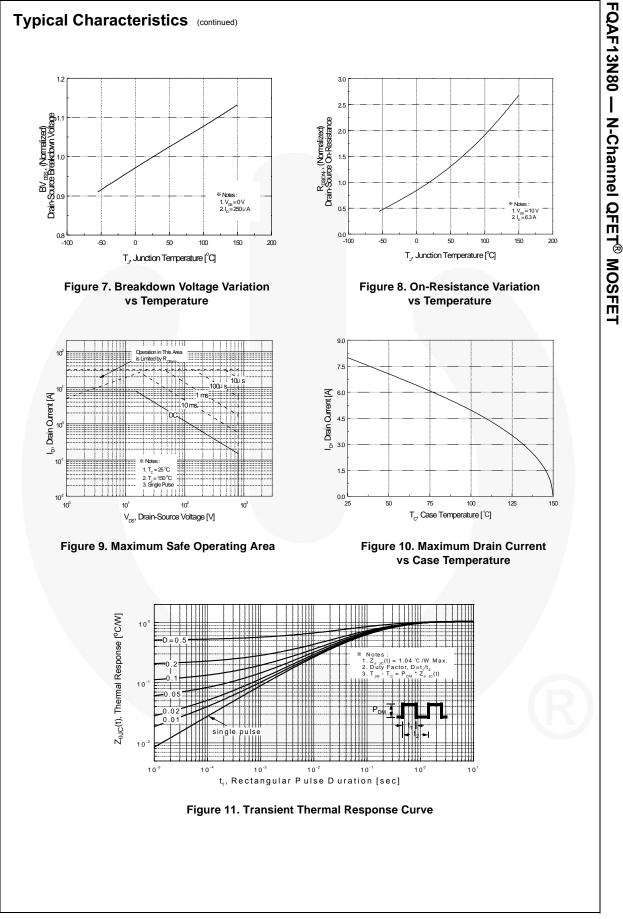
### **Thermal Characteristics**

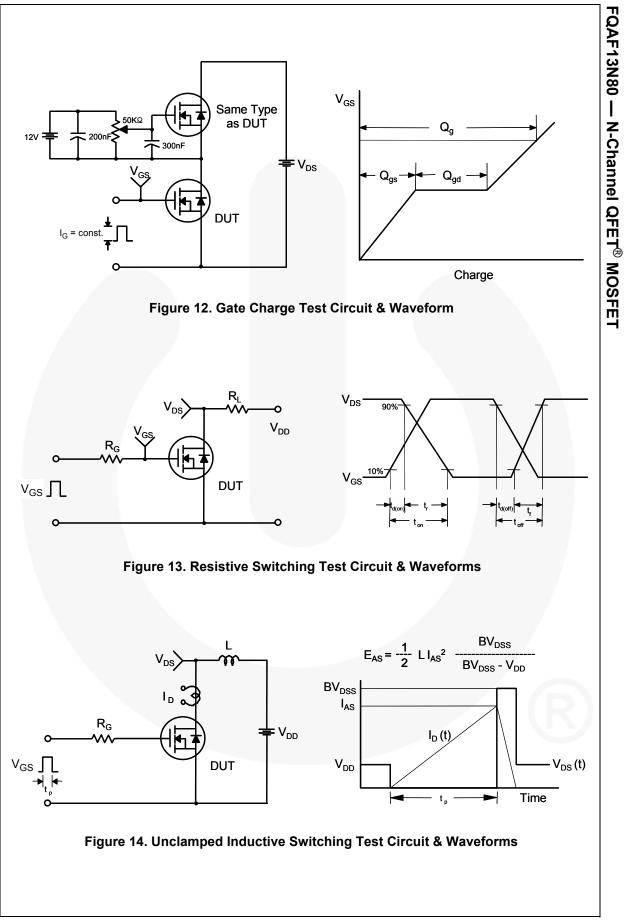
Symbol	Parameter	FQAF13N80	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	1.04	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	40	°C/W	

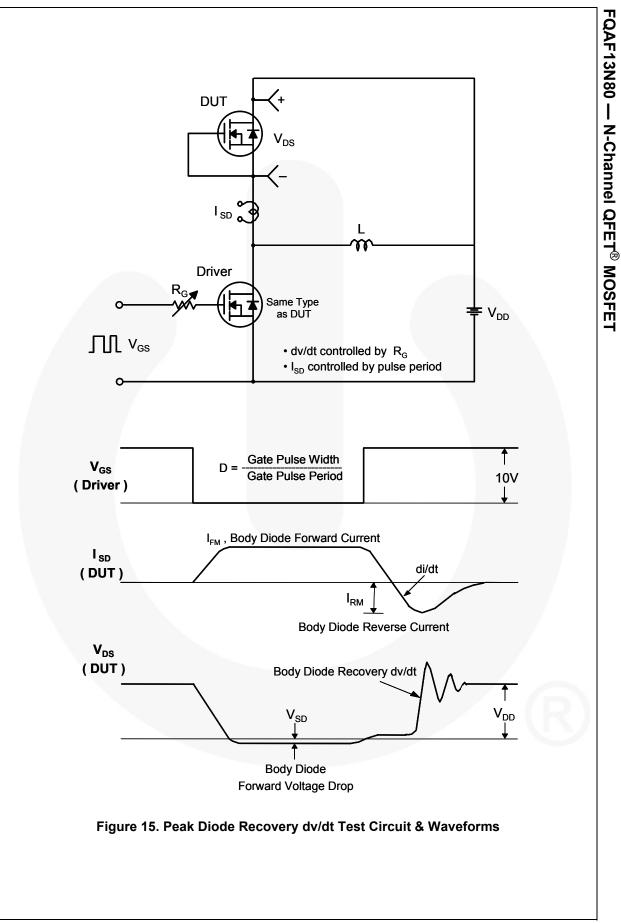
Part Number		Top Mark	Packa	age	Packing Method	Reel	Size	Tape W	'idth	Quantity
FQAF1	3N80	FQAF13N80	TO-3	TO-3PF Tube		N/.	A	N/A		30 units
lectri	cal Cł	aracteristics <b>⊤</b>	c = 25°C unle	ess otherwis	e noted.					
Symbol		Parameter		Test Conditions		Min	Тур	Max	Unit	
	vractori	stics								
BV <sub>DSS</sub>	racteristics Drain-Source Breakdown Voltage		$V_{GS} = 0$	V, I <sub>D</sub> = 250 μA		800			V	
ΔBV <sub>DSS</sub>	Breakdown Voltage Temperature					000				
/ ΔT <sub>J</sub>	Coefficient		$I_D = 250 \ \mu$ A, Referenced to 25°C				0.95		V/°C	
I <sub>DSS</sub>	7			V <sub>DS</sub> = 800 V, V <sub>GS</sub> = 0 V				10	μA	
	Zero Gate Voltage Drain Current		ent	V <sub>DS</sub> = 640 V, T <sub>C</sub> = 125°C					100	μA
I <sub>GSSF</sub>	Gate-Bo	ody Leakage Current, F	orward		0 V, V <sub>DS</sub> = 0 V				100	nA
GSSR	Gate-Body Leakage Current, Reverse			$V_{GS} = -30 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$					-100	nA
On Cha	racteri	stics								
V <sub>GS(th)</sub>	Gate Th	reshold Voltage	1	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$			3.0		5.0	V
R <sub>DS(on)</sub>		tatic Drain-Source In-Resistance		$V_{GS} = 10 \text{ V}, I_D = 4.0 \text{ A}$				0.58	0.75	Ω
9 <sub>FS</sub>	Forward	orward Transconductance		V <sub>DS</sub> = 50 V, I <sub>D</sub> = 4.0 A				10.5		S
<b>Dynam</b> C <sub>iss</sub>		acteristics apacitance	-	V <sub>DS</sub> = 2	5 V, V <sub>GS</sub> = 0 V,			2700	3500	pF
C <sub>oss</sub>	Output	Capacitance		f = 1.0 MHz				275	360	pF
C <sub>rss</sub>	Reverse	e Transfer Capacitance	•					30	39	pF
Switchi	ing Cha	racteristics								
t <sub>d(on)</sub>	-	n Delay Time		V – 4	0 \/   _ 12 6 A			60	130	ns
t <sub>r</sub>	Turn-Or	n Rise Time		$V_{DD} = 400 \text{ V}, \text{ I}_{\text{D}} = 12.6 \text{ A},$ $\text{R}_{\text{G}} = 25 \ \Omega$				150	310	ns
t <sub>d(off)</sub>	Turn-Of	f Delay Time						155	320	ns
<sup>t</sup> f	Turn-Of	f Fall Time				(Note 4)		110	230	ns
Qg	Total Ga	ate Charge		$V_{DS} = 6$				68	88	nC
Q <sub>gs</sub>	Gate-Se	ource Charge		$V_{GS} = 1$				15		nC
Q <sub>gd</sub>	Gate-D	rain Charge				(Note 4)		32		nC
Drain S		Diode Characteris	stice on	d Mavi	mum Potings					
l <sub>s</sub>	1	m Continuous Drain-S							8.0	A
		m Pulsed Drain-Source							36	A
		ource Diode Forward V		$V_{GS} = 0 \text{ V}, \text{ I}_{S} = 8.0 \text{ A}$				1.4	V	
SM					V, I <sub>S</sub> = 12.6 A,			850		ns
<sup>I</sup> sм V <sub>SD</sub>		e Recoverv Time		$dI_{\rm E} / dt = 100 \text{ A/us}$				1		
I <sub>SM</sub> V <sub>SD</sub> t <sub>rr</sub>	Reverse	e Recovery Time e Recovery Charge			= 100 A/us			11.3		μC

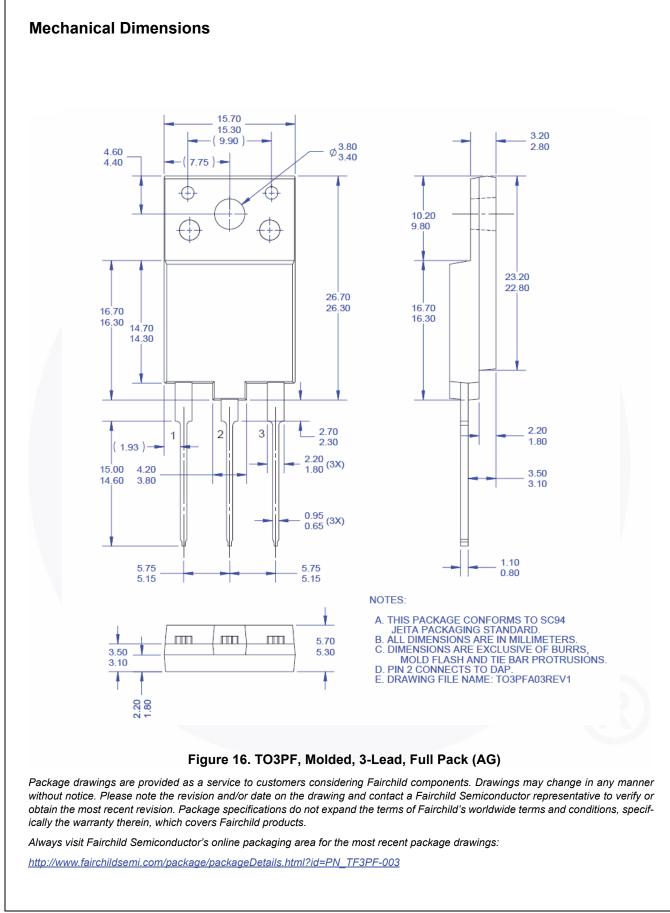


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FQAF13N80 — N-Channel QFET<sup>®</sup> MOSFET



Obsolete

Not In Production

Datasheet contains specifications on a product that is discontinued by Fairchild

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Rev. 166

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