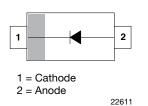
## 1N4148WS-V

www.vishay.com

**Vishay Semiconductors** 

## **Small Signal Fast Switching Diode**





22610

**MARKING** (example only)



Bar = cathode marking XY = type code

#### **MECHANICAL DATA**

Case: SOD-323

Weight: approx. 4.3 mg

#### Packaging codes/options:

GS18/10K per 13" reel (8 mm tape), 10K/box GS08/3K per 7" reel (8 mm tape), 15K/box

5		
-	1	
	5	50

· Fast switching diodes

Silicon epitaxial planar diode

AEC-Q101 gualified

**FEATURES** 

· Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

· These diodes are also available in other case styles including the DO-35 case with the type designation 1N4148, the MiniMELF case with the type designation LL4148, and the SOT-23

case with the type designation IMBD4148-V

PARTS TABL	E			
PART	ORDERING CODE	TYPE MARKING	INTERNAL CONSTRUCTION	REMARKS
1N4148WS-V	1N4148WS-V-GS18 or 1N4148WS-V-GS08	A2	Single diode	Tape and reel

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Reverse voltage		V <sub>R</sub>	75	N		
Repetitive peak reverse voltage		V <sub>RRM</sub>	100	V		
Average rectified current half wave rectification with resistive load <sup>(1)</sup>	f ≥ 50 Hz	I <sub>F(AV)</sub>	150	mA		
Surge forward current	$t < 1 s and T_j = 25 °C$	I <sub>FSM</sub>	350			
Power dissipation <sup>(1)</sup>		P <sub>tot</sub>	200	mW		

#### Note

<sup>(1)</sup> Valid provided that electrodes are kept at ambient temperature.

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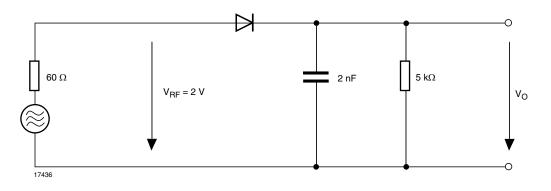
<b>THERMAL CHARACTERISTICS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air <sup>(1)</sup>		R <sub>thJA</sub>	650	K/W	
Junction temperature		Tj	150		
Operating temperature range		Tj	- 55 to + 150	°C	
Storage temperature range		T <sub>stg</sub>	- 65 to + 150		

Note

<sup>(1)</sup> Valid provided that electrodes are kept at ambient temperature

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>			1000	mV
	I <sub>F</sub> = 100 mA	V <sub>F</sub>			1200	
Leakage current	V <sub>R</sub> = 20 V	I <sub>R</sub>			25	nA
	V <sub>R</sub> = 75 V	I <sub>R</sub>			5	μΑ
	V <sub>R</sub> = 100 V	I <sub>R</sub>			100	
	$V_R = 20 \text{ V}, \text{ T}_j = 150 ^\circ\text{C}$	I <sub>R</sub>			50	
Diode capacitance	$V_F = V_R = 0 V$	CD			4	pF
Voltage rise when switching ON	Tested with 50 mA pulses, $t_p = 0.1 \ \mu s$ , rise time < 30 ns, $f_p = (5 \text{ to } 100) \text{ kHz}$	V <sub>fr</sub>			2.5	v
Reverse recovery time	$I_F = 10 \text{ mA}, i_R = 1 \text{ mA}, V_R = 6 \text{ V}, \\ R_L = 100 \ \Omega$	t <sub>rr</sub>			4	ns
Rectification efficiency	$f = 100 \text{ MHz}, V_{RF} = 2 \text{ V}$	ην	0.45			

#### **RECTIFICATION EFFICIENCY MEASUREMENT CIRCUIT**





### **Vishay Semiconductors**

### **TYPICAL CHARACTERISTICS** ( $T_{amb} = 25$ °C, unless otherwise specified)

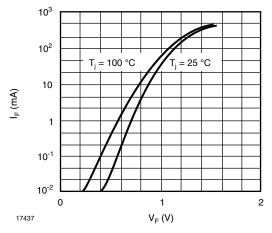


Fig. 1 - Forward Characteristics

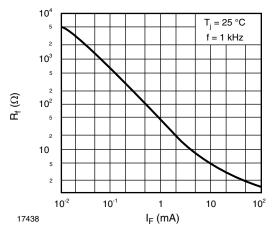


Fig. 2 - Dynamic Forward Resistance vs. Forward Current

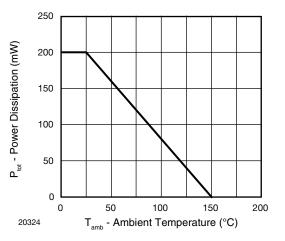


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

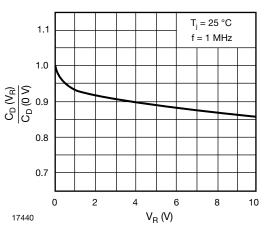


Fig. 4 - Relative Capacitance vs. Reverse Voltage

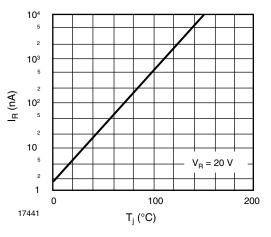
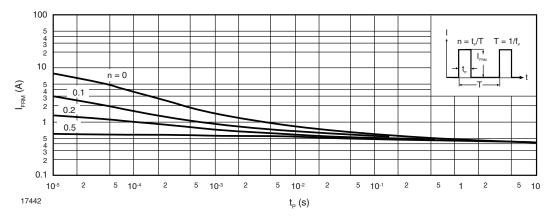


Fig. 5 - Leakage Current vs. Junction Temperature

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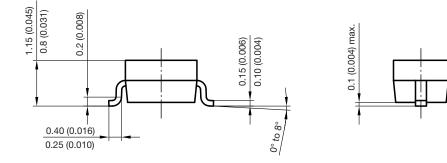


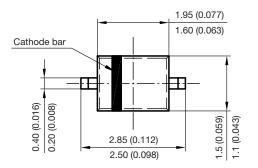




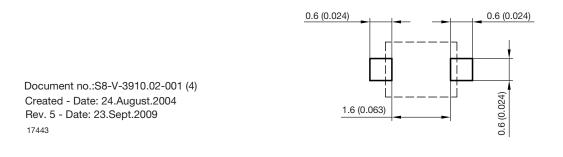
#### PACKAGE DIMENSIONS in millimeters (inches): SOD-323

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Foot print recommendation:



Rev. 1.9, 23-Mar-12

4

Document Number: 85751

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