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## FQP3N80C / FQPF3N80C N-Channel QFET<sup>®</sup> MOSFET 800 V, 3.0 A, 4.8 Ω

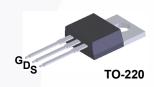
#### Features

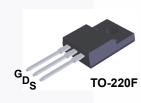
- 3.0 A, 800 V,  ${\rm R}_{\rm DS(on)}$  = 4.8  $\Omega$  (Max.) @ V\_{\rm GS} = 10 V,  ${\rm I}_{\rm D}$  = 1.5 A
- Low Gate Charge (Typ. 13 nC)
- Low Crss (Typ. 5.5 pF)
- 100% Avalanche Tested

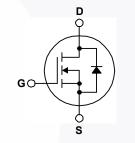
#### June 2014

### Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.







#### MOSFET Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

Symbol		Parameter	FQP3N80C	FQPF3N80C	Unit	
V <sub>DSS</sub>	Drain to Source Voltage	9		8	V	
I <sub>D</sub>	Ducin Current	-Continuous (T <sub>C</sub> = 25 <sup>o</sup> C)	-Continuous (T <sub>C</sub> = 25 <sup>o</sup> C) -Continuous (T <sub>C</sub> = 100 <sup>o</sup> C)			А
	Drain Current	-Continuous (T <sub>C</sub> = 100 <sup>o</sup> C)				Α
I <sub>DM</sub>	Drain Current	- Pulsed	(Note 1)	12	12 *	Α
V <sub>GSS</sub>	Gate to Source Voltage			±	V	
E <sub>AS</sub>	Single Pulsed Avalanche Energy			3	mJ	
I <sub>AR</sub>	Avalanche Current				А	
E <sub>AR</sub>	Repetitive Avalanche Energy		(Note 1)	1(	mJ	
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	4	V/ns	
P <sub>D</sub>	Dower Dissinction	(T <sub>C</sub> = 25°C)	$(T_{\rm C} = 25^{\rm o}{\rm C})$		39	W
	Power Dissipation	- Derate above 25°C	- Derate above 25ºC		0.31	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range			-55 to	°C	
TL	Maximum Lead Tempe 1/8" from Case for 5 Se	3	°C			
*Drain current l	limited by maximum junction					

#### **Thermal Characteristics**

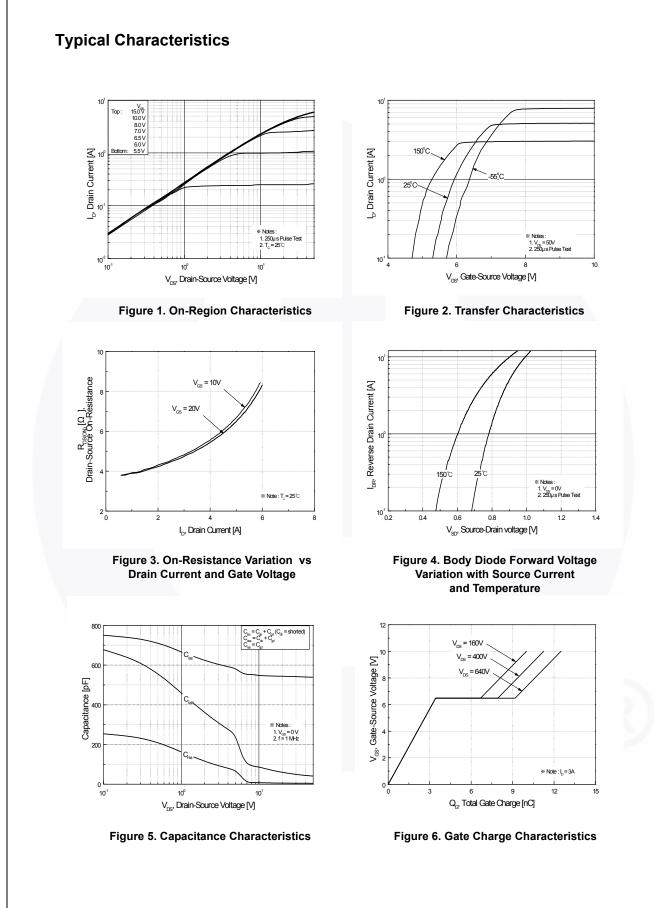
Symbol	Parameter	FQP3N80C	FQPF3N80C	Unit	
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case, Max	1.17	3.2	°C/W	
$R_{ ext{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max	62.5	62.5	°C/W	

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FQP3N80C / FQPF3N80C — N-Channel QFET<sup>®</sup> MOSFET

Part NumberTop MarkFQP3N80CFQP3N80CFQPF3N80CFQPF3N80C		Top Mark	Pac	kage	Packing Method	Ree	l Size	Tape Width	ı Qı	uantity	
		FQP3N80C	TO	-220	Tube	Τι	ıbe	N/A	50	50 units	
		TO-	TO-220F Tube T		ıbe	N/A	50	50 units			
lectric	cal Char	acteristics <b>⊤</b> <sub>c</sub> =	25°C ur	nless oth	erwise noted.						
Symbol		Parameter		6	Test Conditions		Min	Тур	Мах	Unit	
Off Cha	racteristi	cs									
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage		V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA			800			V		
ΔBV <sub>DSS</sub>	Breakdown Voltage Temperature Coefficient Zero Gate Voltage Drain Current		$I_{D} = 250 \ \mu\text{A}, \text{Referenced to } 25^{\circ}\text{C}$ $V_{DS} = 800 \ \text{V}, \ V_{GS} = 0 \ \text{V}$ $V_{DS} = 640 \ \text{V}, \ T_{C} = 125^{\circ}\text{C}$				1		V/°C		
DSS								10	μA		
033						-		100	μA		
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward		$V_{GS}$ = 30 V, $V_{DS}$ = 0 V			1		100	nA		
GSSR	Gate-Body Leakage Current, Reverse		$V_{GS}$ = -30 V, $V_{DS}$ = 0 V					-100	nA		
On Cha	racteristi	cs									
V <sub>GS(th)</sub>	Gate Threshold Voltage		$V_{DS}$ = $V_{GS}$ , $I_D$ = 250 $\mu$ A			3.0		5.0	V		
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance		$V_{GS}$ = 10 V, I <sub>D</sub> = 1.5 A				4.0	4.8	Ω		
9 <sub>FS</sub>	Forward Tra	Forward Transconductance		V <sub>DS</sub> = 50 V, I <sub>D</sub> = 1.5 A				3		S	
	ic Charact			Ĩ							
C <sub>iss</sub>	Input Capa		_	$V_{DS}$ = 25 V, $V_{GS}$ = 0 V,			543	705	pF		
C <sub>oss</sub>	Output Cap			f = 1.0	MHz			54	70	pF	
C <sub>rss</sub>	Reverse Tr	ansfer Capacitance	_					5.5	7.5	pF	
Switchi	ng Chara	cteristics									
d(on)	Turn-On Delay Time Turn-On Rise Time		$V_{DD}$ = 400 V, I <sub>D</sub> = 3 A, R <sub>G</sub> = 25 $\Omega$				15	40	ns		
r							43.5	95	ns		
d(off)	Turn-Off De	elay Time		0				22.5	55	ns	
f	Turn-Off Fa	all Time				(Note 4)		32	75	ns	
ე <sub>g</sub>	Total Gate	Charge		V <sub>DS</sub> =	640 V, I <sub>D</sub> = 3 A,			13	16.5	nC	
ସୁ <sub>gs</sub>	Gate-Sourc	ce Charge		V <sub>GS</sub> = 10 V				3.4		nC	
Q <sub>gd</sub>	Gate-Drain	Charge				(Note 4)		5.8		nC	
Drain-S	ource Dic	ode Characteristi	cs and	l Maxir	num Ratings						
s		Continuous Drain-Sour			-				3.0	Α	
SM	Maximum Pulsed Drain-Source Diode For		rward Current					12	Α		
V <sub>SD</sub>	Drain-Sour	ce Diode Forward Volt	age	V <sub>GS</sub> =	0 V, I <sub>S</sub> = 3.0 A				1.4	V	
trr	Reverse Re	ecovery Time			0 V, I <sub>S</sub> = 3.0 A,			642		ns	
Q <sub>rr</sub>	Reverse Recovery Charge			dI <sub>F</sub> / dt = 100 A/μs				4.0		μC	
otes:											
Repetitive R	ating : Pulse widt	h limited by maximum junctio	n temperati	ure.							
L = 67 mH, I	<sub>AS</sub> = 3.0 A, V <sub>DD</sub> =	= 50 V, R <sub>G</sub> = 25 Ω, starting T	J = 25°C.								

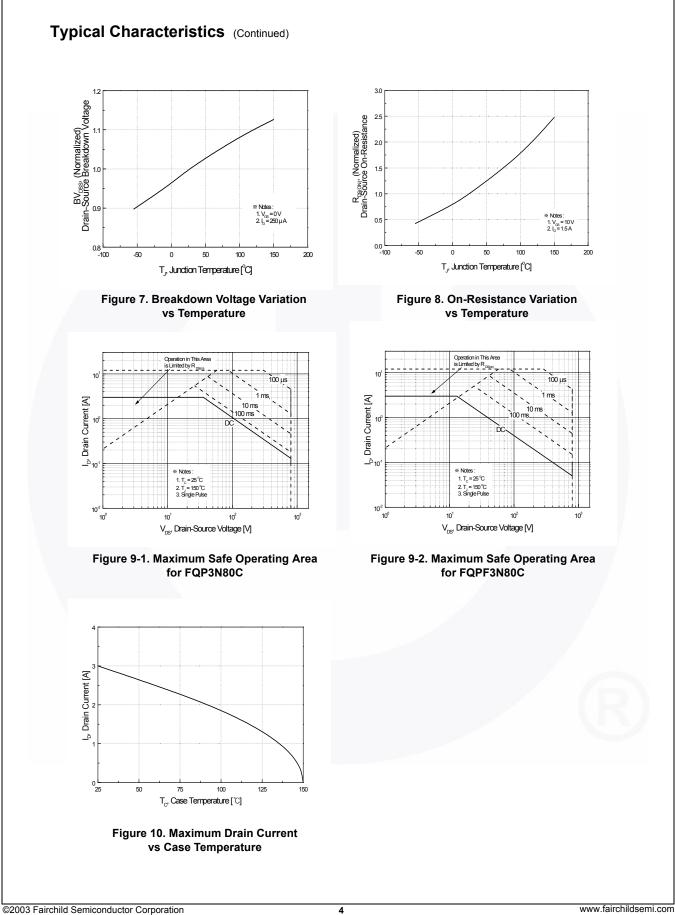
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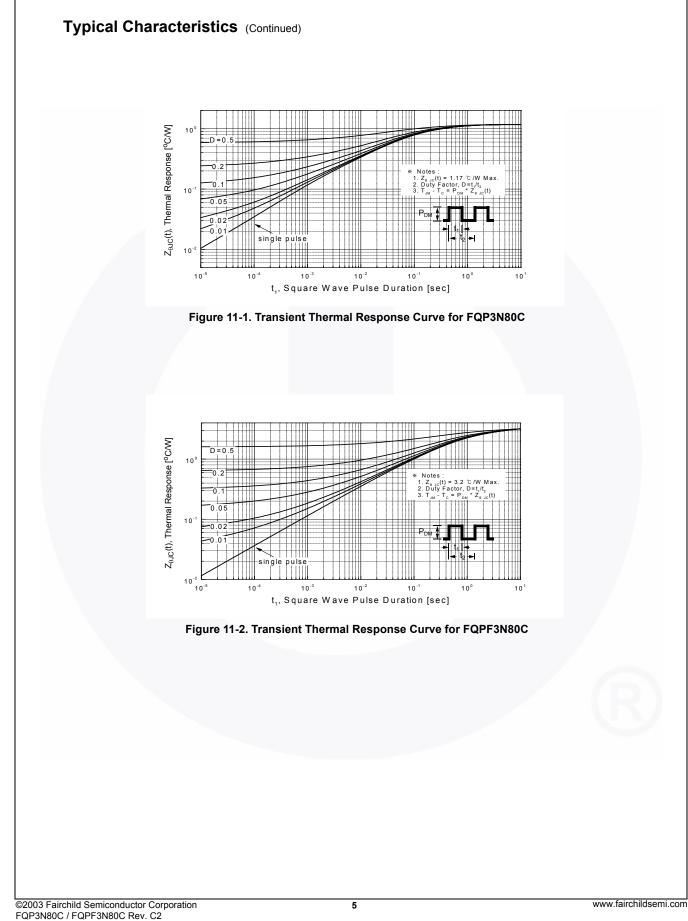
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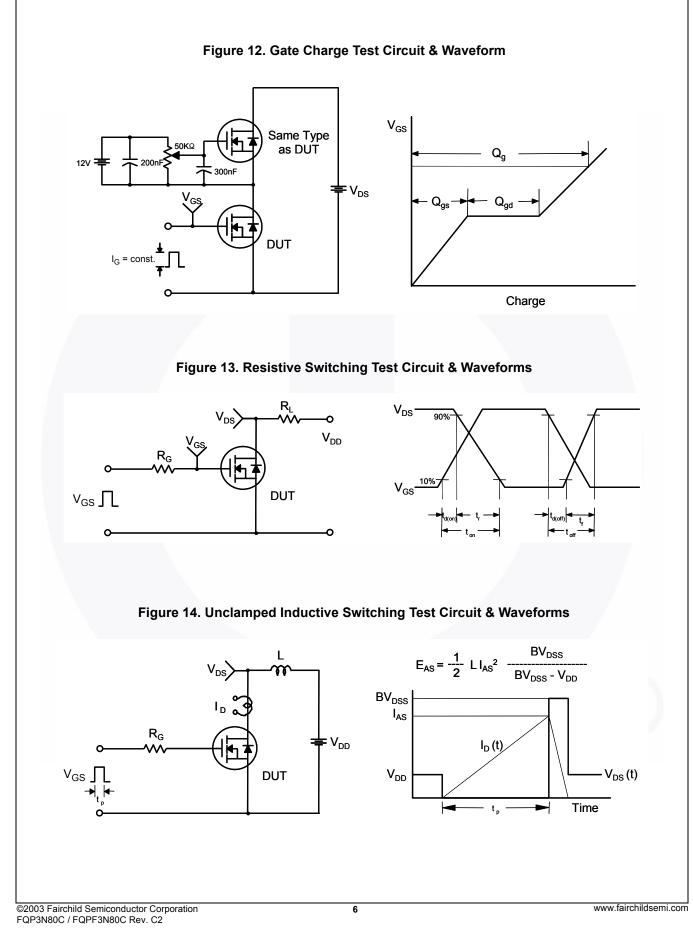
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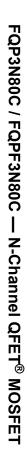


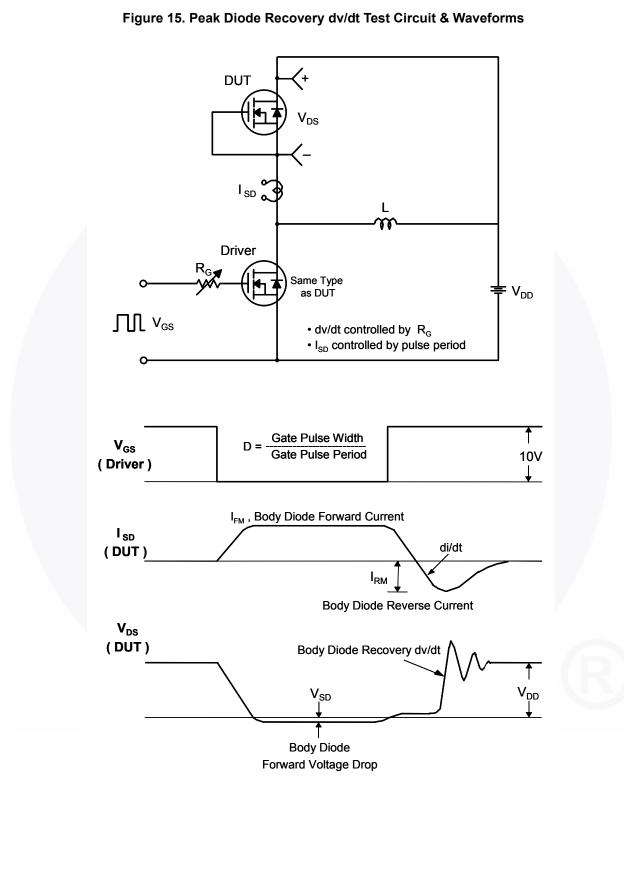
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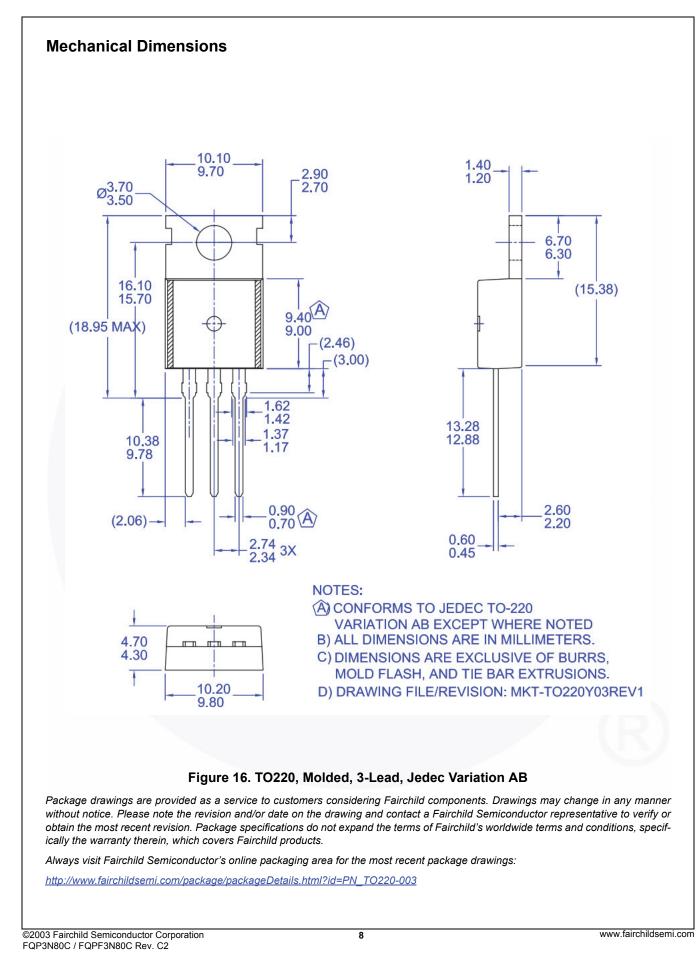


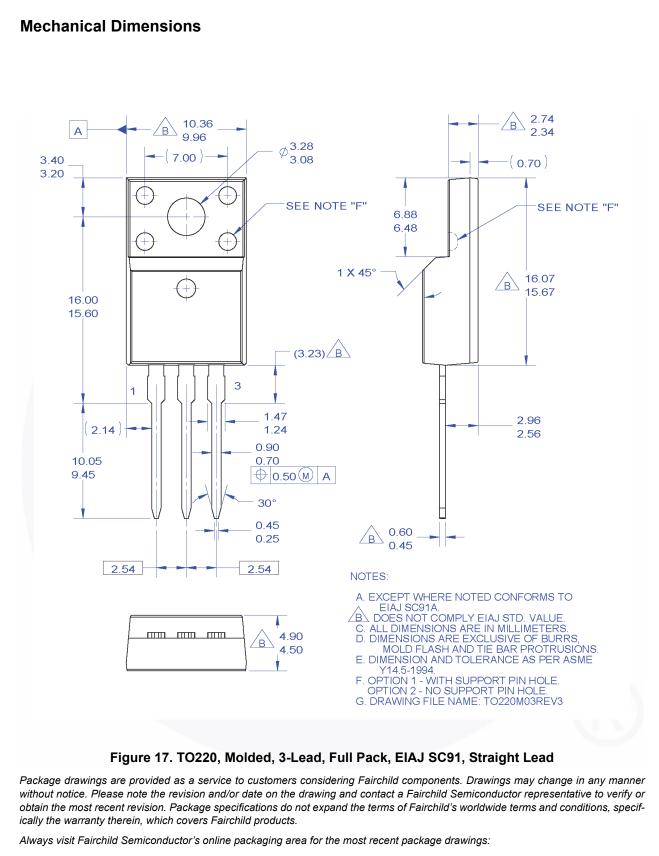
FQP3N80C / FQPF3N80C — N-Channel QFET<sup>®</sup> MOSFET











http://www.fairchildsemi.com/package/packageDetails.html?id=PN\_TF220-003

FQP3N80C / FQPF3N80C — N-Channel QFET<sup>®</sup> MOSFET



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