

RoHS

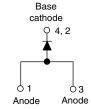
COMPLIANT

HALOGEN

FREE

Schottky Rectifier, 5.5 A





9 mJ

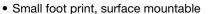
D-PAK (TO-252AA)

E_{AS}

PRODUCT SUMMARY				
Package	D-PAK (TO-252AA)			
I _{F(AV)}	5.5 A			
V _R	40 V			
V _F at I _F	See Electrical table			
I _{RM}	40 mA at 125 °C			
T _J max.	150 °C			
Diode variation	Single die			

FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability



- High frequency operation
- Popular D-PAK outline
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-50WQ04FNHM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	5.5	Α		
V_{RRM}		40	V		
I _{FSM}	t _p = 5 µs sine	340	Α		
V _F	5 A _{pk} , T _J = 125 °C	0.44	V		
TJ	Range	- 40 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-50WQ04FNHM3	UNITS		
Maximum DC reverse voltage	V_R	40	V		
Maximum working peak reverse voltage	V_{RWM}	40	V		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 135 °C, rectangular waveform		5.5	
Maximum peak one cycle	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	550	Α
non-repetitive surge current See fig. 7		10 ms sine or 6 ms rect. pulse	V _{RRM} applied	90	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.5 A, L = 8 mH		9	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.2	Α



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		5 A	T _{.1} = 25 °C	0.51	V
Maximum forward voltage drop	V _{FM} ⁽¹⁾	10 A	11=25 0	0.63	
See fig. 1	V _{FM} ('')	5 A	T 405.00	0.44	
		10 A	T _J = 125 °C	0.59	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	3	A
See fig. 2	IRM (*)	T _J = 125 °C		40	mA .
Thereshold voltage	V _{F(TO)}	T -T maximum		0.27	V
Forward slope resistance	r _t	$T_J = T_J$ maximum		26.77	mΩ
Typical junction capacitance	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz), 25 °C		405	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 5.0		nH	

Note

 $^{^{(1)}\,}$ Pulse width $<300~\mu s,$ duty cycle <2~%

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 40 to 150	°C	
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	3.0	°C/W	
Approximate weight			0.3	g	
Approximate weight			0.01	oz.	
Marking device		Case style D-PAK	50WQ0	4FNH	

Note

(1)
$$\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$$
 thermal runaway condition for a diode on its own heatsink

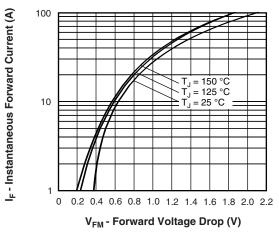


Fig. 1 - Maximum Forward Voltage Drop Characteristics

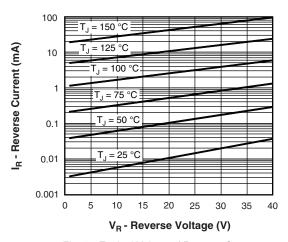


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

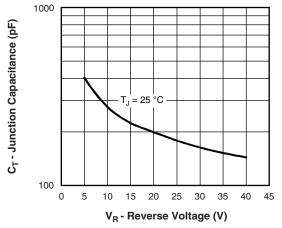


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

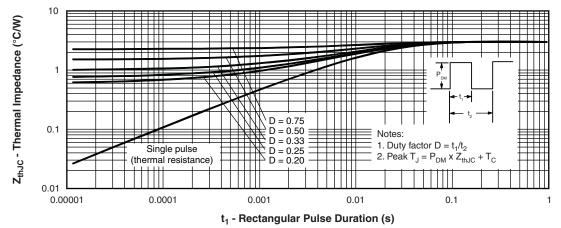


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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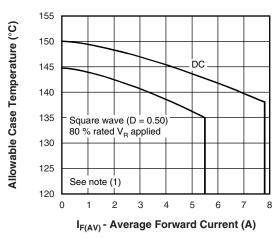


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

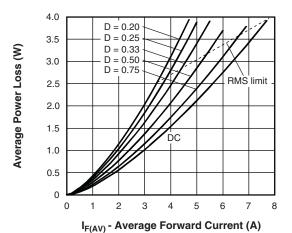
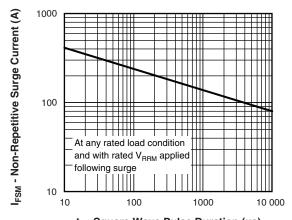


Fig. 6 - Forward Power Loss Characteristics



 $t_{\rm p}$ - Square Wave Pulse Duration (μ s)

Fig. 7 - Maximum Non-Repetitive Surge Current

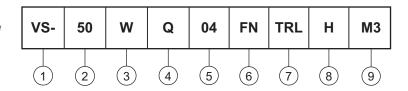
Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (5.5 A)

Package identifier:

W = D-PAK

Schottky "Q" series

5 - Voltage rating (04 = 40 V)

6 - FN = TO-252AA (D-PAK)

7 - • None = Tube

• TR = Tape and reel

• TRL = Tape and reel (left oriented)

• TRR = Tape and reel (right oriented)

8 - H = AEC-Q101 qualified

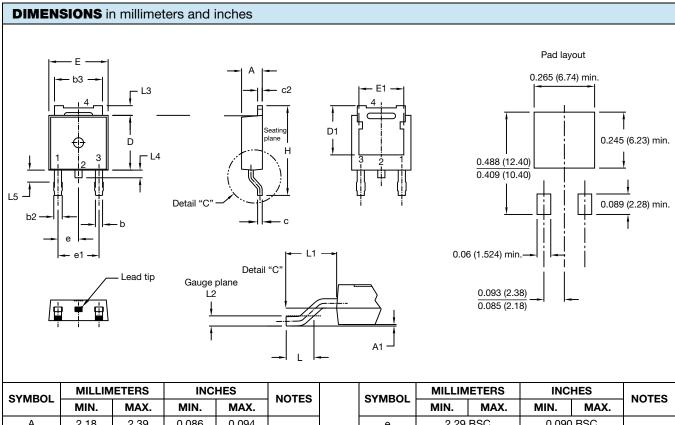
9 - Environmental digit:

M3 = Halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-50WQ04FNHM3	75	3000	Antistatic plastic tube			
VS-50WQ04FNTRHM3	2000	2000	13" diameter reel			
VS-50WQ04FNTRRHM3	3000	3000	13" diameter reel			
VS-50WQ04FNTRLHM3	3000	3000	13" diameter reel			

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95519</u>				
Part marking information	www.vishay.com/doc?95518			
Packaging information	www.vishay.com/doc?95033			

DPAK (TO-252AA)



SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STMIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	2.18	2.39	0.086	0.094	
A1	-	0.13	-	0.005	
b	0.64	0.89	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	3
С	0.46	0.61	0.018	0.024	
c2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	5
D1	5.21	-	0.205	-	3
Е	6.35	6.73	0.250	0.265	5
E1	4.32	-	0.170	-	3

SYMBOL	IVIILLIIV	IEIENO	INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
е	2.29 BSC		0.090 BSC		
Н	9.40	10.41	0.370	0.410	
L	1.40	1.78	0.055	0.070	
L1	2.74 BSC		0.108 REF.		
L2	0.51	0.51 BSC		BSC	
L3	0.89	1.27	0.035	0.050	3
L4	1	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	2

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (5) Outline conforms to JEDEC® outline TO-252AA

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