MOSFET – Single N-Channel, Small Signal, XLLGA3, 0.62 x 0.62 x 0.4 20 V, 224 mA

Features

- Single N-Channel MOSFET
- Ultra Small and Thin Package (0.62 x 0.62 x 0.4 mm)
- Low R_{DS(on)} Solution in 0.62 x 0.62 mm Package
- 1.5 V Gate Voltage Rating
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- Small Signal Load Switch
- Analog Switch
- High Speed Interfacing
- Optimized for Power Management in Ultra Portable Products

MAXIMUM RATINGS (T_J = 25° C unless otherwise stated)

Par	rameter		Symbol	Value	Units
Drain-to-Source Vo	ltage		V _{DSS}	20	V
Gate-to-Source Vol	Gate-to-Source Voltage		V _{GS}	±8.0	V
Continuous Drain	Steady	$T_A = 25^{\circ}C$	I _D	224	mA
Current (Note 1)	State	$T_A = 85^{\circ}C$		162	
	t ≤ 5 s	$T_A = 25^{\circ}C$		241	
Power Dissipa- tion (Note 1)	Steady State	$T_A = 25^{\circ}C$	P _D	120	mW
	t ≤ 5 s	$T_A = 25^{\circ}C$		139	
Pulsed Drain Curre	nt	t _p = 10 μs	I _{DM}	673	mA
Operating Junction and Storage Temperature			T _J , T _{STG}	-55 to 150	°C
Source Current (Body Diode)		۱ _S	120	mA	
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)		ΤL	260	°C	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Units
Junction-to-Ambient - Steady State (Note 1)	R _{θJA}	1040	°C/W
Junction-to-Ambient – t \leq 5 s (Note 1)	$R_{\theta JA}$	900	

1. Surface Mounted on FR4 Board using the minimum recommended pad size, (or 2 $\rm mm^2),\,1$ oz Cu.

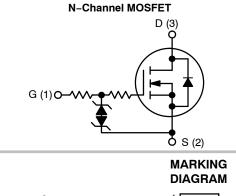
2. Pulse Test: pulse width \leq 300 μ s, duty cycle \leq 2%.

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MOSFET					
V _{(BR)DSS}	R _{DS(on)} MAX	I _D MAX			
	1.4 Ω @ 4.5 V				
20 V	1.9 Ω @ 2.5 V	224 mA			
	2.2 Ω @ 1.8 V				
	4.3 Ω @ 1.5 V				





A = Specific Device Code M = Date Code

ORDERING INFORMATION

Device	Package	Shipping [†]
NTNS3193NZT5G	XLLGA3	8000 /
	(Pb-Free)	Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Units
OFF CHARACTERISTICS				•			
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I _D = 250 μ A		20			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	$I_D = -250 \ \mu A$, ref to $25^{\circ}C$			19		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V, V _{DS} = 20 V	$T_J = 25^{\circ}C$			1.0	μΑ
Gate-to-Source Leakage Current	I _{GSS}	V _{DS} = 0 V, V	∕ _{GS} = ±8.0 V			±2.0	μΑ
ON CHARACTERISTICS (Note 3)							
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} = V _{DS} ,	I _D = 250 μA	0.4		1.0	V
Negative Gate Threshold Temperature Coefficient	V _{GS(TH)} /T _J				1.9		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 4.5 V,	_S = 4.5 V, I _D = 100 mA		0.65	1.4	Ω
		V_{GS} = 2.5 V, I _D = 50 mA V_{GS} = 1.8 V, I _D = 20 mA			0.9	1.9	
					1.1	2.2	
		V _{GS} = 1.5 V	= 1.5 V, I _D = 10 mA		1.4	4.3	1
Forward Transconductance	9fs	V _{DS} = 5 V, I _D = 100 mA			0.56		S
Source-Drain Diode Voltage	V _{SD}	V _{GS} = 0 V,	l _S = 10 mA		0.55	1.0	V
CHARGES & CAPACITANCES							
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1 MHz, V _{DS} = 15 V			15.8		pF
Output Capacitance	C _{OSS}				3.5		
Reverse Transfer Capacitance	C _{RSS}				2.4		
Total Gate Charge	Q _{G(TOT)}				0.70		nC
Threshold Gate Charge	Q _{G(TH)}	V _{GS} = 4.5 V, V _{DS} = 15 V, I _D = 200 mA			0.05		1
Gate-to-Source Charge	Q _{GS}				0.14		1
Gate-to-Drain Charge	Q _{GD}				0.10		1
SWITCHING CHARACTERISTICS, VG	S = 4.5 V (Note 3)						
Turn-On Delay Time	t _{d(ON)}				18		ns
Rise Time	t _r	V _{GS} = 4.5 V, V _{DD} = 15 V,			35		1
Turn-Off Delay Time	t _{d(OFF)}	$I_D = 200 \text{ mA}, R_G = 2 \Omega$			201		1

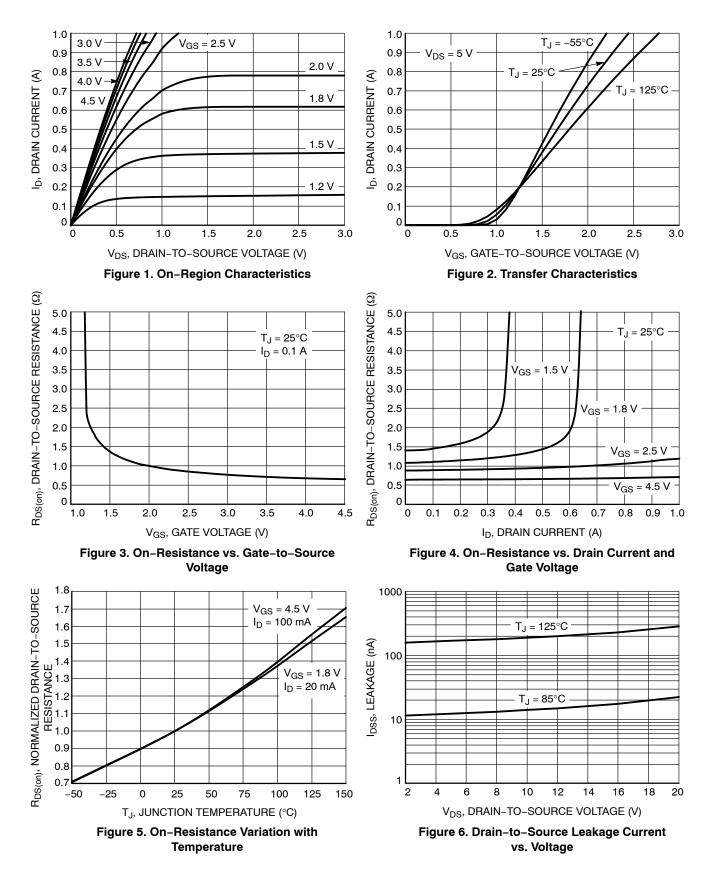
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3. Switching characteristics are independent of operating junction temperatures.

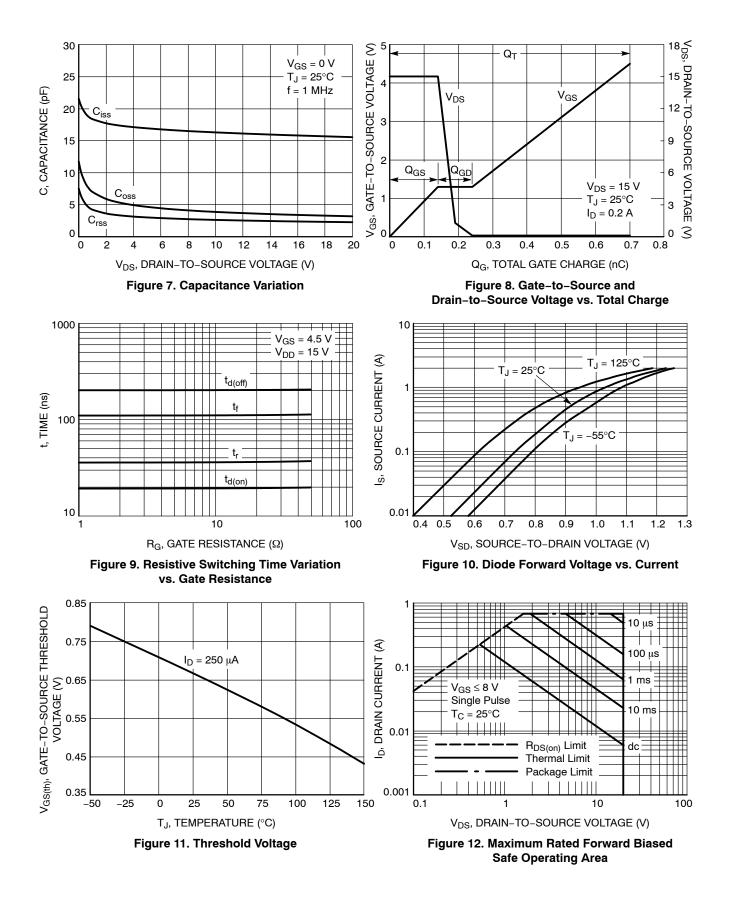
t_f

Fall Time

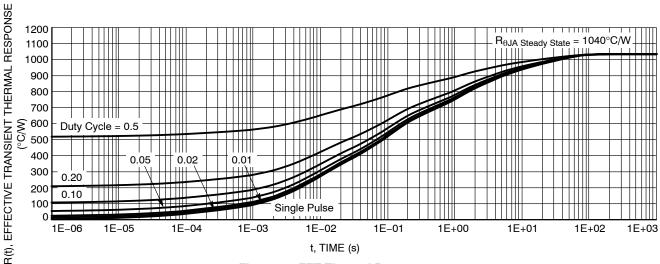
TYPICAL CHARACTERISTICS



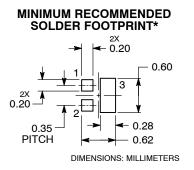
TYPICAL CHARACTERISTICS

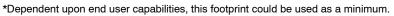


TYPICAL CHARACTERISTICS







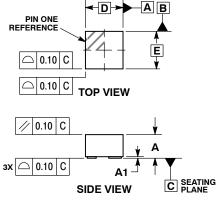


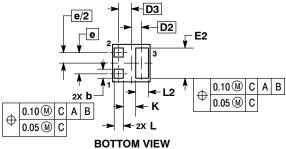




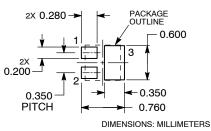
XLLGA3, 0.62x0.62, 0.35P CASE 713AB ISSUE O

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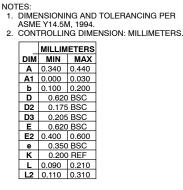




RECOMMENDED SOLDER FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



GENERIC MARKING DIAGRAM*



X = Specific Device Code

M = Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb–Free indicator, "G", may or not be present.

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