# **MOSFET** - Power, Dual

# **N-Channel**

80 V, 15 mΩ, 31 A

# NTMFD6H846NL

#### **Features**

- Small Footprint (5x6 mm) for Compact Design
- Low R<sub>DS(on)</sub> to Minimize Conduction Losses
- Low Q<sub>G</sub> and Capacitance to Minimize Driver Losses
- These Devices are Pb-Free and are RoHS Compliant

# **MAXIMUM RATINGS** ( $T_J = 25^{\circ}C$ unless otherwise noted)

Parameter		Symbol	Value	Unit	
Drain-to-Source Voltage		$V_{DSS}$	80	V	
Gate-to-Source Voltage	Э		V <sub>GS</sub>	±20	V
Continuous Drain		T <sub>C</sub> = 25°C	I <sub>D</sub>	31	Α
Current R <sub>θJC</sub> (Notes 1, 2, 3)	Steady State	T <sub>C</sub> = 100°C		22	
Power Dissipation		T <sub>C</sub> = 25°C	$P_{D}$	34	W
R <sub>θJC</sub> (Notes 1, 2)		T <sub>C</sub> = 100°C		17	
Continuous Drain	Steady State	T <sub>A</sub> = 25°C	I <sub>D</sub>	9.4	Α
Current R <sub>θJA</sub> (Notes 1, 2, 3)		T <sub>A</sub> = 100°C		6.7	
Power Dissipation		T <sub>A</sub> = 25°C	$P_{D}$	3.2	W
R <sub>θJA</sub> (Notes 1, 2)		T <sub>A</sub> = 100°C		1.6	
Pulsed Drain Current	$T_A = 25^{\circ}C, t_p = 10 \mu s$		I <sub>DM</sub>	114	Α
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C	
Source Current (Body Diode)		I <sub>S</sub>	28	Α	
Single Pulse Drain-to-Source Avalanche Energy (T <sub>J</sub> = 25°C, I <sub>L(pk)</sub> = 1.1 A)		E <sub>AS</sub>	201	mJ	
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)		TL	260	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Junction-to-Case - Steady State	$R_{\theta JC}$	4.4	°C/W
Junction-to-Ambient - Steady State (Note 2)	$R_{\theta JA}$	47	

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Surface-mounted on FR4 board using a 650 mm<sup>2</sup>, 2 oz. Cu pad.
- Maximum current for pulses as long as 1 second is higher but is dependent on pulse duration and duty cycle.

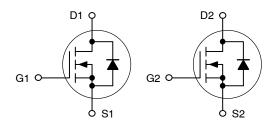


# ON Semiconductor®

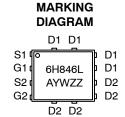
#### www.onsemi.com

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX
80 V	15 mΩ @ 10 V	04.4
	19 mΩ @ 4.5 V	31 A

#### **Dual N-Channel**







A = Assembly Location

Y = Year
W = Work Week
ZZ = Lot Traceability

### **ORDERING INFORMATION**

See detailed ordering, marking and shipping information in the package dimensions section on page 5 of this data sheet.

# **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

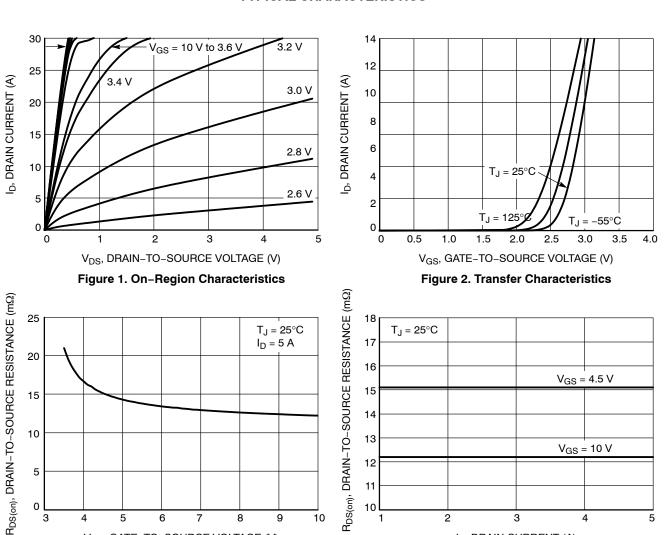
Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS	•				•	•	
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		80			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /				47.1		mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{GS} = 0 V$	T <sub>J</sub> = 25 °C				
		V <sub>DS</sub> = 80 V	T <sub>J</sub> = 125°C			100	μΑ
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub>	<sub>S</sub> = 20 V			100	nA
ON CHARACTERISTICS (Note 4)	•						-
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{GS} = V_{DS}$ , $I_D = 21 \mu A$		1.2		2.0	V
Threshold Temperature Coefficient	V <sub>GS(TH)</sub> /T <sub>J</sub>				-5.5		mV/°C
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 5 A		12.2	15	_
		V <sub>GS</sub> = 4.5 V	I <sub>D</sub> = 5 A		15.1	19	mΩ
Forward Transconductance	9 <sub>F</sub> s	$V_{DS} = 8 \text{ V}, I_{D}$	= 15 A		50		S
CHARGES, CAPACITANCES & GATE RE	SISTANCE				•	•	
Input Capacitance	C <sub>ISS</sub>	$V_{GS} = 0 \text{ V, f} = 1 \text{ MHz, V}_{DS} = 40 \text{ V}$ $V_{GS} = 10 \text{ V, V}_{DS} = 40 \text{ V; I}_{D} = 15 \text{ A}$			900		
Output Capacitance	Coss				120		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>				7		
Total Gate Charge	Q <sub>G(TOT)</sub>				17		
Total Gate Charge	Q <sub>G(TOT)</sub>	V <sub>GS</sub> = 4.5 V, V <sub>DS</sub> = 40 V; I <sub>D</sub> = 15 A			8		1
Threshold Gate Charge	Q <sub>G(TH)</sub>				2		nC V
Gate-to-Source Charge	$Q_GS$				3		
Gate-to-Drain Charge	$Q_GD$				3		
Plateau Voltage	$V_{GP}$				3.2		
SWITCHING CHARACTERISTICS (Note 5	5)						-
Turn-On Delay Time	t <sub>d(ON)</sub>				10		
Rise Time	t <sub>r</sub>	$V_{GS} = 4.5 \text{ V}, V_{D}$	s = 64 V.		40		1
Turn-Off Delay Time	t <sub>d(OFF)</sub>	$I_D = 15 \text{ A}, R_G = 2.5 \Omega$			20		ns -
Fall Time	t <sub>f</sub>				7		
DRAIN-SOURCE DIODE CHARACTERIS	TICS						-
Forward Diode Voltage	$V_{SD}$	V <sub>GS</sub> = 0 V,	T <sub>J</sub> = 25°C		0.79	1.2	
		T <sub>J</sub> = 125°C		0.64		V	
Reverse Recovery Time	t <sub>RR</sub>	$V_{GS} = 0 \text{ V, dIS/dt} = 100 \text{ A/}\mu\text{s,}$ $I_{S} = 15 \text{ A}$			32		
Charge	t <sub>a</sub>				20		ns
Discharge Time	t <sub>b</sub>				12		
Reverse Recovery Charge	Q <sub>RR</sub>				25		nC

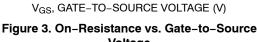
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Pulse Test: pulse width  $\leq 300~\mu s$ , duty cycle  $\leq 2\%$ .

5. Switching characteristics are independent of operating junction temperatures.

#### **TYPICAL CHARACTERISTICS**





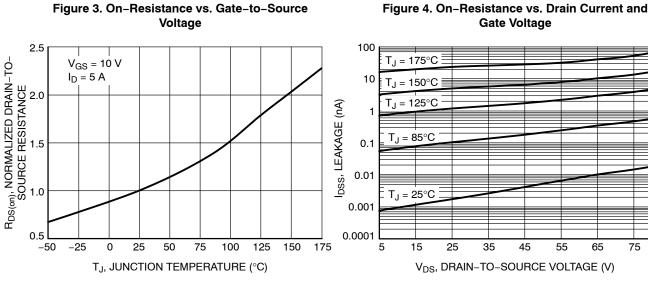


Figure 5. On-Resistance Variation with **Temperature** 

Figure 6. Drain-to-Source Leakage Current vs. Voltage

I<sub>D</sub>, DRAIN CURRENT (A)

V<sub>GS</sub> = 10 V

#### **TYPICAL CHARACTERISTICS**

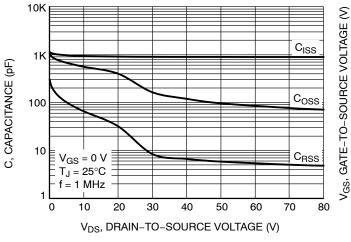


Figure 7. Capacitance Variation

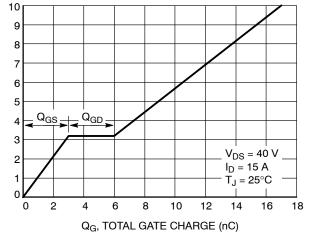


Figure 8. Gate-to-Source and Drain-to-Source Voltage vs. Total Charge

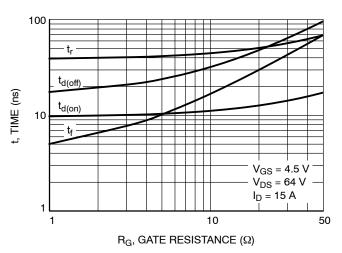


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

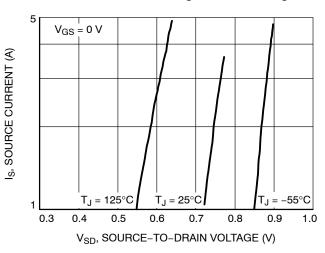


Figure 10. Diode Forward Voltage vs. Current

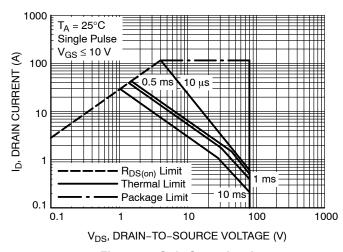


Figure 11. Safe Operating Area

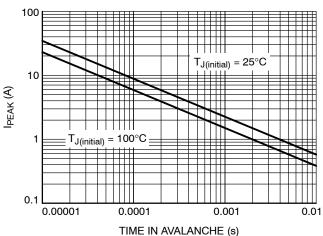


Figure 12. Maximum Drain Current vs. Time in Avalanche

#### **TYPICAL CHARACTERISTICS**

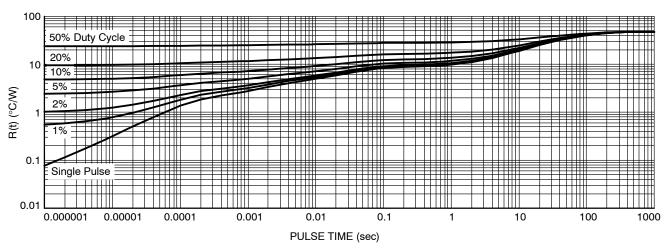


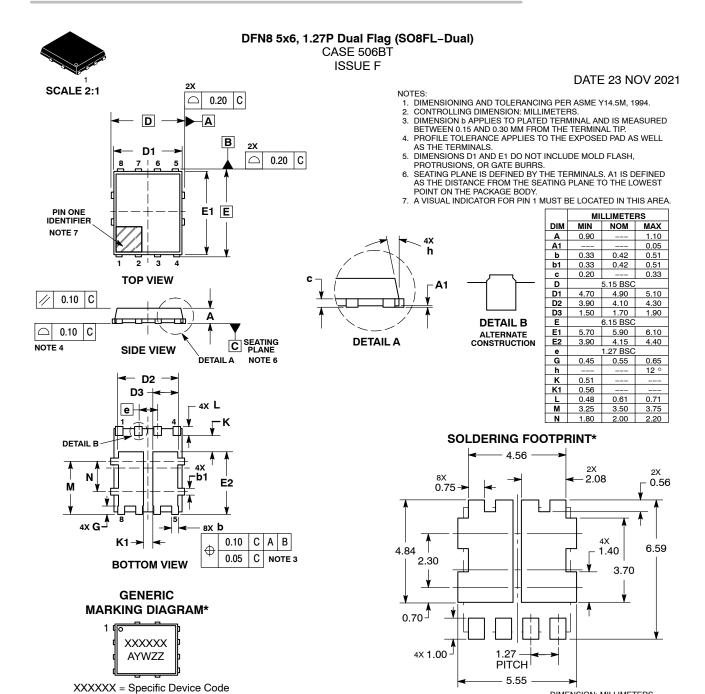
Figure 13. Thermal Response

#### **DEVICE ORDERING INFORMATION**

Device	Marking	Package	Shipping <sup>†</sup>
NTMFD6H846NLT1G	6H846L	DFN8 (Pb-Free)	1500 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.





\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

*Tł	nis information is generic. Please refer to
d	evice data sheet for actual part marking.
Ρ	b-Free indicator, "G" or microdot "■", may
0	r may not be present. Some products may
n	ot follow the Generic Marking.

= Work Week

= Lot Traceability

= Year

Υ

W

77

**DOCUMENT NUMBER:** 

= Assembly Location

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**DESCRIPTION:** DFN8 5X6, 1.27P DUAL FLAG (SO8FL-DUAL)

98AON50417E

**PAGE 1 OF 1** 

**DIMENSION: MILLIMETERS** 

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