VS-MBRS130L-M3

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RoHS COMPLIANT

High Performance Schottky Rectifier, 1.0 A



SMB (DO-214AA)

PRIMARY CHARACTERISTICS				
I _{F(AV)}	1.0 A			
V _R	30 V			
V _F at I _F	0.30 V			
I _{RM} max.	20 mA at 125 °C			
T _J max.	125 °C			
E _{AS}	3.0 mJ			
Package	SMB (DO-214AA)			
Circuit configuration	Single			

FEATURES

- · Small foot print, surface mountable
- · Very low forward voltage drop
- High frequency operation
- HALOGEN Guard ring for enhanced ruggedness and long FREE term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

The VS-MBRS130L-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I _{F(AV)}	Rectangular waveform	1.0	A				
V _{RRM}		30	V				
I _{FSM}	t _p = 5 μs sine	230	A				
V _F	1.0 A _{pk} , T _J = 125 °C	0.30	V				
TJ	Range	-55 to +125	°C				

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-MBRS130L-M3	UNITS
Maximum DC reverse voltage	V _R	30	V
Maximum working peak reverse voltage	V _{RWM}		v

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDI	VALUES	UNITS			
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _L = 112 °C, rectangular waveform		1.0			
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated load	230	А		
non-repetitive surge current		10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	40			
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 6 mH		3.0	mJ		
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum V_A = 1.5 x V_R typical		1.0	А		

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS			
	V _{FM} ⁽¹⁾	1 A	T.I = 25 °C	0.420	V	
Maximum forward voltage drop		2 A	1] = 23 0	0.470		
Maximum forward voltage drop		1 A	− T.ı = 125 °C	0.300		
		2 A	1J=125 C	0.370		
	I _{RM} ⁽¹⁾	T _J = 25 °C		1	mA	
Maximum reverse leakage current		T _J = 100 °C	$V_R = Rated V_R$	10		
		T _J = 125 °C		20		
Maximum junction capacitance	CT	V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 °C		200	pF	
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10			V/µs	

Note

 $^{(1)}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature range	T _J ⁽¹⁾		-55 to +125	°C	
Maximum storage temperature range	T _{Stg}		-55 to +150	U	
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation See fig. 4	25	°C/W	
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	C/VV	
Approvimete weight			0.10	g	
Approximate weight			0.003	oz.	
Marking device		Case style SMB (DO-214AA)	13L		

Notes

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ $\frac{1}{2}$ thermal runaway condition for a diode on its own heatsink (1)

(2) Mounted 1" square PCB

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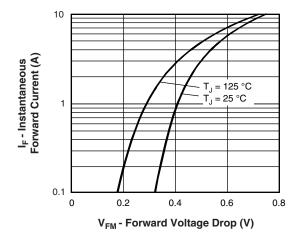


Fig. 1 - Maximum Forward Voltage Drop Characteristics

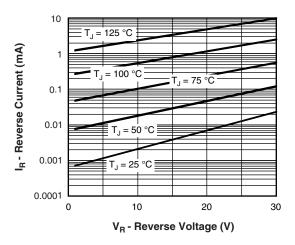


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

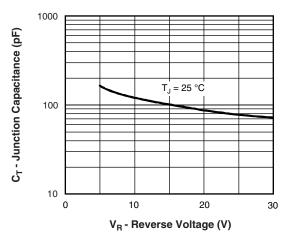
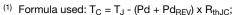


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage Note



 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

3

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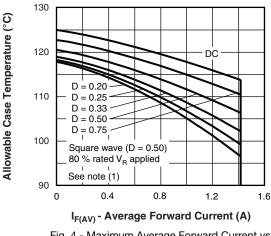


Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature

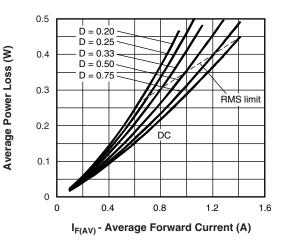


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current

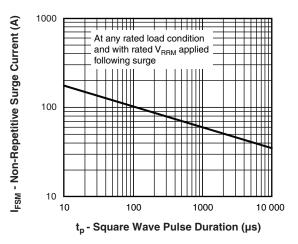


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

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Revision: 30-Jul-2021

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ORDERING INFORMATION TABLE

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Device code	vs-	MBR	S	1	30	L	-M3
	1	2	3	4	5	6	7
	1	- Visł	nay Sem	niconduc	ctors pro	oduct	
			ottky M SMB	BR serie	es		
	H			ng (1 = ⁻	1 A)		
	5	- Volt	tage rati	ng (30 =	= 30 V)		
	6	- L=	low forv	vard volt	age		
	7	M3	s = halog	gen-free	, RoHS-	complia	ant, and

ORDERING INFORMATION (Example)							
PREFERRED P/N	PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-MBRS130L-M3/5BT	5BT	3200	13" diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95401				
Part marking information	www.vishay.com/doc?95403				
Packaging information	www.vishay.com/doc?95404				

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4

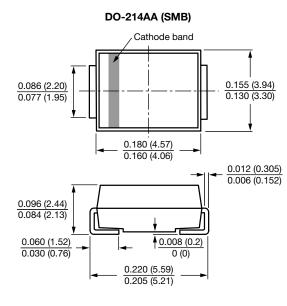


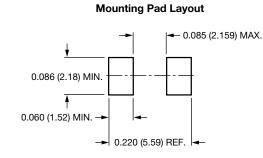
Outline Dimensions

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SMB

DIMENSIONS in inches (millimeters)





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