

STP130NS04ZB - STB130NS04ZB-1 STB130NS04ZB - STW130NS04ZB

N-channel clamped - 7 mΩ - 80A TO-220/I²/D²PAK/TO-247 Fully protected mesh overlay™ MOSFET

General features

Туре	V _{DSS}	R _{DS(on)}	I _D
STP130NS04ZB	clamped	<9 mΩ	80A
STB130NS04ZB	clamped	<9 mΩ	80A
STW130NS04ZB	clamped	<9 mΩ	80A
STB130NS04ZB	clamped	<9 mΩ	80A

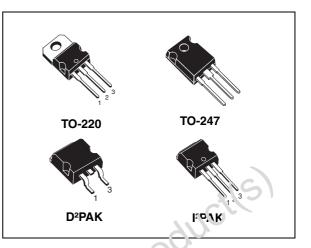
- 100% avalanche tested
- Low capacitance and gate charge
- 175°C maximum junction temperature

Description

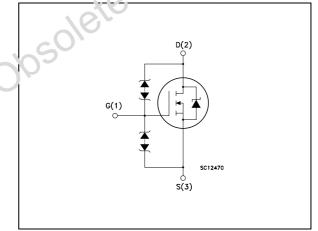
This fully clamped MOSFET is produced by using the latest advanced Company's Mesh Overlay process which is based on a novel strip layout. The inherent benefits of the new technology coupled with the extra clamping capabilities make this product particularly suitable for the harshest operation conditions such as those encountered in the automotive environment .Any other application requiring extra ruggedness is also recommended.

Applications

- High switching current
- Linear applications



Internal schematic diagram



Order codes

Part number	Marking	Package	Packaging
STP130NS04ZB	P130NS04ZB	TO-220	Tube
STB130NS04ZBT4	B130NS04ZB	D ² PAK	Tape & reel
STW130NS04ZB	W130NS04ZB	TO-247	Tube
STB130NS04ZB-1	B130NS04ZB	I²PAK	Tube

October 2006

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Electrical ratings 1

Table 1.	Absolute maximum ratings		
Symbol	Parameter	Value	Unit
V _{DS}	Drain-source voltage ($V_{GS} = 0$)	clamped	V
V _{GS}	Gate-source voltage	clamped	V
۱ _D	Drain current (continuous) at $T_C = 25^{\circ}C$	80	А
I _D	Drain current (continuous) at $T_C = 100^{\circ}C$	60	А
I _{DG}	Drain gate current (continuous)	± 50	mA
I _{GS}	Gate source current (continuous)	± 50	mA
I _{DM} ⁽¹⁾	Drain current (pulsed)	320	А
P _{TOT}	Total dissipation at $T_C = 25^{\circ}C$	300	W
	Derating factor	2.0	W/°C
V _{ESD(G-S)}	Gate-source ESD(HBM-C=100 pF, R=1.5 K Ω)	4 20Ct	КV
T _J T _{stg}	Operating junction temperature Storage temperature	-55 to 175	°C

Table 1 Absolute maximum ratings

T _{stg}	Storage temperature		-55 to 175			
 Pulse width limited by safe operating area Table 2. Thermal data 						
	00	TO-220	D²PAK/I²PA K	TO-247	Unit	
R _{thj-case}	Thermal resistance junction-case Max	0.50			°C/W	
Rthj-pcb ⁽¹⁾	Thermal resistance junction-pcb Max		35		°C/W	
R _{thj-a}	Thermal resistance junction-ambient Max	62.5		50		
T ₁ ⁽²⁾	Maximum lead temperature for soldering purpose	300			°C	

When mounted on 1 inch² FR4 2oZ Cu 1.

2. (1.6 mm from case, for 10 sec)

Table 3. **Avalanche characteristics**

Symbol	Parameter	Value	Unit
I _{AS}	Avalanche current, repetitive or not-repetitive (pulse width limited by Tj Max)	80	A
E _{AS}	Single pulse avalanche energy (starting Tj=25°C, Id=Iar, Vdd=30V)	500	mJ



Electrical characteristics 2

(T_{CASE}=25°C unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Тур.	Max. Unit		
V _{(BR)DSS}	Drain-source breakdown voltage	I _D = 1 mA, V _{GS} = 0 -40 < Tj < 175 °C	33		V		
I _{DSS}	Zero gate voltage drain current (V _{GS} = 0)	V _{DS} = 16 V,T _j = 25 °C V _{DS} = 16 V,T _j = 125 °C			10 100	- F	
I _{GSS}	Gate body leakage current (V _{DS} = 0)	V _{GS} = ±10 V, T _j = 25 °C			10	nA	
V _{GSS}	Gate-source breakdown voltage	I_{GS} = ± 100 μ A	18			>	
V _{GS(th)}	Gate threshold voltage	$V_{DS} = V_{GS} = I_D = 1 \text{ mA}$	2		4	V	
R _{DS(on)}	Static drain-source on resistance	V _{GS} = 10 V ,I _D = 40 A		77	9	mΩ	
			222)			
Table 5.	Dynamic				-		

Table 4.	On/off	states
	010011	010100

Table 5. Dynamic

	Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
	g _{fs} ⁽¹⁾	Forward transconductance	V _{DS} =15V, I _D = 40A		50		S
	C _{iss} C _{oss} C _{rss}	Input capacitance Output capacitance Reverse transfer capacitance	V _{DS} =25V, f=1 MHz, V _{GS} =0		2700 1275 285		pF pF pF
	t _{d(on)} t _r	Turn-on delay time Rise time	$V_{DD} = 17.5 \text{ V}, I_D = 40 \text{ A},$ $R_G = 4.7 \Omega, V_{GS} = 10 \text{ V}$ (see Figure 13)		40 10		ns ns
	t _{d(off)} t _f	Turn-off delay time Fall time	$V_{DD} = 17.5 \text{ V}, \text{ I}_{D} = 40 \text{ A},$ $R_{G} = 4.7 \Omega, V_{GS} = 10 \text{ V}$ (see Figure 13)		220 100		ns ns
sole	Q _g Q _{gs} Q _{gd}	Total gate charge Gate-source charge Gate-drain charge	V_{DD} =20V, I _D = 80A V_{GS} =10V (see Figure 14)		80 20 27	105	nC nC nC
002	1. Pulsed: p	pulse duration=300µs, duty cycle	1.5%				



Symbol	Parameter Test conditions Min				Max	Unit
I _{SD}	Source-drain current				80	А
$I_{SDM}^{(1)}$	Source-drain current (pulsed)				320	А
$V_{SD}^{(2)}$	Forward on voltage	I _{SD} =80A, V _{GS} =0			1.5	V
t _{rr} Q _{rr} I _{RRM}	Reverse recovery time Reverse recovery charge Reverse recovery current	I _{SD} =80A, di/dt = 100A/μs, V _{DD} =25V, Tj=150°C (see Figure 15)		90 0.18 4		ns μC Α

Table 6.Source drain diode

1. Pulse width limited by safe operating area

2. Pulse: pulse duration=300µs, duty cycle 1.5%



2.1 Electrical characteristics (curves)

Figure 1. Safe operating area

Figure 2. Thermal impedance

0.05

0.02

0.01

1073

10⁻²

Transfer characteristics

10-1

SINGLE PULSE

10⁻⁴

= 0.5

10

10⁻²10⁻⁵

Figure 4.

280TO

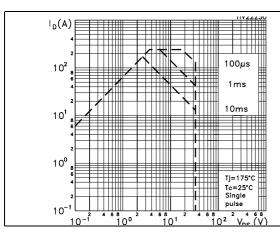
 $Z_{th} = k R_{thJ-c}$

tρ ⊢_ −

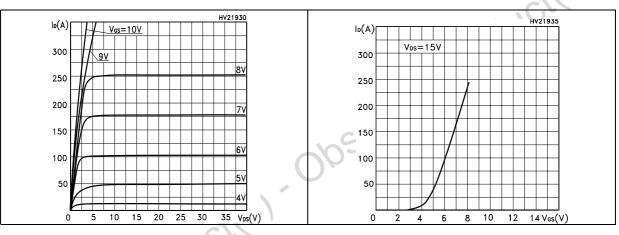
10⁰ tp(s)

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 $\delta = t_p / \tau$







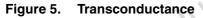
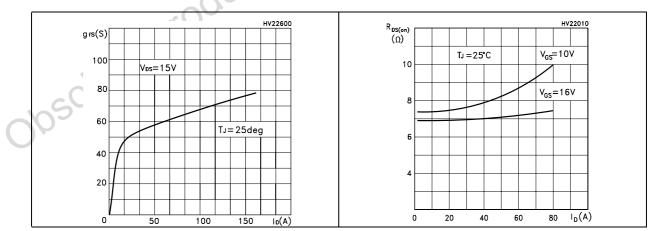


Figure 6. Static drain-source on resistance



Gate charge vs gate-source voltage Figure 8.

150 TJ (°C

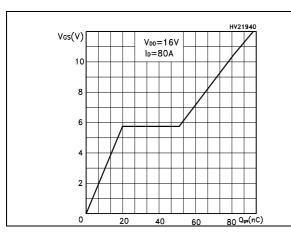
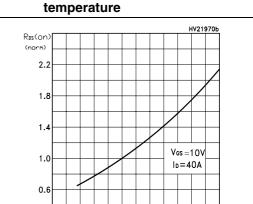


Figure 7.

Figure 9. Normalized gate threshold voltage vs temperature



Normalized on resistance vs

Figure 10. Source-drain diode forward characteristics

-50

0

50 100

٥

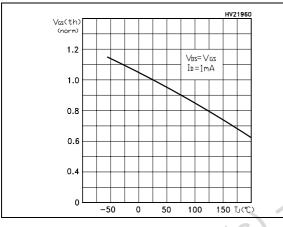


Figure 11. Capacitance variations

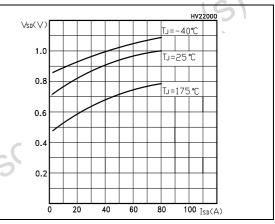


Figure 12. Normalized B_{VDSS} vs temperature

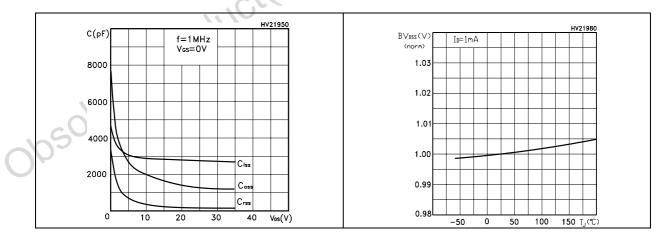


Figure 14. Gate charge test circuit

3 Test circuit

Figure 13. Switching times test circuit for resistive load

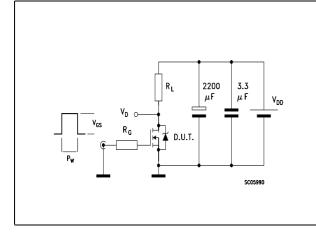
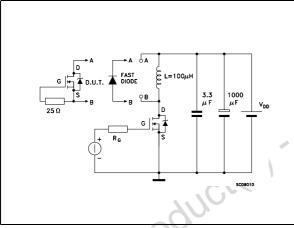
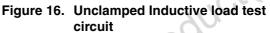


Figure 15. Test circuit for inductive load switching and diode recovery times





 $V_{1} = 20V = V_{OMAX}$ $I_{0} = CONST$ $I_{0} = CONST$ $I_{0} = CONST$ $I_{0} = 0.0.T.$ V_{0} $V_{1} = 20V = V_{OMAX}$ $I_{0} = CONST$ $I_{0} = 0.0.T.$ V_{0} V_{0} V_{0} SC06000



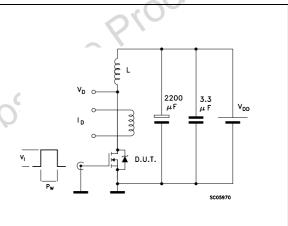
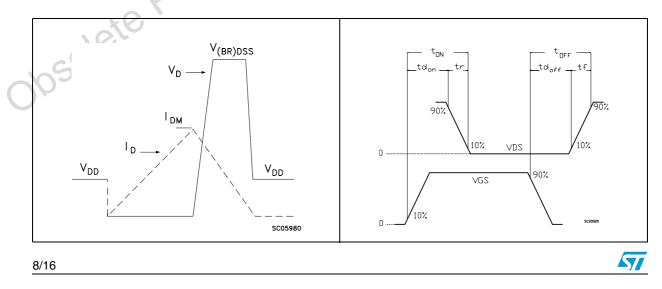


Figure 18. Switching time waveform



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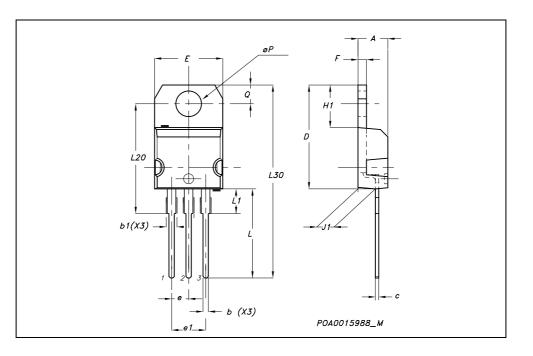
4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

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DIM.	mm.			inch		
	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX.
А	4.40		4.60	0.173		0.181
b	0.61		0.88	0.024		0.034
b1	1.15		1.70	0.045		0.066
С	0.49		0.70	0.019		0.027
D	15.25		15.75	0.60		0.620
Е	10		10.40	0.393		0.409
е	2.40		2.70	0.094		0.106
e1	4.95		5.15	0.194		0.202
F	1.23		1.32	0.048		0.052
H1	6.20		6.60	0.244		0.256
J1	2.40		2.72	0.094		0.107
L	13		14	0.511		0.551
L1	3.50		3.93	0.137		0.154
L20		16.40			0.645	
L30		28.90			1.137	
øP	3.75		3.85	0.147		0.151
Q	2.65		2.95	0.104		0.116



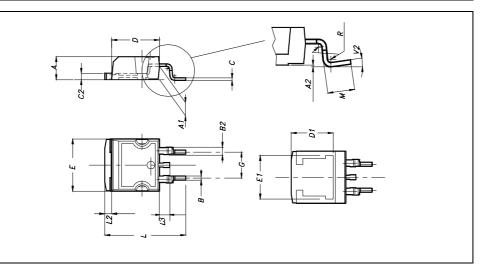






DIM.	mm.			inch		
	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX.
А	4.4		4.6	0.173		0.181
A1	2.49		2.69	0.098		0.106
A2	0.03		0.23	0.001		0.009
В	0.7		0.93	0.027		0.036
B2	1.14		1.7	0.044		0.067
С	0.45		0.6	0.017		0.023
C2	1.23		1.36	0.048		0.053
D	8.95		9.35	0.352		0.368
D1		8			0.315	
Е	10		10.4	0.393		
E1		8.5			0.334	
G	4.88		5.28	0.192		0.208
L	15		15.85	0.590		0.625
L2	1.27		1.4	0.050		0.055
L3	1.4		1.75	0.055		0.068
М	2.4		3.2	0.094		0.126
R		0.4			0.015	
V2	0º		4º			

D²PAK MECHANICAL DATA

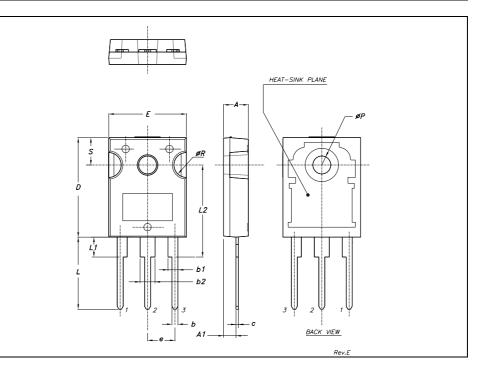




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DIM.	mm.			inch		
	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX.
А	4.85		5.15	0.19		0.20
A1	2.20		2.60	0.086		0.102
b	1.0		1.40	0.039		0.055
b1	2.0		2.40	0.079		0.094
b2	3.0		3.40	0.118		0.134
С	0.40		0.80	0.015		0.03
D	19.85		20.15	0.781		0.793
E	15.45		15.75	0.608		0.620
е		5.45			0.214	
L	14.20		14.80	0.560		0.582
L1	3.70		4.30	0.14		0.17
L2		18.50			0.728	
øP	3.55		3.65	0.140		0.143
øR	4.50		5.50	0.177		0.216
S		5.50			0.216	





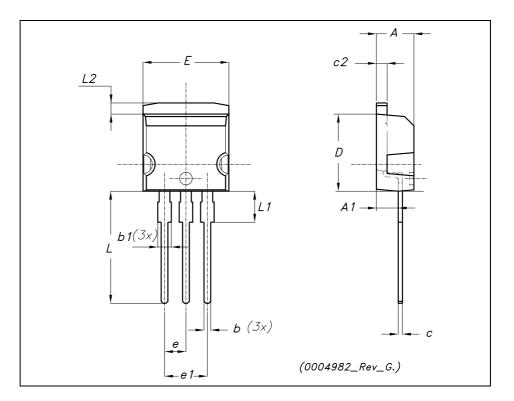
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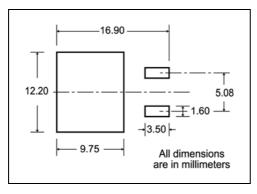
DIM.	mm.			inch		
	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX
А	4.40		4.60	0.173		0.181
A1	2.40		2.72	0.094		0.107
b	0.61		0.88	0.024		0.034
b1	1.14		1.70	0.044		0.066
с	0.49		0.70	0.019		0.027
c2	1.23		1.32	0.048		0.052
D	8.95		9.35	0.352		0.368
е	2.40		2.70	0.094		0.106
e1	4.95		5.15	0.194		0.202
E	10		10.40	0.393		0.410
L	13		14	0.511		0.551
L1	3.50		3.93	0.137		0.154





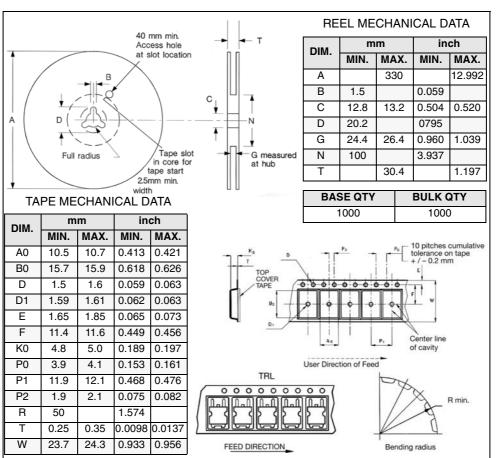


5 Packaging mechanical data



D²PAK FOOTPRINT

TAPE AND REEL SHIPMENT



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* on sales type

6 Revision history

Date	Revision	Changes
10-Feb-2005	1	First release.
14-Jan-2006	2	Inserted D ² PAK, complete version.
03-Oct-2006	3	Inserted I ² PAK.

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