LF maximum peak of 260 °C

- Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

MECHANICAL DATA

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test Polarity: as marked

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V40DM100C	UNIT	
Device marking code			V40DM100C		
Maximum repetitive peak reverse voltage		V _{RRM}	100	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)} ⁽¹⁾	40	А	
	per diode		20		
Peak forward surge current 8.3 ms single half superimposed on rated load	sine-wave	I _{FSM}	240	А	
Operating junction temperature range		T _J ⁽²⁾	-40 to +175	*0	
Storage temperature range		T _{STG}	-55 to +175	- °C	

Notes

30

3D Models

⁽¹⁾ Mounted on infinite heatsink

 $^{(2)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: dP_D/dT_J < 1/R_{0,JA}

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eSMP[®] Series



DESIGN SUPPORT TOOLS AVAILABLE

PRIMARY CHARACTERISTICS

I_{F(AV)}

V_{RRM}

IFSM

 V_F at I_F = 20 A (T_A = 125 °C)

T_J max.

Package

Circuit configuration

Cathode

2 x 20 A

100 V

240 A

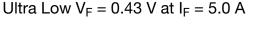
0.64 V

175 °C

SMPD (TO-263AC)

Common cathode

Dual High-Voltage TMBS[®] (Trench MOS Barrier Schottky) Rectifier



FEATURES

- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- · Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL J-STD-020. level 1, per
- AEC-Q101 qualified available:
- AUTOMOTIV GRAD

Vishay General Semiconductor





RoHS

COMPLIANT

HALOGEN FREE



V40DM100C







Vishay General Semiconductor

ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	$I_F = 5 A$	T _A = 25 °C	V _F (1)	0.52	-	V	
	I _F = 10 A			0.60	-		
	I _F = 20 A			0.73	0.77		
	I _F = 5 A	T _A = 125 °C		0.43	-		
	I _F = 10 A			0.53	-		
	I _F = 20 A			0.64	0.71		
	$V_{\rm R} = 70 \text{ V} \qquad \frac{T_{\rm A} = 25 \text{ °C}}{T_{\rm A} = 125 \text{ °C}} \qquad -$	0.01	-				
Reverse current at rated V_R per diode		T _A = 125 °C	I _R ⁽²⁾	2.5	-	mA	
	V _R = 100 V	T _A = 25 °C		-	0.7		
		T _A = 125 °C		5	20		
Typical junction capacitance	4.0 V, 1 MHz		CJ	1920	-	pF	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V40DM100C	UNIT	
Typical thermal resistance per device	$R_{\theta JC}^{(1)}$	1.0	°C/W	
	R _{0JA} ⁽²⁾⁽³⁾	50		

Notes

⁽¹⁾ Mounted on infinite heatsink

 $^{(2)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$ - junction-to-ambient

⁽³⁾ Free air, without heatsink

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V40DM100C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel	
V40DM100CHM3/I (1)	0.55	l	2000/reel	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified



Vishay General Semiconductor

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

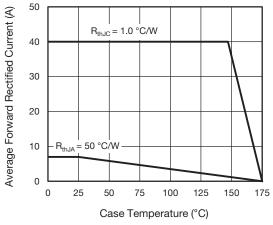


Fig. 1 - Maximum Forward Current Derating Curve

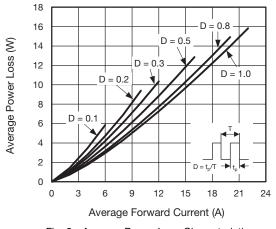
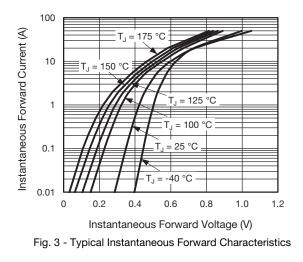


Fig. 2 - Average Power Loss Characteristics



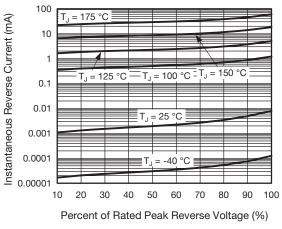
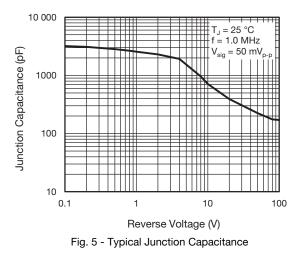


Fig. 4 - Typical Reverse Leakage Characteristics



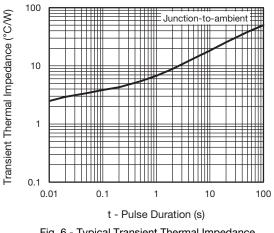


Fig. 6 - Typical Transient Thermal Impedance

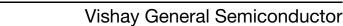
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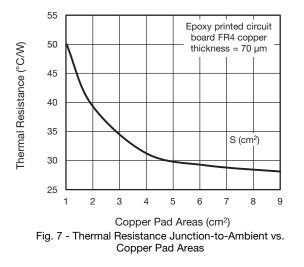
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V40DM100C

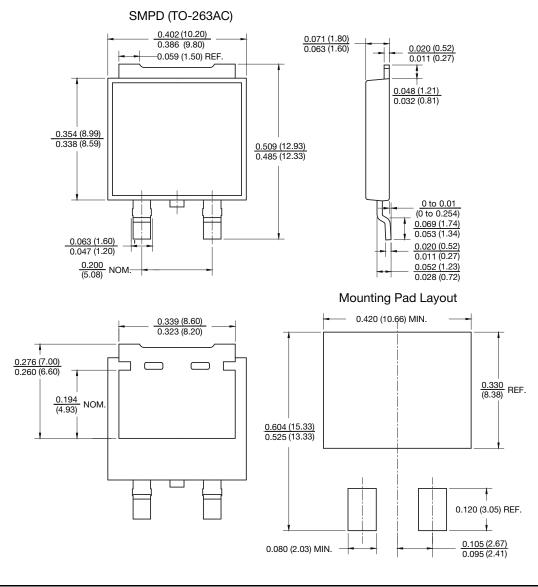




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