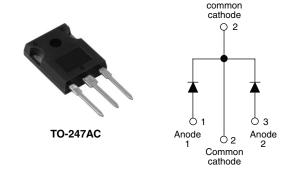
63CPQ100PbF

Vishay High Power Products

Schottky Rectifier, 2 x 30 A



Base

PRODUCT SUMMARY						
I _{F(AV)}	2 x 30 A					
V _R	100 V					

FEATURES

- 175 °C T_J operation
- Center tap TO-247 package
- · Low forward voltage drop
- · High frequency operation
- · High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- · Designed and qualified for industrial level

DESCRIPTION

The 63CPQ100PbF center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	60	А						
V _{RRM}		100	V						
I _{FSM}	t _p = 5 μs sine	2200	А						
V _F	30 Apk, $T_J = 125 \ ^{\circ}C$ (per leg)	0.64	V						
TJ	Range	- 55 to 175	°C						

VOLTAGE RATINGS									
PARAMETER	SYMBOL	63CPQ100PbF	UNITS						
Maximum DC reverse voltage	100	V							
Maximum working peak reverse voltage	V _{RWM}	100	V						

ABSOLUTE MAXIMUM RATINGS									
PARAMETER		SYMBOL	TEST COND	VALUES	UNITS				
Maximum average forward currentper legSee fig. 5per device			50 % duty cycle at T_{C} = 153 °C, rectangular waveform		30				
		'F(AV)	$I_{F(AV)}$ 50 % duty cycle at $I_C = 153$ °C, rectangular waveform			А			
Maximum peak one cycle non-repetitive surge current per leg		I _{ESM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	2200				
See fig. 7	1 0		10 ms sine or 6 ms rect. pulse	V_{RRM} applied	410				
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 30 mH		15	mJ			
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	А			

* Pb containing terminations are not RoHS compliant, exemptions may apply





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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS			
		30 A	T _{.1} = 25 °C	0.77	V			
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	60 A	1j=25°C	0.92				
See fig. 1	VFM ('')	30 A	T 105 00	0.64				
		60 A	T _J = 125 °C	0.76				
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V Deted V	0.3	m A			
See fig. 2		T _J = 125 °C	V _R = Rated V _R	25	mA			
Threshold voltage	V _{F(TO)}	T T movimum		0.38	V			
Forward slope resistance	r _t	i j = i j maximum	$T_J = T_J$ maximum		mΩ			
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal ran	1300	pF				
Typical series inductance per leg	L _S	Measured lead to lead 5 m	7.5	nH				
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

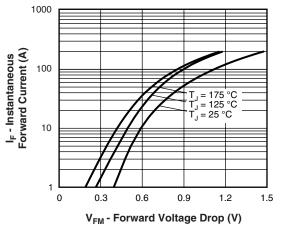
Note

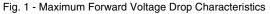
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C			
Maximum thermal resistance, junction to case per leg			DC operation See fig. 4	0.8				
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	0.4	°C/W			
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.25				
Approximate weight				6	g			
				0.21	oz.			
Mounting torque	minimum			6 (5)	kgf ⋅ cm			
Mounting torque –	maximum			12 (10)	(lbf ⋅ in)			
Marking device			Case style TO-247AC (JEDEC)	63CP	Q100			



Schottky Rectifier, 2 x 30 A Vishay High Power Products





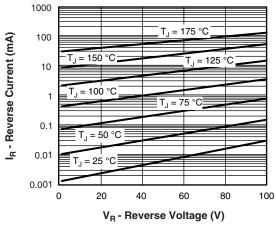


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

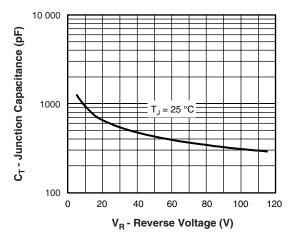


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

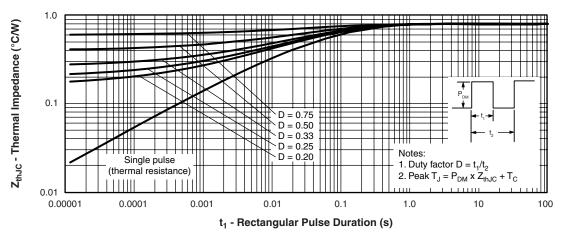
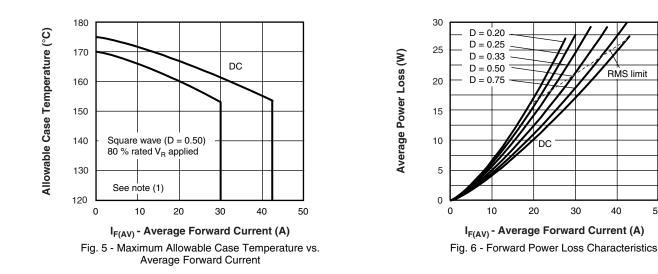


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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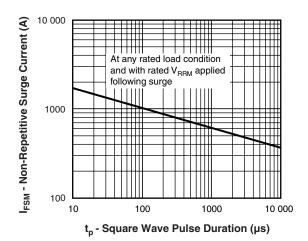


Fig. 7 - Maximum Non-Repetitive Surge Current

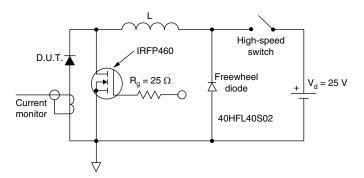


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$ $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D); I_R at V_{R1} = 80 \% rated V_R$

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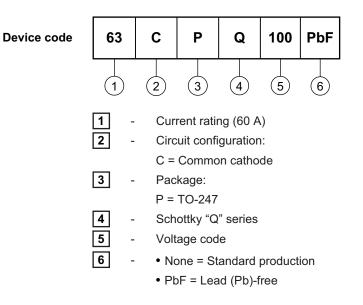
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Schottky Rectifier, 2 x 30 A Vishay High Power Products

ORDERING INFORMATION TABLE



Tube standard pack quantity: 25 pieces

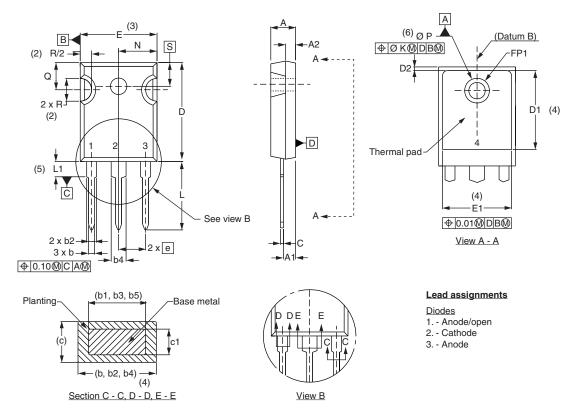
LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95223					
Part marking information	http://www.vishay.com/doc?95226				

Outline Dimensions





DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	INCHES		NOTES SYMBO		MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES	3	STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209			D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098			E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	5 BSC	
b1	0.99	1.35	0.039	0.053			FK	2.	54	0.0	010	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.37	0.065	0.094			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			N	7.62	BSC	0.3		
b5	2.59	3.38	0.102	0.133			ΦP	3.56	3.66	0.14	0.144	
С	0.38	0.86	0.015	0.034			Φ P1	-	6.98	-	0.275	
c1	0.38	0.76	0.015	0.030			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	1.78	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	' BSC	

Notes

- ⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- ⁽⁴⁾ Thermal pad contour optional with dimensions D1 and E1
- ⁽⁵⁾ Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- ⁽⁷⁾ Outline conforms to JEDEC outline TO-247 with exception of dimension c

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



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