Vishay Semiconductors

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High Performance Schottky Rectifier, 100 A





PowerTab[®]

PRIMARY CHARACTERISTICS				
I _{F(AV)}	100 A			
V _R	100 V			
V _F at I _F	0.82 V			
I _{RM}	180 mA at 125 °C			
E _{AS}	9 mJ			
T _J max.	175 °C			
Package	PowerTab [®]			
Circuit configuration	Single			

FEATURES

- 175 °C max. operating junction temperature
- High frequency operation
- Low forward voltage drop
- Continuous high current operation
- Guard ring for enhanced ruggedness and long term reliability
 RoHS compliant
- · Screw mounting only
- Designed and qualified according to JEDEC®-JESD 47
- PowerTab[®] package
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-100BGQ100 Schottky rectifier has been optimized for low reverse leakage at high temperature.

The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I	Rectangular waveform	100	А		
I _{F(AV)}	T _C	124	°C		
V _{RRM}		100	V		
I _{FSM}	t _p = 5 μs sine	6300	А		
100 A _{pk} (typical)		0.77	V		
V _F	TJ	125	O°		
TJ	Range	-55 to +175	C°		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	100BGQ100	UNITS	
Maximum DC reverse voltage	V _R	100	V	
Maximum working peak reverse voltage	V _{RWM}	100	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_C = 124 °C, rectangular waveform 100		А	
Maximum peak one cycle	I	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	6300	А
non-repetitive surge current	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	800	A
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 4.5 mH 9		mJ	
Repetitive avalanche current	I _{AR}			А	

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1





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ELECTRICAL SPECIFICAT	FIONS
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PARAMETER	SYMBOL	TEST CONDITIONS		VALUES		
PARAMETER	STNIDUL			TYP.	MAX.	UNITS
		50 A	T 05 %C	0.83	0.86	v
Forward voltage drop	V _{FM} ⁽¹⁾	100 A	− T _J = 25 °C	1.01	1.08	
Forward voltage drop	VFM (')	50 A	T _J = 125 °C	0.66	0.7	
		100 A		0.77	0.82	
Poveros loskago ourrent	I (1)	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	22	300	μA
Reverse leakage current	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = naleu V _R	14	18	mA
Maximum junction capacitance	CT	V_{R} = 5 V_{DC} , (test signal range 100 kHz to 1 MHz) 25 °C		13	20	pF
Typical series inductance	L _S	Measured from tab to mounting plane		3	.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10	000	V/µs

Note

⁽¹⁾ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and temperature range	l storage	T _J , T _{Stg}		-55 to +175	°C	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	0.50 °C/W		
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.30	0/11	
Approximate weight				5	g	
Approximate weight				0.18	oz.	
Mounting torque	minimum			1.2 (10)	N · m	
Mounting torque	maximum			2.4 (20)	(lbf · in)	
Marking device			Case style PowerTab [®]	100BC	GQ100	

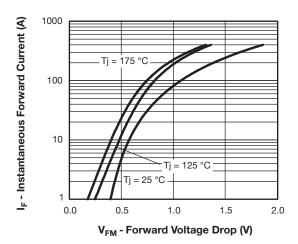


Fig. 1 - Maximum Forward Voltage Drop Characteristics

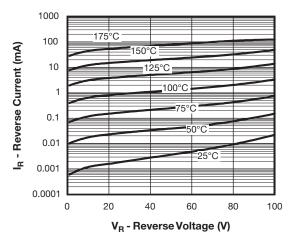


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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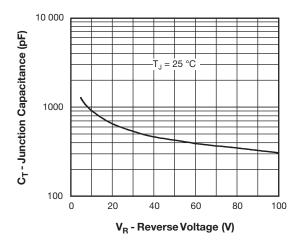


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

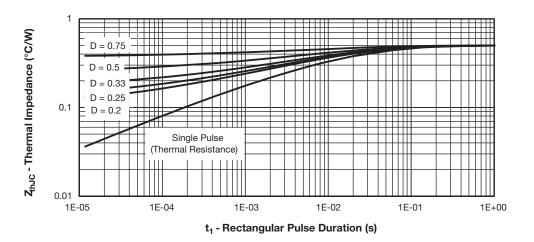
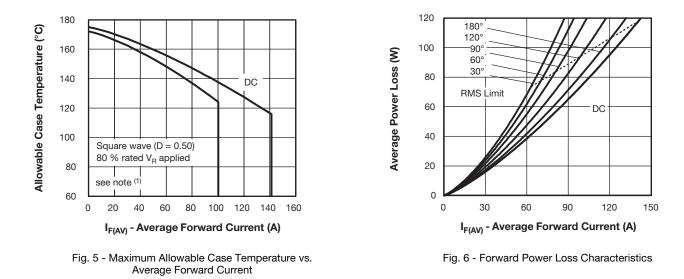


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



Revision: 16-Jan-2019

3

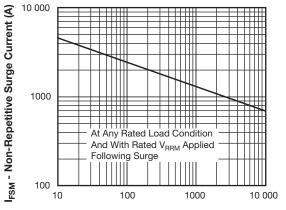
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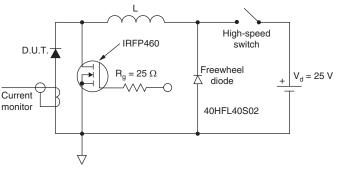
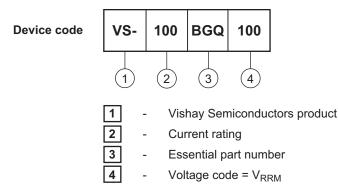


Fig. 8 - - Unclamped Inductive Test Circuit

Note

ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95240				
Part marking information	www.vishay.com/doc?95370			
Application note	www.vishay.com/doc?95179			
SPICE model	www.vishay.com/doc?96588			

Revision: 16-Jan-2019

4

Document Number: 94581

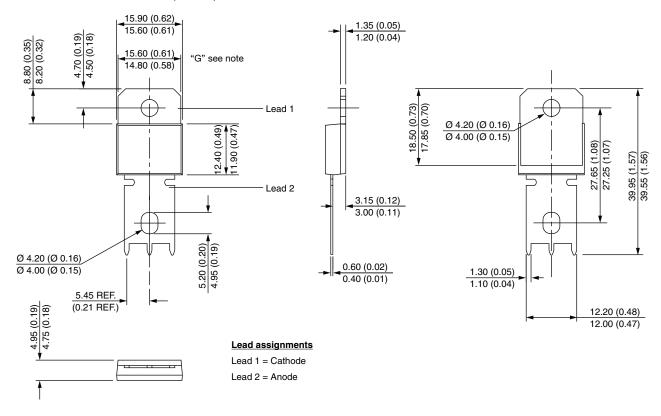
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DIMENSIONS in millimeters (inches)



Note:

Outline conform to JEDEC® TO-275, except for dimension "G" only

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