

---

## Features

- Adjustable DC Characteristic
- Transmit and Receive Gain Adjustable
- Symmetrical Input of Microphone Amplifier
- Anti-clipping in Transmit Direction
- Automatic Line-loss Compensation
- Built-in Ear Protection
- DTMF and MUTE Input
- Adjustable Sidetone Suppression Independent of Sending and Receiving Amplification
- Integrated Amplifier for Open-listening Operation
- Anti-clipping for Loudspeaker Amplifier
- Improved Acoustical Feedback Suppression
- Selectable Line Impedance
- Voice Switch
- Supply Voltages for All Functional Blocks of a Subscriber Set
- Operation Possible from 10-mA Line Current
- Filters Against EMI on Critical I/O

## Benefits

- Complete System Integration of Analog Signal Processing on One Chip
- Very Few External Components
- Less Components for EMI protection

## Applications

- Feature Phones
- Answering Machines
- Fax Machines
- Speaker Phones

## 1. Description

The Atmel® telephone circuit U4089B-P is a linear integrated circuit for use in feature phones, answering machines and fax machines. It contains the speech circuit, sidetone equivalent and ear protection rectifiers. The circuit is line-powered and contains all components necessary for the amplification of signals and adaptation to the line.

An integrated voice switch with loudspeaker amplifier enables open-listening or hands-free operation. With an anti-feedback function, acoustical feedback during open listening can be reduced significantly. The generated supply voltage is suitable for a wide range of peripheral circuits.



---

## Monolithic Integrated Feature Phone Circuit

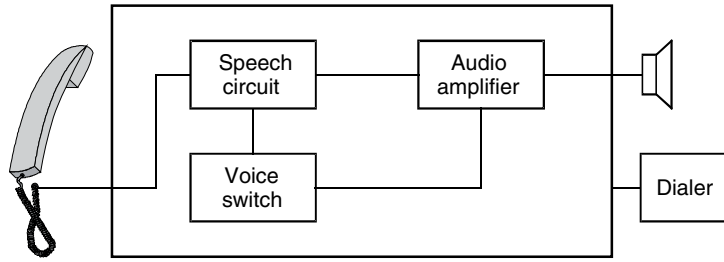
---

## Atmel U4089B-P

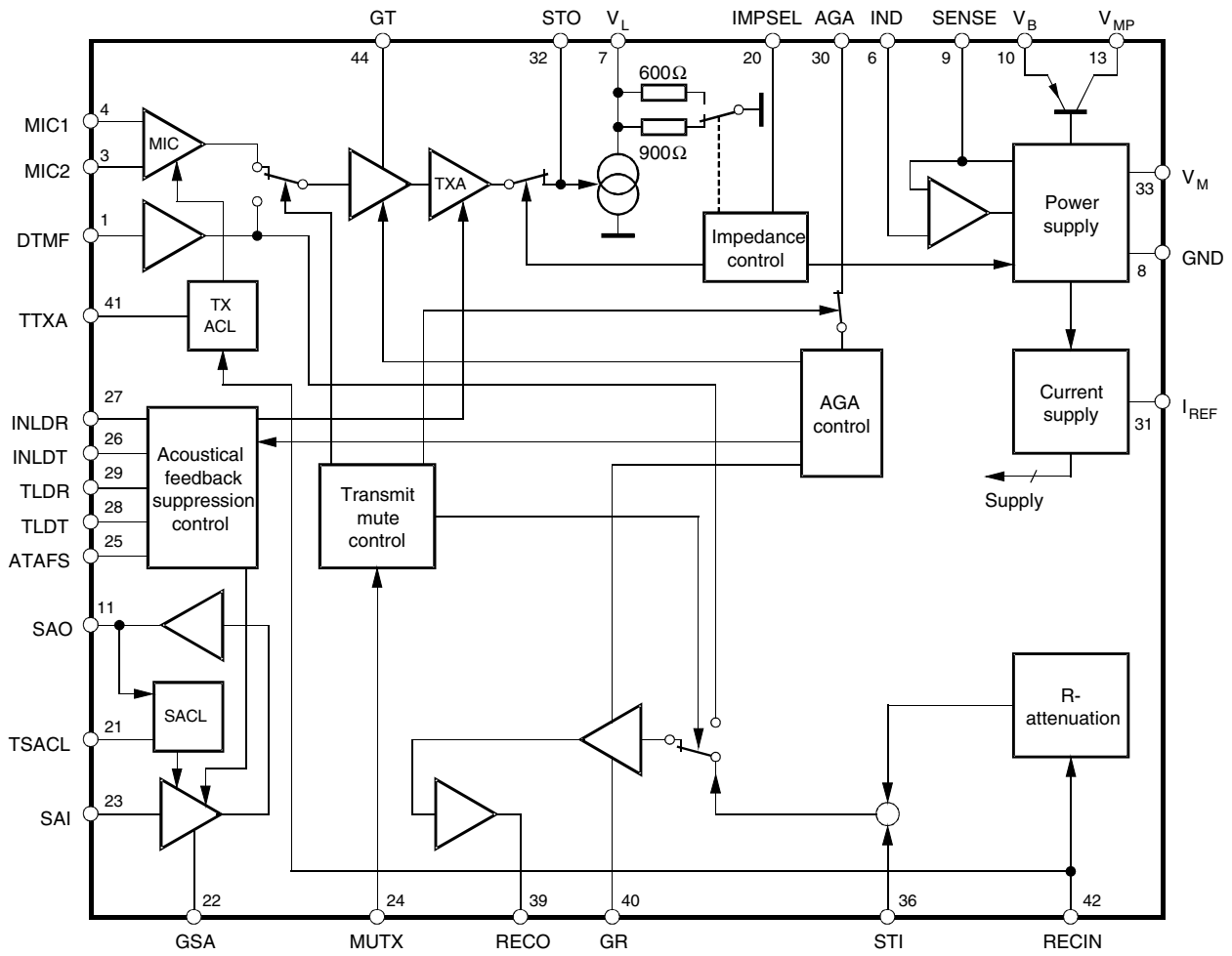
4518D-CORD-02/11



**Figure 1-1.** Simple Block Diagram



**Figure 1-2.** Block Diagram



## 2. Pin Configuration

Figure 2-1. Pinning SSO44

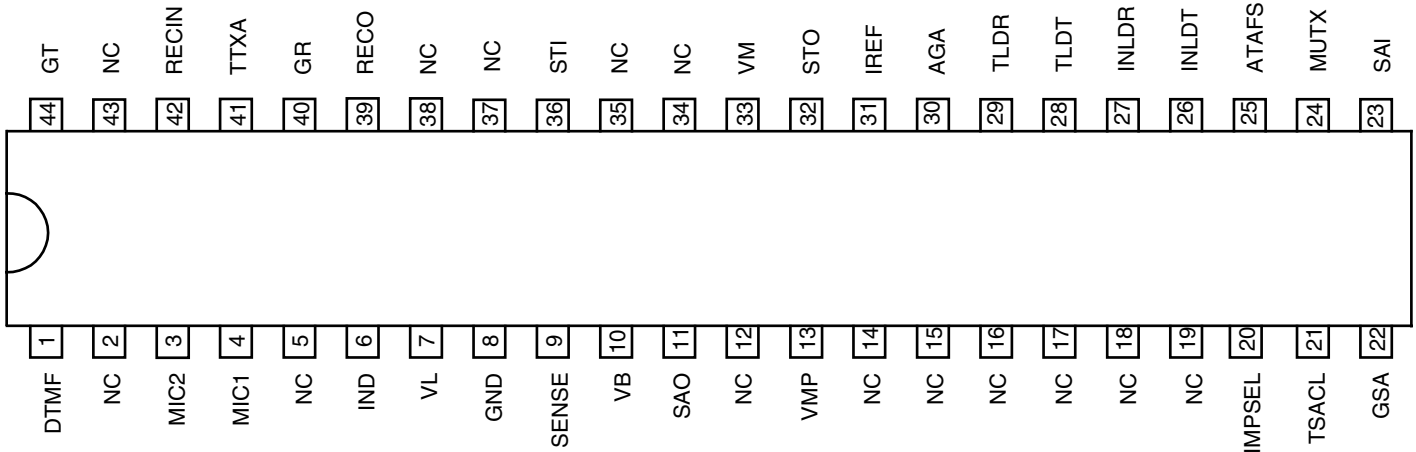


Table 2-1. Pin Description

Pin	Symbol	Function
1	DTMF	Input for DTMF signals. Also used for the answering machine and hands-free input.
3	MIC 2	Non-inverting input of microphone amplifier.
4	MIC 1	Inverting input of microphone amplifier.
6	IND	The internal equivalent inductance of the circuit is proportional to the value of the capacitor at this pin. A resistor connected to ground may be used to reduce the DC line voltage.
7	V <sub>L</sub>	Line voltage.
8	GND	Reference point for DC and AC output signals.
9	SENSE	A small resistor (fixed) connected from this pin to V <sub>L</sub> sets the slope of the DC characteristic, and affects the line-length equalization characteristics, and the line current at which the loudspeaker amplifier is switched on.
10	V <sub>B</sub>	Unregulated supply voltage for peripheral circuits (voice switch); limited to typically 7V.
11	SAO	Output of loudspeaker amplifier.
13	V <sub>MP</sub>	Regulated supply voltage 3.3V for peripheral circuits. The maximum output current is 2mA.
20	IMPSEL	Control input for selection of line impedance 1) 600Ω 2) 900Ω 3) Mute of second transmit stage (TXA); also used for indication of external supply (answering machine); last chosen impedance is stored.
21	TSACL	Time constant of anti-clipping of speaker amplifier.
22	GSA	Current input for setting the gain of the speaker amplifier. Adjustment characteristic is logarithmical. For RGSA > 2MΩ, the speaker amplifier is switched off.
23	SAI	Speaker amplifier input (for loudspeaker, tone ringer and hands-free use).

Note: Filters against electromagnetic interference (EMI) are located at pins MIC1, MIC2, RECIN and STI.

**Table 2-1.** Pin Description (Continued)

Pin	Symbol	Function
24	MUTX	Three-state input of transmit mute: 1) Speech condition; inputs MIC1/MIC2 active. 2) DTMF condition; input DTMF active. A part of the input signal is passed to the receiving amplifier as a confidence signal during dialing. 3) Input DTMF used for answering machine and hands-free use; receive branch is not affected.
25	ATAFS	Attenuation of acoustical feedback suppression. Maximum attenuation of AFS circuit is set by a resistor at this pin. Without the resistor, AFS is switched off.
26	INLDT	Input of transmit level detector.
27	INLDR	Input of receive level detector.
28	TLDT	Time constant of transmit level detector.
29	TLDR	Time constant of receive level detector.
30	AGA	Automatic gain adjustment with line current. A resistor connected from this pin to GND sets the starting point. Maximum gain change is 6 dB.
31	IREF	Internal reference current generation; $R_{REF} = 62k\Omega$ ; $I_{REF} = 20\mu A$
32	STO	Side-tone reduction output. Output resistance is approximately $300\Omega$ . Maximum load impedance is $10k\Omega$ .
33	$V_M$	Reference node for microphone-earphone and loudspeaker amplifier. Supply for electret microphone ( $I_M \geq 300mA$ ).
36	STI	Input for side-tone network.
39	RECO	Output of receiving amplifier.
40	$G_R$	A resistor connected from this pin to GND sets the receiving amplification of the circuit; amplifier RA1 can be muted by applying $V_{MP}$ to $G_R$ .
41	TTXA	Time constant of anti-clipping in transmit path.
42	RECI	Input of receiving path; input impedance is typically $80k\Omega$ .
44	$G_T$	A resistor from this pin to GND sets the amplification of microphone and DTMF signals; the input amplifier can be muted by applying $V_{MP}$ to $G_T$ .

Note: Filters against electromagnetic interference (EMI) are located at pins MIC1, MIC2, RECI and STI.

### 3. DC Line Interface and Supply-voltage Generation

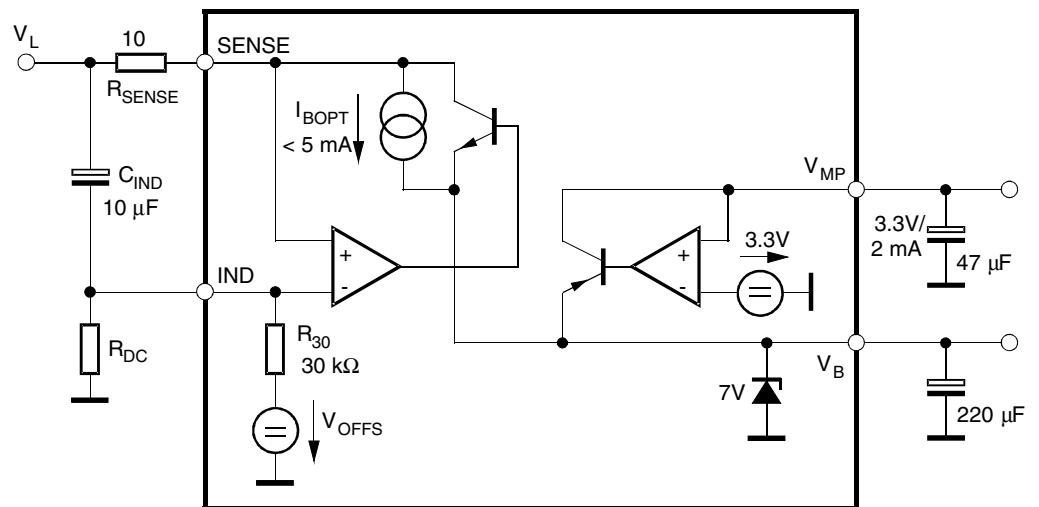
The DC line interface consists of an electronic inductance and an output stage which charges the capacitor at  $V_B$ . The value of the equivalent inductance is given by:

$$L = R_{SENSE} \times C_{IND} \times (R_{DC} \times R_{30}) / (R_{DC} + R_{30})$$

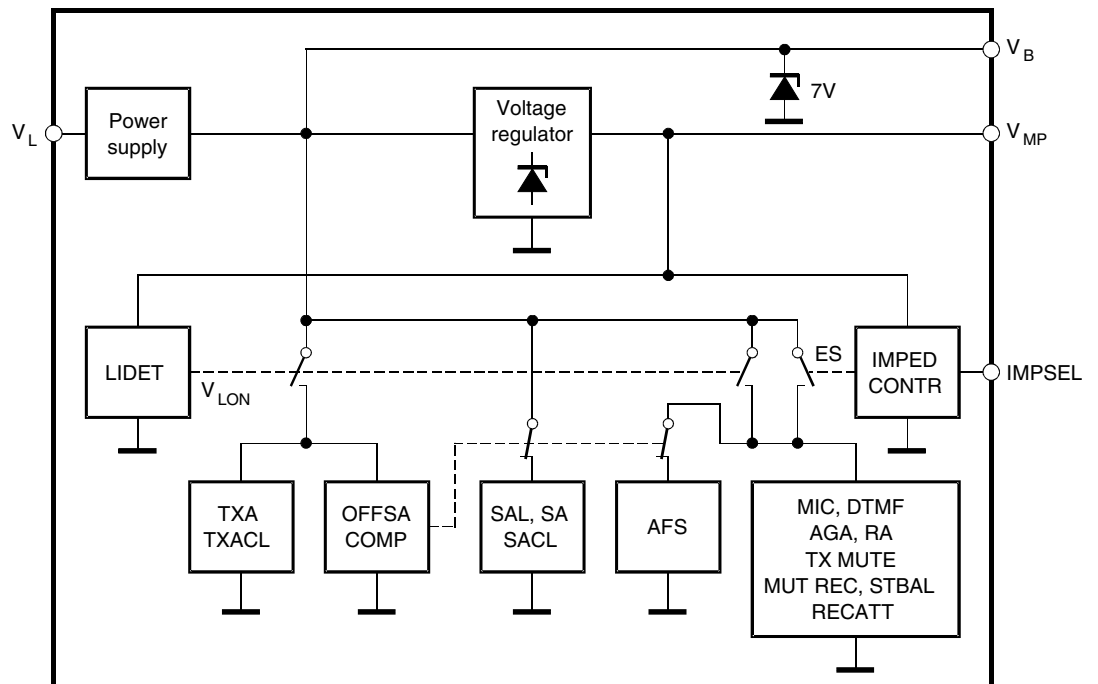
In order to improve the supply during worst-case operating conditions, the PNP current source ( $I_{BOPT}$ ) supplies an extra amount of current to the supply voltages when the NPN in parallel is unable to conduct current.

The Atmel® U4089B-P contains a series regulator which provides a supply voltage  $V_{MP}$  of 3.3V at 2mA suitable for a microprocessor.

**Figure 3-1.** DC Line Interface With Electronic Inductance and Generation of Regulated and Unregulated Supply



**Figure 3-2.** Functional Blocks for Power Supply



1. In speech condition, the system is supplied by the line current. If the LIDET block detects a line voltage above the fixed threshold (1.9V), the internal signal VLON is activated, thus switching on all blocks of the chip.

For line voltages below 1.9V, the switches remain in their quiescent state as shown in [Figure 3-2](#).

OFFSACOMP disables the group listening feature (SAI, SA, SACL, AFS) below line currents of approximately 10 mA.

2. Selecting IMPSEL = high impedance activates all switches on the ES line.

## 4. Acoustic Feedback Suppression

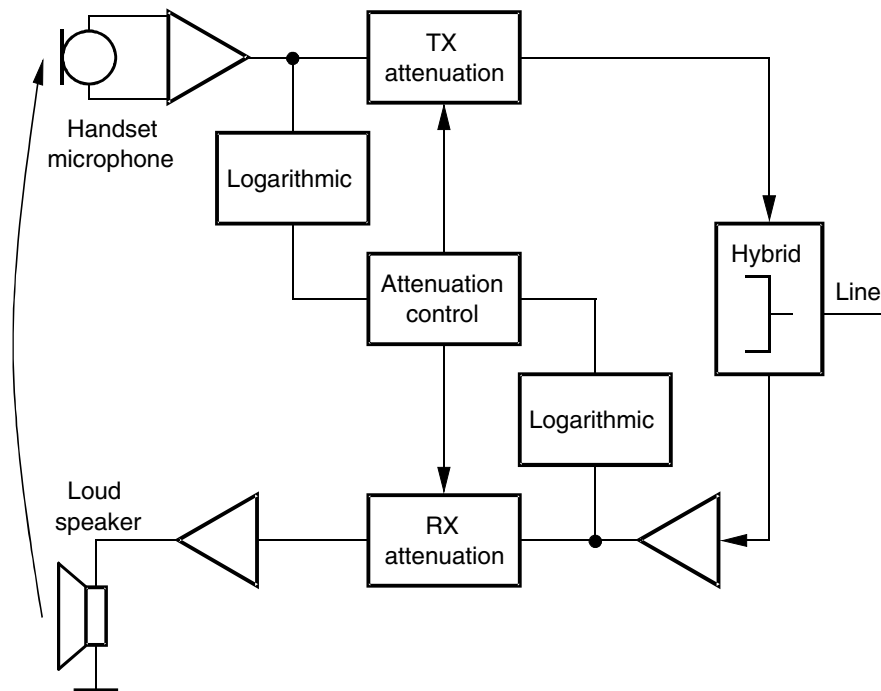
Acoustical feedback from the loudspeaker to the handset microphone may cause instability in the system. The Atmel® U4089B-P offers a very efficient feedback suppression circuit which uses a modified voice switch topology. [Figure 4-1](#) shows the basic system configuration.

Two attenuators (TX ATT and RX ATT) reduce the critical loop gain by introducing an externally adjustable amount of loss either in the transmit or in the receive path. The sliding control in block ATT CONTR determines whether the TX or the RX signal has to be attenuated. The overall loop gain remains constant under all operating conditions.

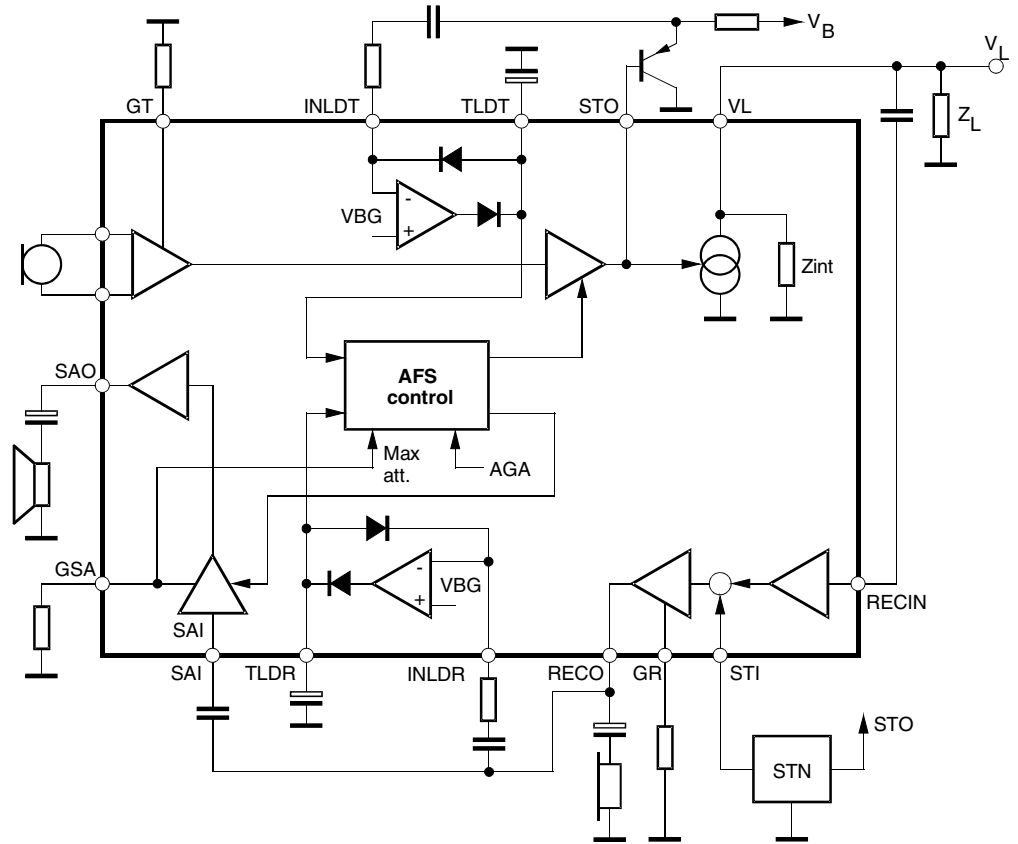
Selection of the active channel is made by comparison of the logarithmically compressed TX and RX envelope curves.

The system configuration for group listening, which is realized in the Atmel U4089B-P, is illustrated in [Figure 4-2](#). TXA and SAI represent the two attenuators; the logarithmic envelope detectors are shown in a simplified way (operational amplifiers with two diodes).

**Figure 4-1.** Basic Voice-switch System



**Figure 4-2.** Integration of the Acoustic Feedback-suppression Circuit Into the Speech Circuit Environment



**Figure 4-3.** Acoustic Feedback Suppression by Alternative Control of Transmit and Speaker Amplifier Gain

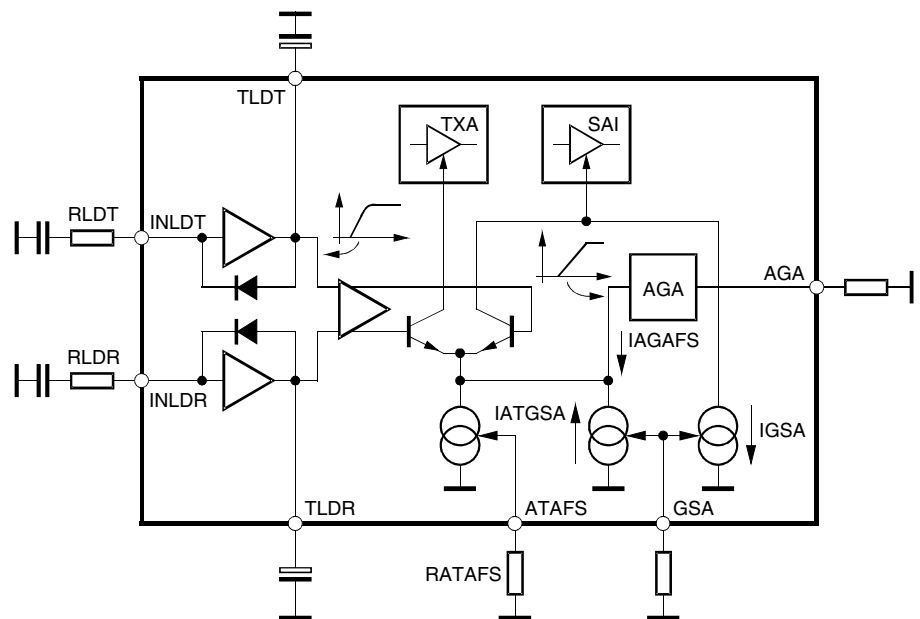


Figure 4-3 on page 7 provides a detailed diagram of the AFS (Acoustic Feedback Suppression). Receive and transmit signals are first processed by logarithmic rectifiers in order to produce the speech envelopes at TLDT and RLDT. After amplification, a decision is made by the differential pair which direction should be transmitted.

The attenuation of the controlled amplifiers TXA and SAI is determined by the emitter current  $I_{AT}$  which is comprised of three parts:

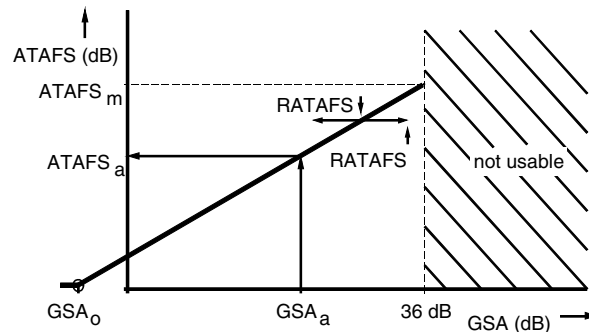
- $I_{ATAFS}$  sets maximum attenuation
- $I_{ATGSA}$  decreases the attenuation when speaker amplifier gain is reduced
- $I_{AGAFS}$  decreases the attenuation according to the loop-gain reduction caused by the AGA function

$$I_{AT} = I_{ATAFS} - I_{ATGSA} - I_{AGAFS}$$

$$\Delta G = I_{AT} \times 0.67 \text{ dB}/\mu\text{A}$$

Figure 4-4 illustrates the principal relationship between speaker amplifier gain (GSA) and attenuation of AFS (ATAFS). Both parameters can be adjusted independently, but the internal coupling between them has to be considered. The maximum GSA value to be used is 36 dB. The shape of the characteristic is moved in the x-direction by adjusting resistor RATAFS, thus changing  $ATAFS_m$ . The actual value of the attenuation ( $ATAFS_a$ ), however, can be determined by reading the value which belongs to the actual gain  $GSA_a$ . If the speaker amplifier gain is reduced, the attenuation of AFS is automatically reduced by the same amount in order to achieve a constant loop gain. Zero attenuation is set for speaker gains  $GSA \geq GSA_0 = 36\text{dB} - ATAFS_m$ .

**Figure 4-4.** Reducing Speaker Amplifier Gain Results in an Equal Reduction of AFS Attenuation





## 5. Operating Range of Speaker Amplifier

The basic behavior is illustrated in Figure 5-1. Actual values of  $I_{L_{ON}}/I_{L_{OFF}}$  vary slightly with the adjustment of the DC characteristics and the selection of the internal line impedance.

Figure 5-1. Threshold of Speaker Amplifier

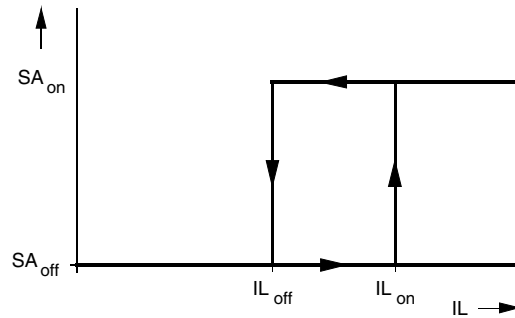
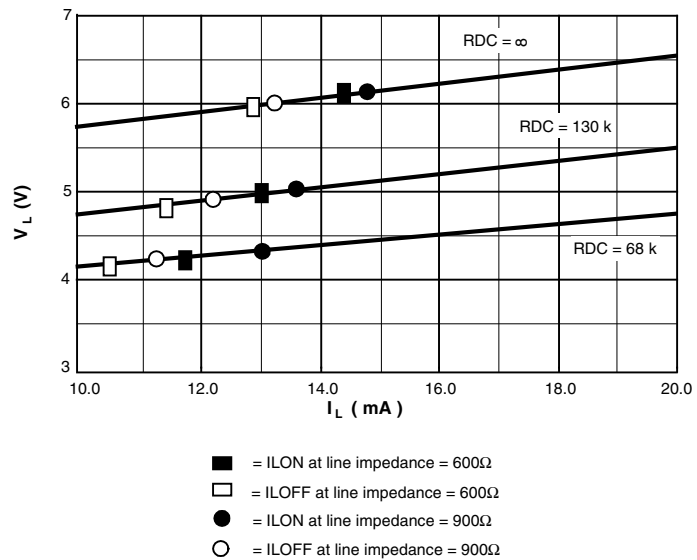


Figure 5-2. Comparator Thresholds Depend on DC Mask and Line Impedance



## 6. Absolute Maximum Ratings

Parameters	Symbol	Value	Unit
Line current	$I_L$	140	mA
DC line voltage	$V_L$	12	V
Junction temperature	$T_j$	125	°C
Ambient temperature	$T_{amb}$	-25 to +75	°C
Storage temperature	$T_{stg}$	-55 to +150	°C
Total power dissipation, $T_{amb} = 60^\circ\text{C}$ , SSO44	$P_{tot}$	0.9	W
ESD (Human Body Model), ESD S 5.1 Standard	$V_{ESD}$	1.5	kV
ESD (Machine Model), JEDEC A115A	$V_{ESD}$	150	V

## 7. Thermal Resistance

Parameters	Symbol	Value	Unit
Junction ambient	$R_{thJA}$	70	K/W

## 8. Electrical Characteristics

$f = 1\text{kHz}$ ,  $0\text{dBm} = 775\text{mV}_{\text{rms}}$ ,  $I_M = 0.3\text{mA}$ ,  $I_{MP} = 2\text{mA}$ ,  $RDC = 130\text{k}\Omega$ ,  $T_{amb} = 25^\circ\text{C}$ ,  $RGSA = 560\text{k}\Omega$ ,  $Z_{ear} = 68\text{nF} + 100\Omega$ ,  $Z_M = 68\text{nF}$ , Pin 30 open (AGA),  $V_{MUTX} = \text{GND}$ , see [Figure 8-11 on page 22](#) (AC tests), unless otherwise specified.

Parameters	Test Conditions	Pin	Symbol	Min.	Typ.	Max.	Unit	
DC Characteristics (see <a href="#">Figure 8-8 on page 19</a> )								
DC voltage drop over circuit	$I_L = 2\text{mA}$		$V_L$	4.6	2.4	5.4	V	
	$I_L = 14\text{mA}$						5.0	V
	$I_L = 60\text{mA}$						7.5	V
	$I_L = 100\text{mA}$						9.4	10.0
<b>Transmission Amplifier, <math>I_L = 14\text{mA}</math>, <math>V_{MIC} = 2\text{mV}</math>, <math>RGT = 27\text{k}\Omega</math>, unless otherwise specified</b>								
Adjustment range of transmit gain			$G_T$	40	45	50	dB	
Transmitting amplification	$RGT = 12\text{k}\Omega$ $RGT = 27\text{k}\Omega$		$G_T$	47 39.8	48	49 41.8	dB	
Frequency response	$I_L \geq 14\text{mA}$ , $f = 300\text{Hz}$ to $3400\text{Hz}$		$\Delta G_T$			$\pm 0.5$	dB	
Gain change with current	Pin 30 open (AGA), $I_L = 14\text{mA}$ to $100\text{mA}$		$\Delta G_T$			$\pm 0.5$	dB	
Gain deviation	$T_{amb} = -10^\circ\text{C}$ to $+60^\circ\text{C}$		$\Delta G_T$			$\pm 0.5$	dB	
CMRR of microphone amplifier			CMRR	60	80		dB	
Input resistance of MIC amplifier	$RGT = 12\text{k}\Omega$ $RGT = 27\text{k}\Omega$		$R_i$	45	50 75	110	$\text{k}\Omega$	
Distortion at line	$I_L > 14\text{mA}$ $V_L = 700\text{mV}_{\text{rms}}$		$d_t$			2	%	
Maximum output voltage	$I_L > 19\text{mA}$ , $d < 5\%$ $V_{mic} = 25\text{mV}$ $CTXA = 1\mu\text{F}$		$V_{Lmax}$	1.8	3	4.2	dBm	

## 8. Electrical Characteristics (Continued)

f = 1kHz, 0dBm = 775mV<sub>rms</sub>, I<sub>M</sub> = 0.3mA, I<sub>MP</sub> = 2mA, RDC = 130kΩ, T<sub>amb</sub> = 25°C, RGSA = 560kΩ, Z<sub>ear</sub> = 68nF + 100Ω, Z<sub>M</sub> = 68nF, Pin 30 open (AGA), V<sub>MUTX</sub> = GND, see [Figure 8-11 on page 22](#) (AC tests), unless otherwise specified.

Parameters	Test Conditions	Pin	Symbol	Min.	Typ.	Max.	Unit
Noise at line psophometrically weighted	I <sub>L</sub> > 14mA G <sub>T</sub> = 48dB		n <sub>o</sub>		-80	-72	dBmp
Anti-clipping attack time release time	CTXA = 1μF each 3dB overdrive				0.5 9		ms ms
Gain at low operating current	I <sub>L</sub> = 10mA I <sub>MP</sub> = 1mA RDC = 68kΩ V <sub>mic</sub> = 1mV I <sub>M</sub> = 300μA		G <sub>T</sub>	40		42.5	dB
Distortion at low operating current	I <sub>L</sub> = 10mA I <sub>M</sub> = 300μA I <sub>MP</sub> = 1mA RDC = 68kΩ V <sub>mic</sub> = 10mV		d <sub>t</sub>			5	%
Line-loss compensation	I <sub>L</sub> = 100mA, RAGA = 20kΩ		ΔG <sub>TI</sub>	-6.4	-5.8	-5.2	dB
Mute suppression a) MIC muted (microphone preamplifier)	I <sub>L</sub> ≥ 14mA Mutx = open		G <sub>TM</sub>	60	80		dB
<b>Receiving Amplifier, I<sub>L</sub> = 14mA, RGR = 62kΩ, unless otherwise specified, V<sub>GEN</sub> = 300mV</b>							
Adjustment range of receiving gain	I <sub>L</sub> ≥ 14mA, single ended		G <sub>R</sub>	-8		+2	dB
Receiving amplification	RGR = 62kΩ RGR = 22kΩ		G <sub>R</sub>	-7.75	-7 1.5	-6.25	dB
Amplification of DTMF signal from DTMF IN to RECO	I <sub>L</sub> ≥ 14mA V <sub>MUTX</sub> = V <sub>MP</sub>		G <sub>RM</sub>	1	4	7	dB
Frequency response	I <sub>L</sub> > 14mA, f = 300Hz to 3400Hz		ΔG <sub>RF</sub>			±0.5	dB
Gain change with current	I <sub>L</sub> = 14mA to 100mA		ΔG <sub>R</sub>			±0.5	dB
Gain deviation	T <sub>amb</sub> = -10° C to +60° C		ΔG <sub>R</sub>			±0.5	dB
Ear protection	I <sub>L</sub> ≥ 14mA V <sub>GEN</sub> = 11V <sub>rms</sub>		EP			1.1	V <sub>rms</sub>
MUTE suppression DTMF operation	I <sub>L</sub> ≥ 14mA V <sub>MUTX</sub> = V <sub>MP</sub>		ΔG <sub>R</sub>	60			dB
Output voltage d ≤ 2%	I <sub>L</sub> = 14mA Z <sub>ear</sub> = 68nF			0.5			V <sub>rms</sub>
Maximum output current d ≤ 2%	Z <sub>ear</sub> = 100Ω			4			mA (peak)
Receiving noise psophometrically weighted	Z <sub>ear</sub> = 68nF + 100Ω I <sub>L</sub> ≥ 14mA		n <sub>i</sub>		-80	-77	dBmp
Output resistance	Output against GND		R <sub>o</sub>			10	Ω
Line-loss compensation	RAGA = 20kΩ I <sub>L</sub> = 100mA		ΔG <sub>RI</sub>	-7.0	-6.0	-5.0	dB
AC impedance			Z <sub>imp</sub>	840	900	960	Ω



## 8. Electrical Characteristics (Continued)

$f = 1\text{kHz}$ ,  $0\text{dBm} = 775\text{mV}_{\text{rms}}$ ,  $I_M = 0.3\text{mA}$ ,  $I_{MP} = 2\text{mA}$ ,  $RDC = 130\text{k}\Omega$ ,  $T_{\text{amb}} = 25^\circ\text{C}$ ,  $RGSA = 560\text{k}\Omega$ ,  $Z_{\text{ear}} = 68\text{nF} + 100\Omega$ ,  $Z_M = 68\text{nF}$ , Pin 30 open (AGA),  $V_{\text{MUTX}} = \text{GND}$ , see [Figure 8-11 on page 22](#) (AC tests), unless otherwise specified.

Parameters	Test Conditions	Pin	Symbol	Min.	Typ.	Max.	Unit
Gain at low operating current	$I_L = 10\text{mA}$ $I_{MP} = 1\text{mA}$ $I_M = 300\mu\text{A}$ $V_{\text{GEN}} = 560\text{mV}$ $RDC = 68\text{k}\Omega$		$G_R$	-8	-7	-6	dB
Distortion at low operating current	$I_L = 10\text{mA}$ $I_{MP} = 1\text{mA}$ $V_{\text{GEN}} = 560\text{mV}$ $RDC = 68\text{k}\Omega$		$d_R$			5	%
<b>Speaker Amplifier</b>							
Minimum line current for operation	No AC signal		$I_{L\text{min}}$			15	mA
Input resistance		23		14		22	k $\Omega$
Gain from SAI to SAO	$V_{\text{SAI}} = 3\text{mV}$ , $I_L = 15\text{mA}$ , $RGSA = 560\text{k}\Omega$ $RGSA = 20\text{k}\Omega$		$G_{\text{SA}}$	35.5	36.5 -3	37.5	dB dB
Output power	Load resistance $R_L = 50\Omega$ , $d < 5\%$ $V_{\text{SAI}} = 20\text{mV}$ $I_L = 15\text{mA}$ $I_L = 20\text{mA}$		$P_{\text{SA}}$ $P_{\text{SA}}$	3	7 20		mW mW
Output noise (input SAI open) psophometrically weighted	$I_L > 15\text{mA}$		$n_{\text{SA}}$			200	$\mu\text{V}_{\text{psoph}}$
Gain deviation	$I_L = 15\text{mA}$ $T_{\text{amb}} = -10^\circ\text{C}$ to $+60^\circ\text{C}$		$\Delta G_{\text{SA}}$			$\pm 1$	dB
Mute suppression	$I_L = 15\text{mA}$ , $V_L = 0\text{dBm}$ , $V_{\text{SAI}} = 4\text{mV}$ Pin 23 open		$V_{\text{SAO}}$			-60	dBm
Gain change with current	$I_L = 15\text{mA}$ to $100\text{mA}$		$\Delta G_{\text{SA}}$			$\pm 1$	dB
Resistor for turning off speaker amplifier	$I_L = 15\text{mA}$ to $100\text{mA}$		$RG_{\text{SA}}$	0.8	1.3	2	M $\Omega$
Gain change with frequency	$I_L = 15\text{mA}$ $f = 300\text{Hz}$ to $3400\text{Hz}$		$\Delta G_{\text{SA}}$			$\pm 0.5$	dB
Attack time of anti-clipping	20dB overdrive		$t_r$		5		ms
Release time of anti-clipping			$t_f$		80		ms
<b>TMF Amplifier Test Conditions: <math>I_{MP} = 2\text{mA}</math>, <math>I_M = 0.3\text{mA}</math>, <math>V_{\text{MUTX}} = \text{VMP}</math></b>							
Adjustment range of DTMF gain	$I_L = 15\text{mA}$ Mute active		$G_D$	40		50	dB
DTMF amplification	$I_L = 15\text{mA}$ , $V_{\text{DTMF}} = 8\text{mV}$ Mute active: $\text{MUTX} = \text{VMP}$		$G_D$	40.7	41.7	42.7	dB
Gain deviation	$I_L = 15\text{mA}$ $T_{\text{amb}} = -10^\circ\text{C}$ to $+60^\circ\text{C}$		$G_D$			$\pm 0.5$	dB

8. Electrical Characteristics (Continued)

f = 1kHz, 0dBm = 775mV<sub>rms</sub>, I<sub>M</sub> = 0.3mA, I<sub>MP</sub> = 2mA, RDC = 130kΩ, T<sub>amb</sub> = 25°C, RGSA = 560kΩ, Z<sub>ear</sub> = 68nF + 100Ω, Z<sub>M</sub> = 68nF, Pin 30 open (AGA), V<sub>MUTX</sub> = GND, see Figure 8-11 on page 22 (AC tests), unless otherwise specified.

Parameters	Test Conditions	Pin	Symbol	Min.	Typ.	Max.	Unit
Input resistance	RGT = 27kΩ RGT = 15kΩ		R <sub>i</sub>	60 26	180 70	300 130	kΩ
Distortion of DTMF signal	I <sub>L</sub> ≥ 15mA V <sub>L</sub> = 0dBm		d <sub>D</sub>			2	%
Gain deviation with current	I <sub>L</sub> = 15mA to 100mA		ΔGD			±0.5	dB
<b>AFS Acousting Feedback Suppression</b>							
Range of attenuation	I <sub>L</sub> ≥ 15mA			0		50	dB
Attenuation of transmit gain	I <sub>L</sub> ≥ 15mA, I <sub>INLDT</sub> = 0μA R <sub>ATAFS</sub> = 30kΩ I <sub>INLDR</sub> = 10μA		ΔG <sub>T</sub>		45		dB
Attenuation of speaker amplifier	I <sub>L</sub> ≥ 15mA I <sub>INLDP</sub> = 0μA R <sub>AT AFS</sub> = 30kΩ I <sub>INLDR</sub> = 10μA		ΔG <sub>SA</sub>		50		dB
AFS disable	I <sub>L</sub> ≥ 15 mA		V <sub>ATAFS</sub>	1.5			V
<b>Supply Voltages, V<sub>mic</sub> = 25mV, T<sub>amb</sub> = -10°C to +60°C</b>							
V <sub>MP</sub>	I <sub>L</sub> = 14mA, RDC = 68kΩ I <sub>MP</sub> = 2mA		V <sub>MP</sub>	3.1	3.3	3.5	V
V <sub>M</sub>	I <sub>L</sub> ≥ 14mA, I <sub>M</sub> = 300μA RDC = 130kΩ		V <sub>M</sub>	1.4		3.3	V
V <sub>B</sub>	I <sub>B</sub> = +20mA, I <sub>L</sub> = 0mA		V <sub>B</sub>		7	7.6	V
<b>MUTX Input (see Figure 8-9 on page 20)</b>							
Input current	V <sub>MUTX</sub> = V <sub>MP</sub> V <sub>MUTX</sub> = GND		I <sub>MUTX</sub> I <sub>MUTX</sub>		+20 -20	+30 -30	μA μA
Input voltage	Input high		V <sub>MUTX</sub>	V <sub>MP</sub> - 0.3V			V
	Input low		V <sub>MUTX</sub>			0.3	V



**Table 8-1.** Atmel® U4089B-P Control

MUTX		MODE
0	MIC1, MIC2 transmit enabled receive enable AFS = on AGA = on TXACL = on	Speech
Z	DTMF transmit enabled receive enable AFS = on AGA = on TXACL = on	For answering machine
1	DTMF transmit enabled DTMF to receive enable AFS = off AGA = off TXACL = off	DTMF dialling

IMPSEL		MODE
0	Line impedance = 600Ω TXA = on ES = off	Speech
0 to Z	Line impedance = 600Ω TXA = off ES = on	Transmit mute
1 to Z	Line impedance = 900Ω TXA = off ES = on	Transmit mute
1	Line impedance = 900Ω TXA = on ES = off	Speech

Logic level
0 = < (0.3V)
Z = > (1V) < (V <sub>MP</sub> - 1V) or (open input)
1 = > (V <sub>MP</sub> - 0.3V)

AFS = Acoustical feedback-suppression control  
 AGA = Automatic gain adjustment  
 TXACL = Transmit anti-clipping control  
 ES = External supply

Figure 8-1. Typical DC Characteristic

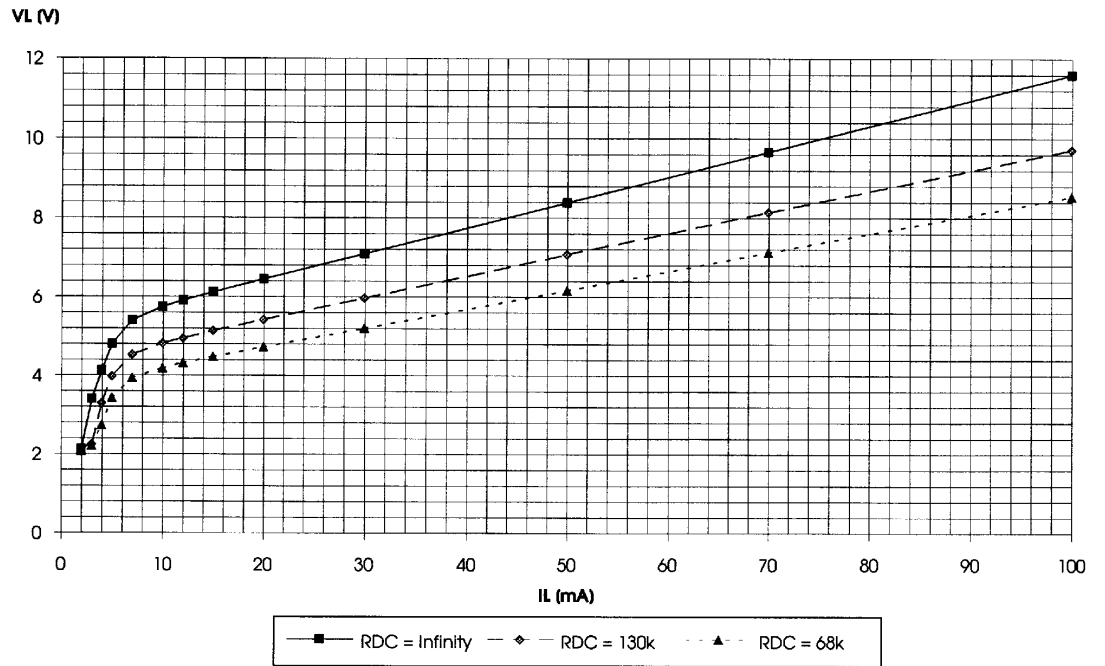
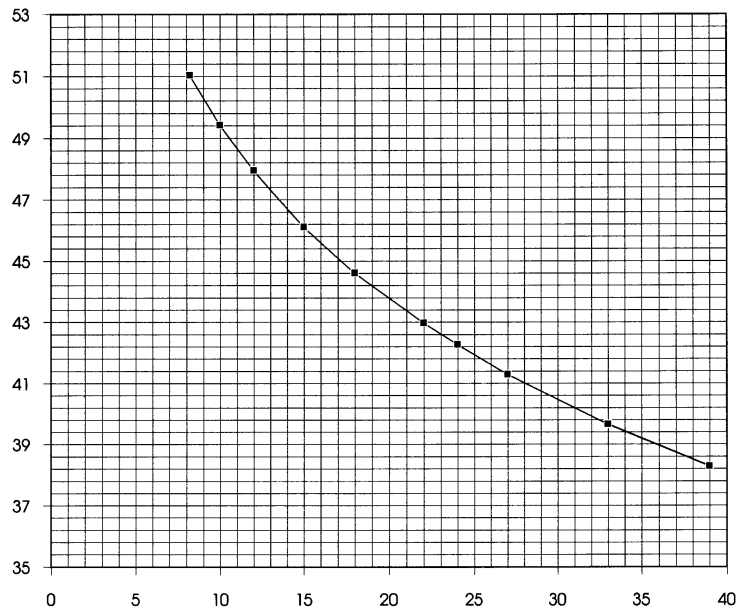
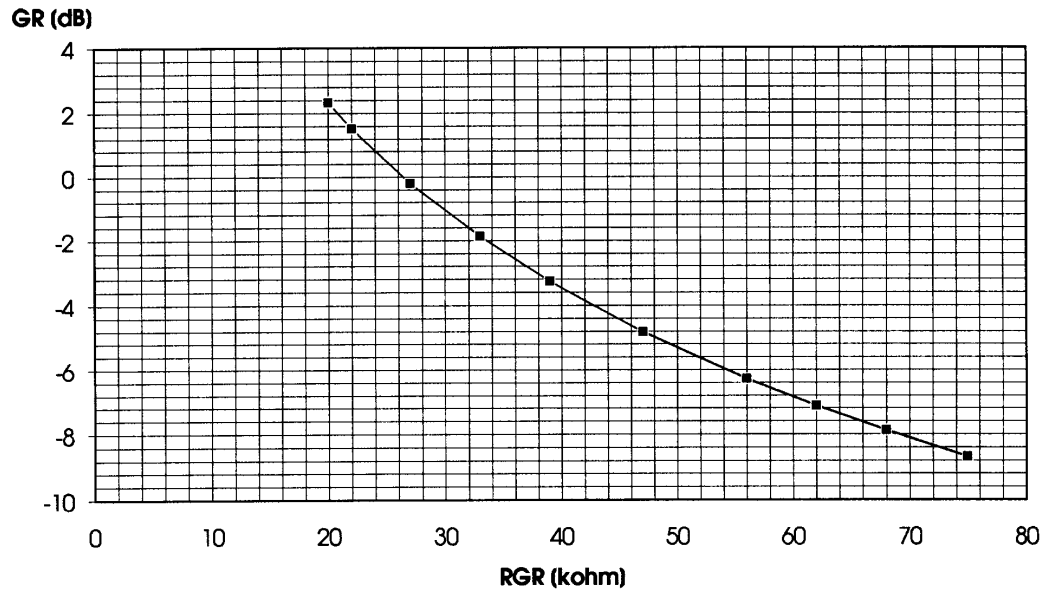


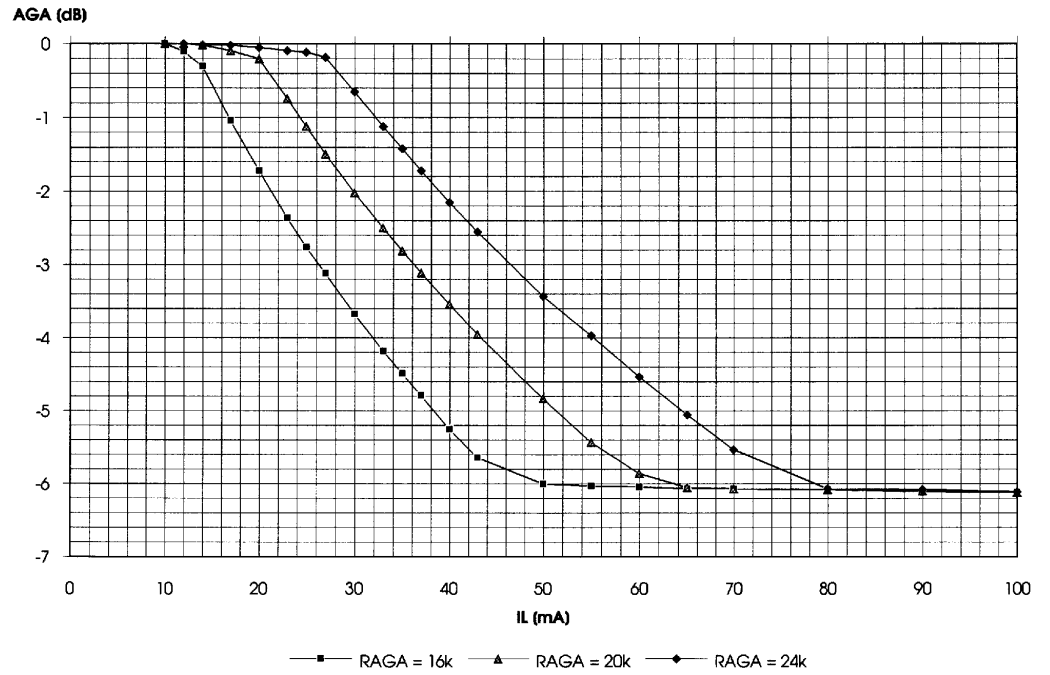
Figure 8-2. Typical Adjustment Range of the Transmit Gain



**Figure 8-3.** Typical Adjustment Range of the Receive Gain

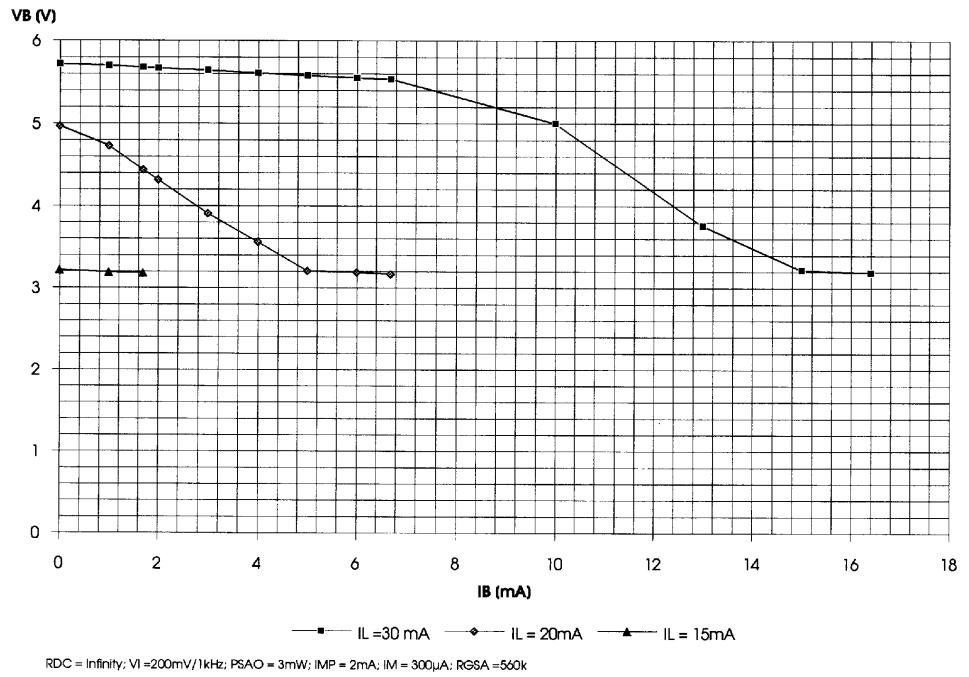


**Figure 8-4.** Typical AGA Characteristic

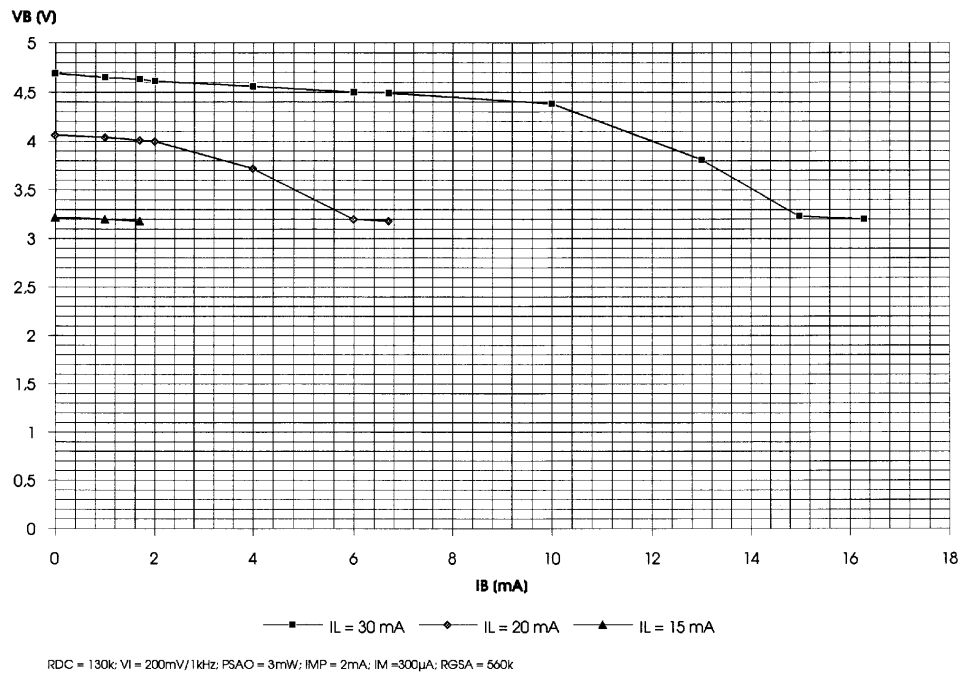




**Figure 8-5.** Typical Load Characteristic of  $V_B$  for a Maximum (RDC = infinity) DC Characteristic and a 3-mW Loudspeaker Output



**Figure 8-6.** Typical Load Characteristic of  $V_B$  for a Medium DC Characteristic (RDC = 130k $\Omega$ ) and a 3-mW Loudspeaker Output



**Figure 8-7.** Typical Load Characteristic of  $V_B$  for a Minimum DC Characteristic (RDC = 68k $\Omega$ ) and a 3-mW Loudspeaker Output

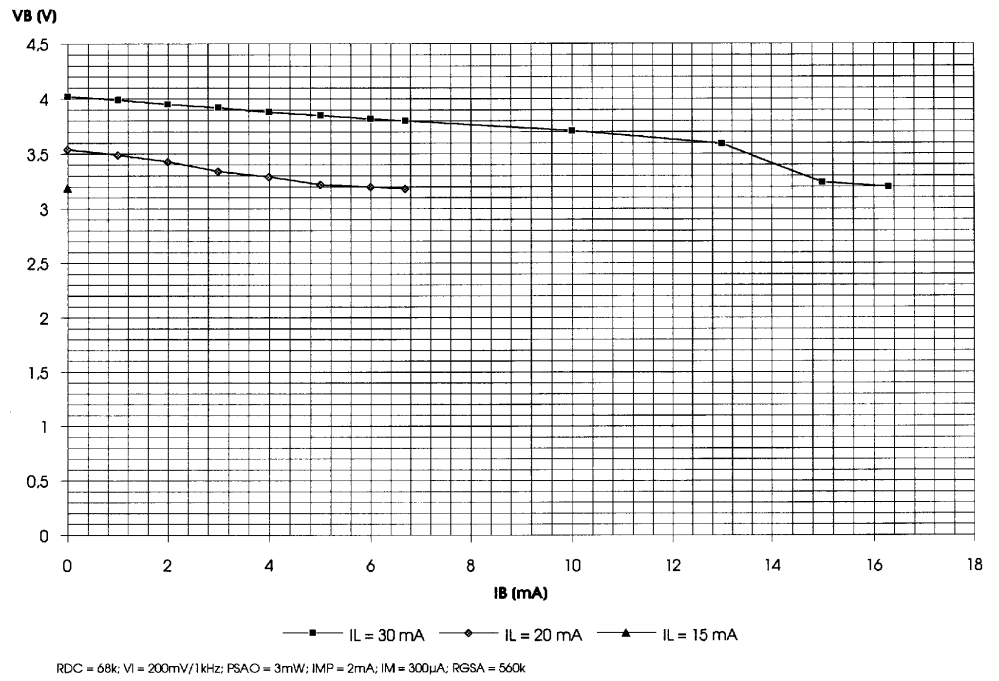
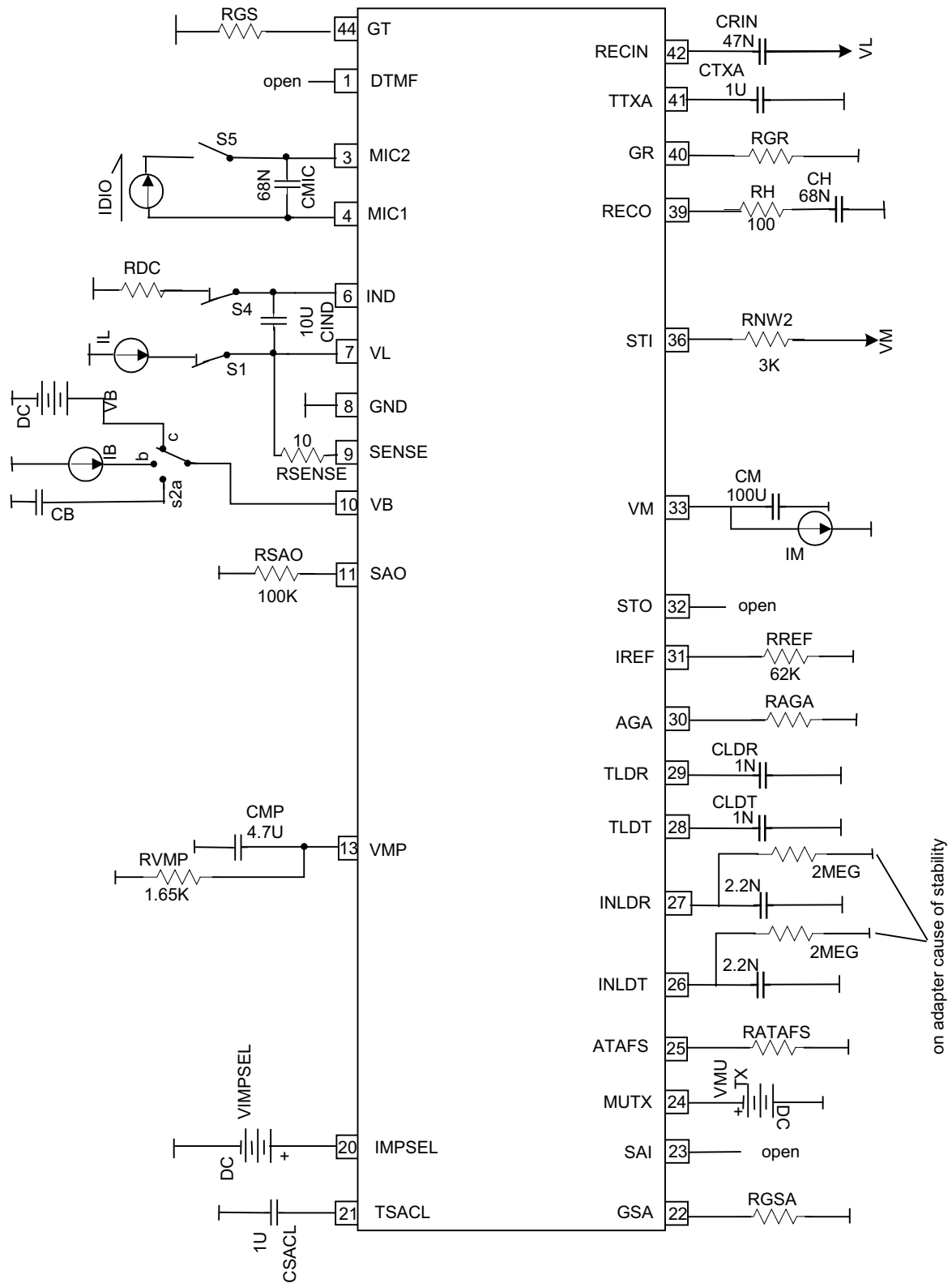


Figure 8-8. DC Voltage Absolute



**Figure 8-9. DC Voltage Current Test**

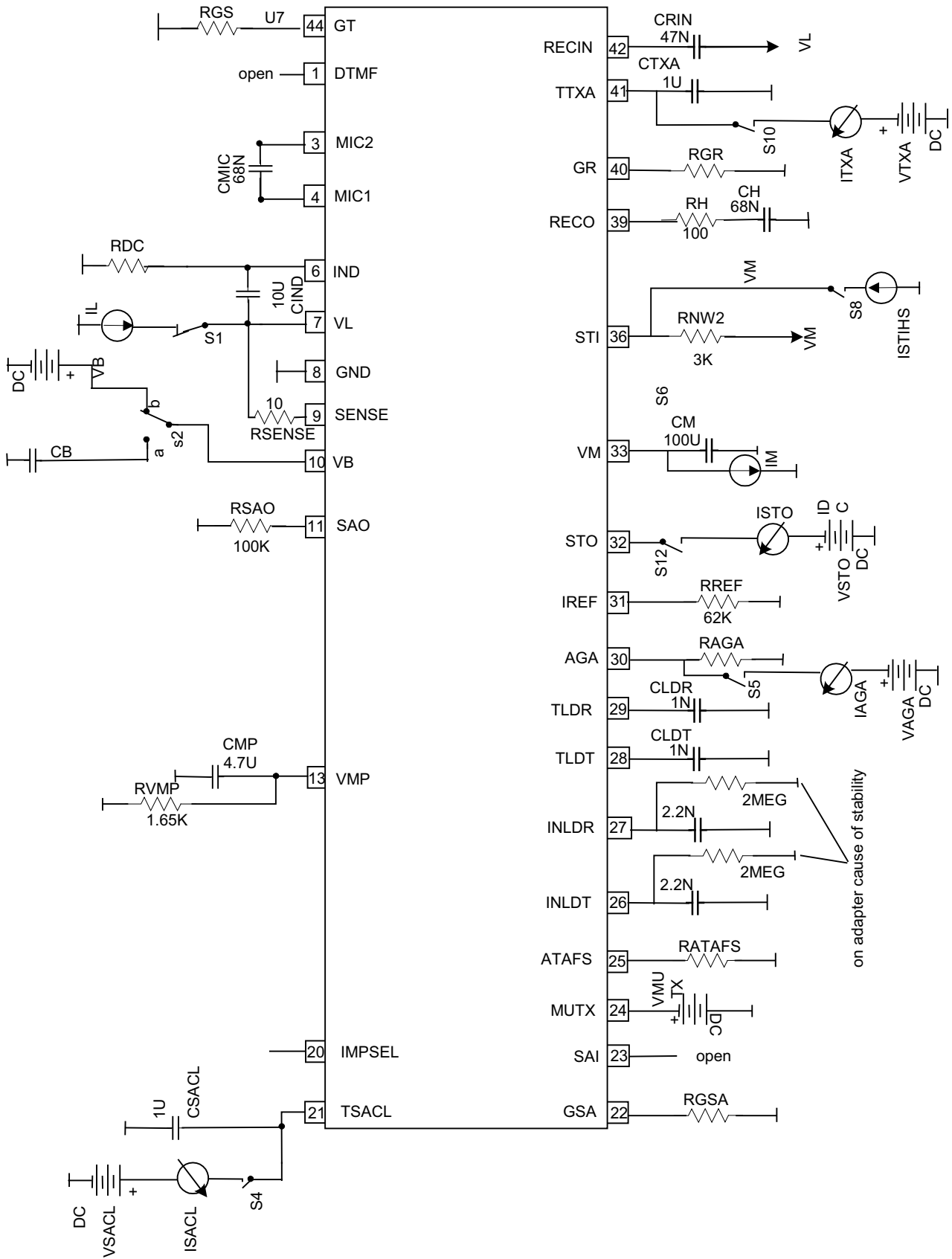


Figure 8-10. DC Ramps

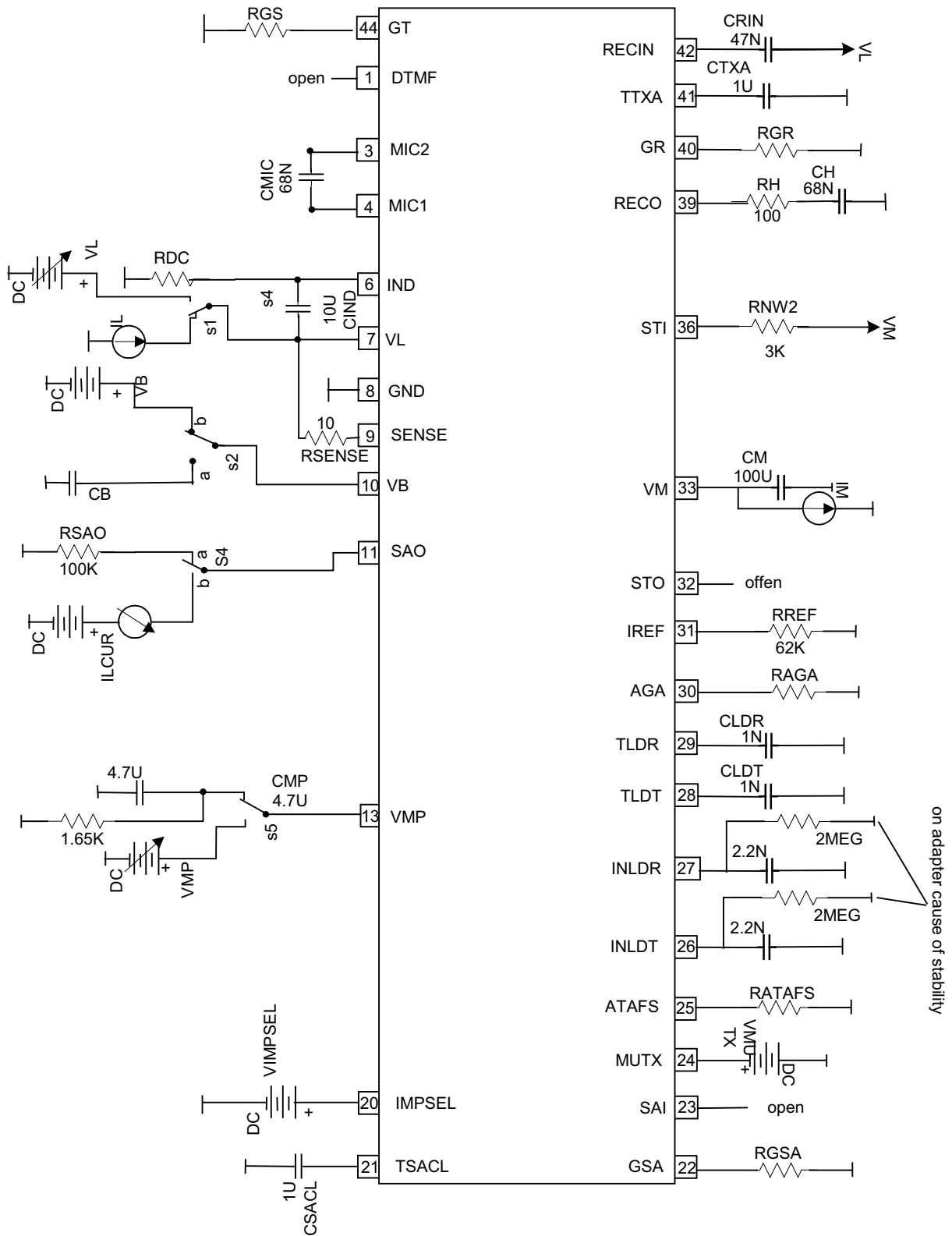
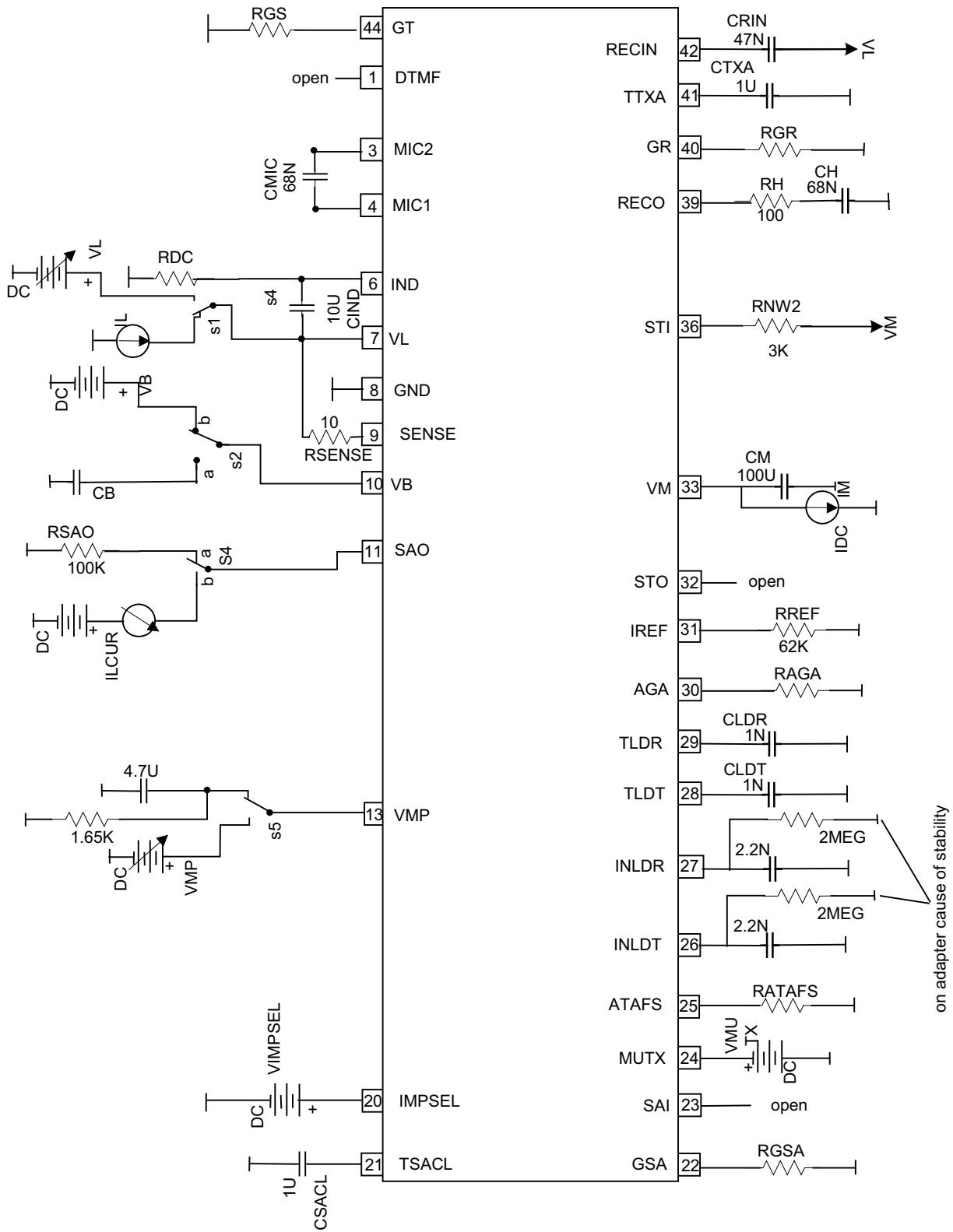


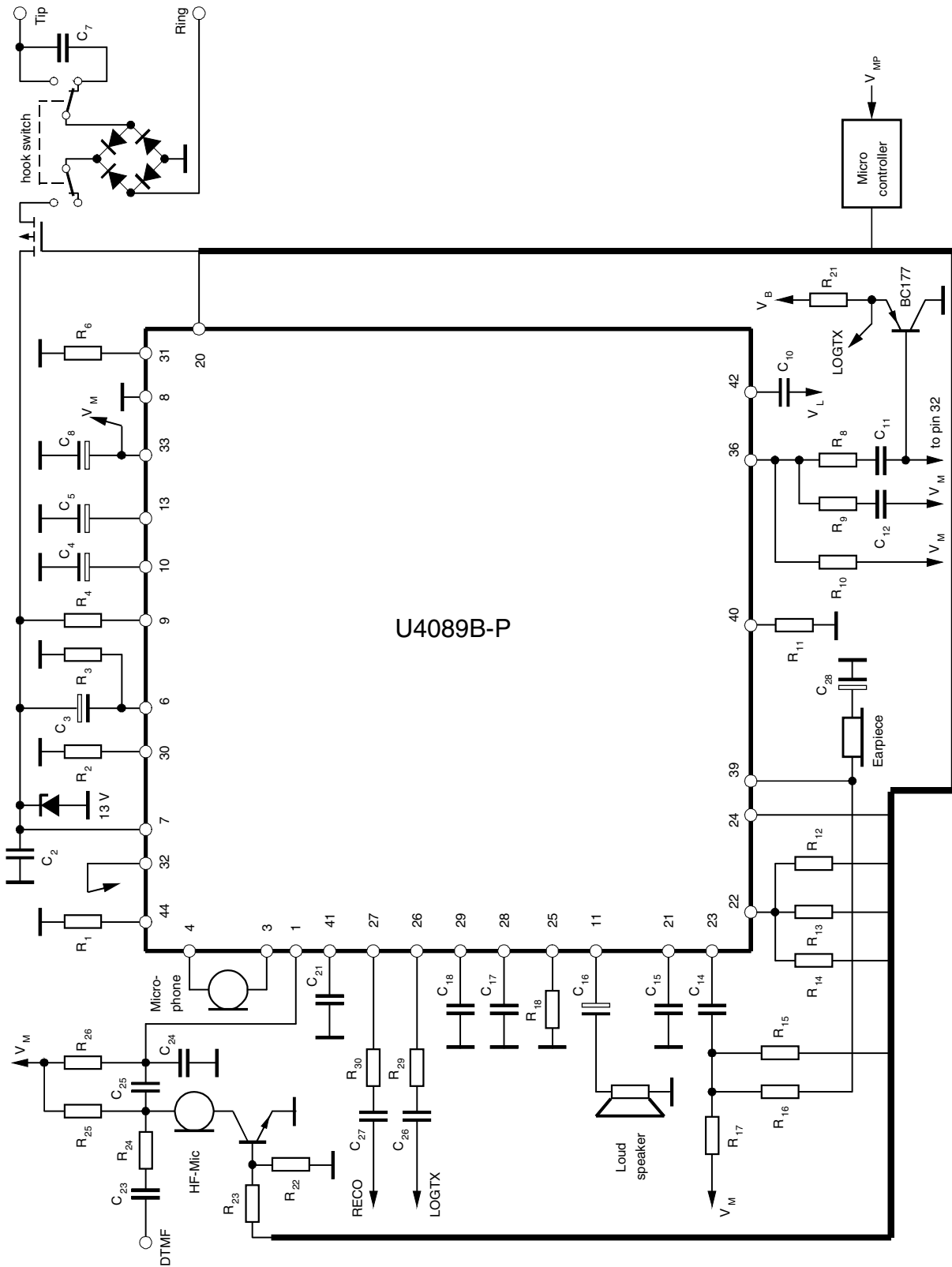
Figure 8-11. AC Tests



**Table 8-2.** Typical Values of External Components (see [Figure 8-12 on page 24](#))

Name	Value	Name	Value	Name	Value	Name	Value
C <sub>2</sub>	4.7nF	C <sub>16</sub>	47μF	R <sub>3</sub>	>68kΩ	R <sub>16</sub>	1kΩ
C <sub>3</sub>	10μF	C <sub>17</sub>	10μF	R <sub>4</sub>	10kΩ	R <sub>17</sub>	1.2kΩ
C <sub>4</sub>	220μF	C <sub>18</sub>	10μF	R <sub>6</sub>	62kΩ	R <sub>18</sub>	30kΩ
C <sub>5</sub>	47μF	C <sub>21</sub>	1μF	R <sub>8</sub>	22kΩ	R <sub>21</sub>	15kΩ
C <sub>7</sub>	1μF	C <sub>23</sub>	6.8nF	R <sub>9</sub>	330kΩ	R <sub>22</sub>	330kΩ
C <sub>8</sub>	100μF	C <sub>24</sub>	10nF	R <sub>10</sub>	3kΩ	R <sub>23</sub>	220kΩ
C <sub>10</sub>	150nF	C <sub>25</sub>	100nF	R <sub>11</sub>	62kΩ	R <sub>24</sub>	68kΩ
C <sub>11</sub>	68nF	C <sub>26</sub>	470nF	R <sub>12</sub>	30kΩ	R <sub>25</sub>	2kΩ
C <sub>12</sub>	33nF	C <sub>27</sub>	33nF	R <sub>13</sub>	62kΩ	R <sub>26</sub>	3.3kΩ
C <sub>14</sub>	100nF	C <sub>28</sub>	10μF	R <sub>14</sub>	120kΩ	R <sub>29</sub>	1kΩ
C <sub>15</sub>	1μF	R <sub>2</sub>	20kΩ	R <sub>15</sub>	47kΩ	R <sub>30</sub>	12kΩ

**Figure 8-12.** Application for Hands-free Operation





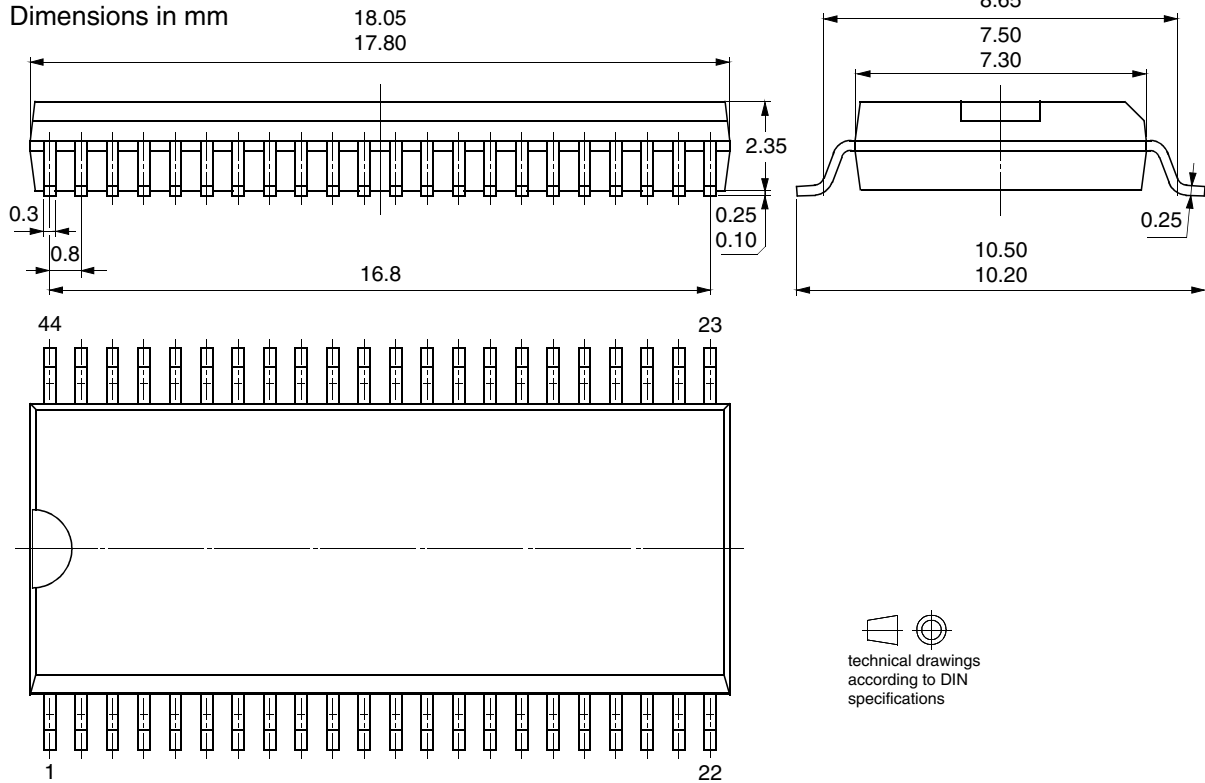
## 9. Ordering Information


Extended Type Number	Package	Remarks
U4089B-PFNY	SSO44	Tubes, Pb-free
U4089B-PFNG3Y	SSO44	Taped and reeled, Pb-free

## 10. Package Information

### Package SSO44

Dimensions in mm



 technical drawings according to DIN specifications



***Atmel Corporation***

2325 Orchard Parkway  
San Jose, CA 95131  
USA

**Tel:** (+1)(408) 441-0311

**Fax:** (+1)(408) 487-2600

***Atmel Asia Limited***

Unit 01-5 & 16, 19/F  
BEA Tower, Millennium City 5  
418 Kwun Tong Road  
Kwun Tong, Kowloon  
HONG KONG

**Tel:** (+852) 2245-6100

**Fax:** (+852) 2722-1369

***Atmel Munich GmbH***

Business Campus  
Parking 4  
D-85748 Garching b. Munich  
GERMANY

**Tel:** (+49) 89-31970-0

**Fax:** (+49) 89-3194621

***Atmel Japan***

9F, Tonetsu Shinkawa Bldg.  
1-24-8 Shinkawa  
Chuo-ku, Tokyo 104-0033  
JAPAN

**Tel:** (+81) (3) 3523-3551

**Fax:** (+81) (3) 3523-7581

© 2011 Atmel Corporation. All rights reserved. / Rev.: 4518D-CORD-02/11

Atmel®, Atmel logo and combinations thereof, and others are registered trademarks or trademarks of Atmel Corporation or its subsidiaries. Other terms and product names may be trademarks of others.

Disclaimer: The information in this document is provided in connection with Atmel products. No license, express or implied, by estoppel or otherwise, to any intellectual property right is granted by this document or in connection with the sale of Atmel products. EXCEPT AS SET FORTH IN THE ATMEL TERMS AND CONDITIONS OF SALES LOCATED ON THE ATMEL WEBSITE, ATMEL ASSUMES NO LIABILITY WHATSOEVER AND DISCLAIMS ANY EXPRESS, IMPLIED OR STATUTORY WARRANTY RELATING TO ITS PRODUCTS INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTY OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT. IN NO EVENT SHALL ATMEL BE LIABLE FOR ANY DIRECT, INDIRECT, CONSEQUENTIAL, PUNITIVE, SPECIAL OR INCIDENTAL DAMAGES (INCLUDING, WITHOUT LIMITATION, DAMAGES FOR LOSS AND PROFITS, BUSINESS INTERRUPTION, OR LOSS OF INFORMATION) ARISING OUT OF THE USE OR INABILITY TO USE THIS DOCUMENT, EVEN IF ATMEL HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. Atmel makes no representations or warranties with respect to the accuracy or completeness of the contents of this document and reserves the right to make changes to specifications and products descriptions at any time without notice. Atmel does not make any commitment to update the information contained herein. Unless specifically provided otherwise, Atmel products are not suitable for, and shall not be used in, automotive applications. Atmel products are not intended, authorized, or warranted for use as components in applications intended to support or sustain life.