MCP2120/22 Developer's Board

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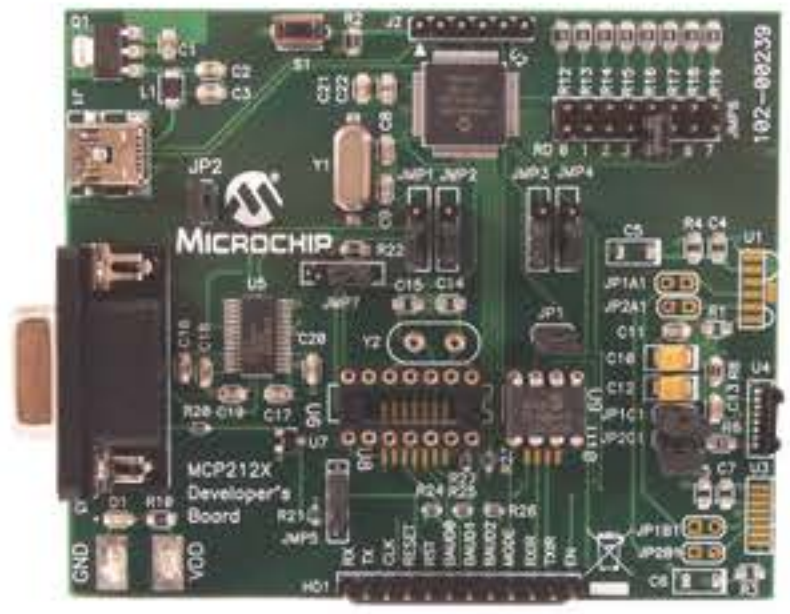
Part Number: MCP212XDM

Documentation & Software

The MCP2120/22 Developer's Board allows for the easy demonstration and development of IrDA applications. The board can be powered via USB or the power test points (VDD and GND). When using the power test points, if JP2 is shorted, the voltage must not exceed the PIC18F65J50 voltage specification.

The preprogrammed PIC18F65J50 firmware generates the MCP2122's clock. The Host interface can be connected to the UART driver device (for IrDA to UART operation), for communication over the DB-9 connector or connected to the PIC18F65J50 for stand alone operation.



Features | Package Contents | Host System Requirements

- Mini USB connector powers the board
- Onboard +3.3V regulator for powering PIC18F65J50
- Hooks for an external regulated DC supply
- Jumper to isolate PIC18F65J50 power signal from the rest of board power.
- DB-9 connector and associated hardware for direct connection to MCP2120 or MCP2122 UART
- UART signals routed by Four 3-pin jumpers to either DB-9 connector or the PIC18F65J50
- External Clock jumper
- Multiple Optical Transceiver circuits, one implemented
- Reset switch for PIC18F65J50 device
- ICSP Header for PIC18F65J50
- MCP2120 crystal socket
- MCP2120/22 SOIC and DIP Footprints (DIP package is the default installation)

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AppNotes	Last Updated	Size
AN946 - Interfacing the MCP2122 to the Host Controller	7/25/2006 4:18:02 PM	623KB
AN923 - Using the MCP2120 Developer's Board of "IR Sniffing"	12/20/2004 4:05:24 PM	424KB
TB073 - Selecting an MCP21XX Device for IrDA® Applications	8/12/2004 2:22:35 PM	124KB
AN756 - Using the MCP2120 for Infrared Communications	3/14/2004 9:38:45 PM	327KB

Documents	Last Updated	Size
MCP2120/22 Developer's Board User's Guide	12/1/2009 10:26:22 AM	1MB
MCP212XDM Gerbers	12/1/2009 10:14:41 AM	271KB
MCP212XDM Firmware	12/1/2009 10:11:25 AM	62KB