

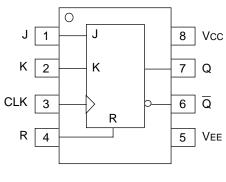
FEATURES

- 525ps propagation delay
- 2.2GHz toggle frequency
- **■** High bandwidth output transistions
- Internal 75K Ω input pull-down resistors
- Available in 8-pin SOIC package

DESCRIPTION

The SY10/100EL35 are high-speed JK Flip-Flops. The J/K data enters the master portion of the flip-flop when the clock is LOW and is transferred to the slave and, thus, the outputs, upon a positive transition of the clock. The reset pin is asynchronous and is activated with a logic HIGH.

PIN CONFIGURATION/BLOCK DIAGRAM



SOIC TOP VIEW

TRUTH TABLE(1)

J	K	R	CLK	Qn+1			
L	L	L	Z	Qn			
L	Н	L	L Z				
Н	L	L	Z	Н			
Н	Н	L	L Z				
Х	Х	Н	Х	L			

NOTE:

1. Z = LOW-to-HIGH transition.

DC ELECTRICAL CHARACTERISTICS

VEE = VEE (Min.) to VEE (Max.); VCC = GND

		Ta = -40°C		TA = 0°C			TA = +25°C			TA = +85°C				
Symbol	Parameter	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit
IEE	Power Supply Current													mA
	10EL	_	27	32	_	27	32	—	27	32	_	27	32	
	100EL	_	27	32		27	32	_	27	32		32	37	
VEE	Power Supply Voltage													V
	10EL	_	-5.2	_	-4.75	-5.2	-5.5	-4.75	-5.2	-5.5	-4.75	-5.2	-5.5	
	100EL	_	-4.5	_	-4.20	-4.5	-5.5	-4.20	-4.5	-5.5	-4.20	-4.5	-5.5	
Iн	Input HIGH Current	_	_	150	_	_	150	_	_	150	_	_	150	μΑ

AC ELECTRICAL CHARACTERISTICS

VEE = VEE (Min.) to VEE (Max.); VCC = GND

		Ta = -40°C		TA = 0°C			TA = +25°C			TA = +85°C				
Symbol	Parameter	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit
fMAX	Maximum Toggle Frequency	1.4	2.0		1.8	2.2		1.8	2.2	_	1.8	2.2	_	GHz
tPLH tPHL	Propagation Delay CLK to Output MR	290 225	515 450		340 275	515 450	690 625	350 275	525 450	700 625	395 350	570 525	745 700	ps
ts	Set-up Time	150	0	_	150	0	_	150	0	_	150	0	_	ps
tH	Hold Time	250	100	_	250	100	_	250	100	_	250	100	_	ps
trr	Reset Recovery	400	200	_	400	200	_	400	200	_	400	200	_	ps
tpw	Minimum Pulse Width CLK, Reset	400	_	_	400	_	_	400	_	_	400	_	_	ps
tr tf	Output Rise/Fall Times Q (20% to 80%)	100	225	350	100	225	350	100	225	350	100	225	350	ps

PRODUCT ORDERING CODE

Ordering Code	Package Type	Operating Range	Marking Code		
SY10EL35ZC	Z8-1	Commercial	HEL35		
SY10EL35ZCTR*	Z8-1	Commercial	HEL35		
SY100EL35ZC	Z8-1	Commercial	XEL35		
SY100EL35ZCTR*	Z8-1	Commercial	XEL35		

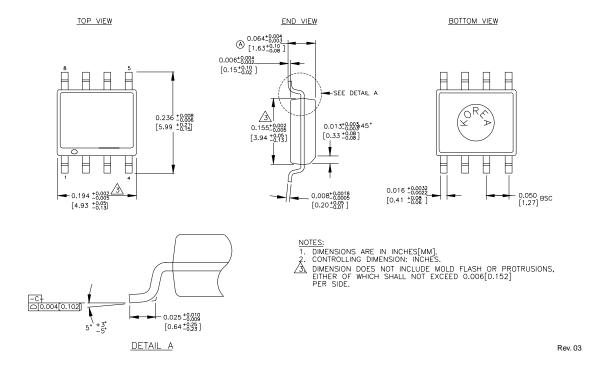
Ordering Code	Package Type	Operating Range	Marking Code		
SY10EL35ZI ⁽¹⁾	Z8-1	Industrial	HEL35		
SY10EL35ZITR*(1)	Z8-1	Industrial	HEL35		
SY100EL35ZI ⁽¹⁾	Z8-1	Industrial	XEL35		
SY100EL35ZITR*(1)	Z8-1	Industrial	XEL35		

Note 1. Recommended for new designs.

^{*}Tape and Reel

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8 LEAD SOIC .150" WIDE (Z8-1)



SY10EL35 SY100EL35

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