

VSC7147 and VSC7147-01 - Hex PBC with Dual Repeater/Retimer

The VSC7147 expands Vitesse's wide offering of fibre channel products with an IC for the growing 2.125 Gbps fibre channel storage market. The device features six Port Bypass Circuits (PBC) that support both 1.0625 Gbps and 2.125 Gbps fibre channel data rates. The VSC7147 enables fibre channel disk arrays, JBODs (Just a Bunch of Disks) and storage subsystem equipment designers to integrate more ports and disk drives and maintain consistently high signal quality using fewer components.

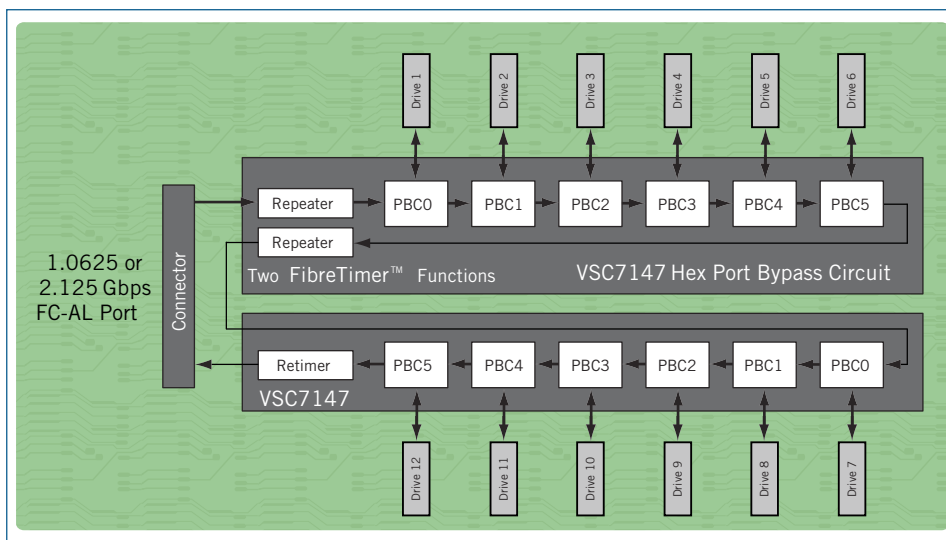
In addition to the PBCs that steer serial fibre channel signals to disk drives and bypass faulty ports, the VSC7147 features dual FibreTimer repeater/retimer cells.

Each FibreTimer cell contains an all-digital clock recovery unit (CRU) that can be configured as either a repeater or a retimer. In the repeater mode, recovered data is retransmitted to a recovered clock, allowing for improved jitter attenuation and low latency. In the retimer mode, recovered data is re-transmitted synchronously to a local reference clock with no jitter transfer ensuring compliance to fibre channel signal quality specifications.

The VSC7147-01 adds an analog signal detect (ASD) function which helps the signal detection capabilities of the Fiber Timers™.

Several VSC7147's can be cascaded together to create a JBOD. The flexibility of the FibreTimers allow repeater and retimer functions to reside where they make the most sense for the backplane board design.

VSC7147 Applications



Features

- Industry's first IC with six Port Bypass Circuits (PBC) supporting both 1.0625 Gbps and 2.125 Gbps Fibre Channel data rates – dual mode provides flexibility by having one device for new and legacy designs
- Two FibreTimer™ cells configurable as:
 - Repeaters for low latency
 - Retimers for true Fibre Channel compliance
 - Reduces external retimer components, provides design flexibility by allowing mixing and matching of the functions for design optimization
- Digital design of the FibreTimers produce a device that is well controlled over voltage, process and temperature variations provides a more robust and reliable design
- User-selectable, on-chip receive termination at either 100Ω or 150Ω
- Speed independent Tx and Rx paths aid in Fibre Channel auto-speed negotiation – allows the device to migrate as the specification evolves
- Device can be strapped for 1 or 2 Gbps operation – supports dual speed storage applications
- World-class signal integrity for jitter compliance – reduces debug and redesign efforts
- Two digital signal detect units to detect the presence of valid Fibre Channel encoded serial data - improves signal detect reliability
- User selectable pre-emphasis on all high-speed outputs - distorts outputs signal for improved signal driving over long trace and cable distances
- 106.25 MHz reference clock
- 5V-tolerant LVTTTL inputs – provides design flexibility and backward compatibility
- Cable equalization on all high-speed inputs – Manages and corrects signal latency and distortion
- Single 2.5V supply - reduces wiring complexity
- 100-pin, 14mm exposed pad TQFP package - ideal for compact JBOD designs

Related Vitesse Products

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