MICREL®

MIC94040/1/2/3

28mΩ R_{DSON} 3A High Side Load Switch in 1.2mm x 1.2mm MLF[®] package

General Description

The MIC94040/1/2/3 is a family of high-side load switches designed to operate from 1.7V to 5.5V input voltage. The load switch pass element is an internal $28m\Omega$ R_{DSON} P-channel MOSFET which enables the device to support up to 3A of continuous current. Additionally, the load switch supports 1.5V logic level control and shutdown features in a tiny 1.2mm x 1.2mm 4 pin MLF $^{\!0}$ package.

The MIC94040 and MIC94041 feature rapid turn on, while the MIC94042 and MIC94043 provide a slew rate controlled soft-start turn-on of $100\mu s$. The soft-start feature is provided to prevent an in-rush current event from pulling down the input supply voltage.

The MIC94041 and MIC94043 feature an active load discharge circuit which switches in a 200Ω load when the switch is disabled to automatically discharge a capacitive load.

An active pull-down on the enable input keeps the MIC94040/1/2/3 in a default OFF state until the enable pin is pulled above 1.2V. Internal level shift circuitry allows low voltage logic signals to switch higher supply voltages. The enable voltage can be as high as 5.5V and is not limited by the input voltage.

The MIC94040/1/2/3 operating voltage range makes them ideal for Lithium ion and NiMH/NiCad/Alkaline battery powered systems, as well as non-battery powered applications. The devices provide low quiescent current and low shutdown current to maximize battery life.

Datasheets and support documentation can be found on Micrel's web site at: www.micrel.com.

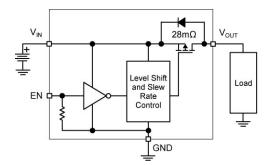
Features

- 28mΩ R_{DSON}
- 3A continuous operating current
- 1.2mm x 1.2mm space saving 4-pin MLF[®] package
- 1.7V to 5.5V input voltage range
- Internal level shift for CMOS/TTL control logic
- Ultra low quiescent current
- Micro-power shutdown current
- Soft-Start: MIC94042, MIC94043
- Load discharge circuit: MIC94041, MIC94043
- · Ultra fast turn off time
- Junction operating temperature from -40°C to +125°C

Applications

- · Cellular phones
- Portable Navigation Devices (PND)
- Personal Media Players (PMP)
- Ultra Mobile PCs
- · Portable instrumentation
- Other Portable applications
- PDAs
- Industrial and DataComm equipment

Typical Application



MIC94040 (ultra fast turn on) MIC94042 (soft-start)

EN Level Shift Slew Rate Control & Load Discharge

V_{OUT}

 $28m\Omega$

MIC94041 (ultra fast turn on with auto-dsicharge) MIC94043 (soft-start with auto-discharge)

MLF and MicroLeadFrame is a registered trademark of Amkor Technology, Inc.

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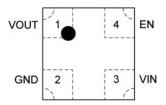
Ordering Information

Part Number	Part Marking ⁽¹⁾	Fast Turn On	Soft-Start	Load Discharge	Package ⁽²⁾	
MIC94040YFL	— P4	•			4-Pin (1.2mm x 1.2mm) MLF [®]	
MIC94041YFL	— P1	•		•	4-Pin (1.2mm x 1.2mm) MLF [®]	
MIC94042YFL	— P2		•		4-Pin (1.2mm x 1.2mm) MLF®	
MIC94043YFL	 P3		•	•	4-Pin (1.2mm x 1.2mm) MLF®	

Notes:

- 1. MLF[®] Pin 1 Identifier symbol is "●".
- 2. MLF® is a GREEN RoHS-compliant package. Lead finish is NiPdAu. Mold compound is Halogen Free.

Pin Configuration



Top View

4-Pin (1.2mm x1.2mm) MLF®

Pin Description

Pin Number	Pin Name	Pin Function
1	V _{OUT}	Drain of P-channel MOSFET.
2	GND	Ground should be connected to electrical ground.
3	V_{IN}	Source of P-channel MOSFET.
4	EN	Enable (Input): Active-high CMOS/TTL control input for switch. Internal ~2M Ω Pull down resistor. Output will be off if this pin is left floating.

Absolute Maximum Ratings⁽¹⁾

Input Voltage (V _{IN})	+6V
Enable Voltage (V _{EN})	+6V
Continuous Drain Current (I _D) (3)	
T _A = 25°C	±3A
T _A = 85°C	±2A
Pulsed Drain Current (I _{DP}) ⁽⁴⁾	±6.0A
Continuous Diode Current (I _S) ⁽⁵⁾	–50mA
Storage Temperature (T _s)	55°C to +150°C
Storage Temperature (T _s) ESD Rating – HBM ⁽⁶⁾	3kV

Operating Ratings⁽²⁾

input voitage (V _{IN})	+1.7 to +5.5V
Junction Temperature (T _J)	40°C to +125°C
Package Thermal Resistance	
$MLF^{\scriptscriptstyle{\circledR}}\left(\theta_{JC}\right)$	90°C/W

Electrical Characteristics

 T_A = 25°C, bold values indicate -40°C $\leq T_A \leq +85$ °C, unless noted.

Symbol	Parameter	Condition	Min	Тур	Max	Units
V _{EN_TH}	Enable Threshold Voltage	$V_{IN} = 1.7V$ to 4.5V, $I_D = -250\mu A$	0.4		1.2	V
IQ	Quiescent Current	$V_{IN} = V_{EN} = 5.5V$, $I_D = OPEN$ Measured on V_{IN} MIC94040, MIC94041		0.1	1	μA
		V_{IN} = V_{EN} = 5.5V, I_D = OPEN Measured on V_{IN} MIC94042, MIC94043		7	10	
I _{EN}	Enable Input Current	$V_{IN} = V_{EN} = 5.5V$, $I_D = OPEN$		2.5	4	μA
I _{SHUT-Q}	Quiescent Current (shutdown)	V_{IN} = +5.5V, V_{EN} = 0V, I_D = OPEN Measured on V_{IN}		0.1	1	μA
I _{SHUT-SWITCH}	OFF State Leakage Current	V_{IN} = +5.5V, V_{EN} = 0V, I_{D} = SHORT Measured on V_{OUT} , $^{(7)}$		0.1	1	μA
R _{DS(ON)}	P-Channel Drain to Source ON	V _{IN} = +5.0V, I _D = -100mA, V _{EN} = 1.5V		28	55	mΩ
	Resistance	V _{IN} = +4.5V, I _D = -100mA, V _{EN} = 1.5V		30	60	mΩ
		V _{IN} = +3.6V, I _D = -100mA, V _{EN} = 1.5V		33	65	mΩ
		V _{IN} = +2.5V, I _D = -100mA, V _{EN} = 1.5V		45	90	mΩ
		V _{IN} = +1.8V, I _D = -100mA, V _{EN} = 1.5V		72	145	mΩ
		V _{IN} = +1.7V, I _D = -100mA, V _{EN} = 1.5V		82	160	mΩ
R _{SHUTDOWN}	Turn-Off Resistance (MIC94041, MIC94043)	$V_{IN} = +3.6V$, $I_{TEST} = 1mA$, $V_{EN} = 0V$		250	400	Ω

Notes:

- 1. Exceeding the absolute maximum rating may damage the device.
- 2. The device is not guaranteed to function outside its operating rating.
- 3. With thermal contact to PCB. See thermal considerations section.
- 4. Pulse width <300µs with < 2% duty cycle.
- 5. Continuous body diode current conduction (reverse conduction, i.e. V_{OUT} to V_{IN}) is not recommended.
- 6. Devices are ESD sensitive. Handling precautions recommended. HBM (Human body model), $1.5k\Omega$ in series with 100pF.
- 7. Measured on the MIC94040YFL and MIC94042YFL.

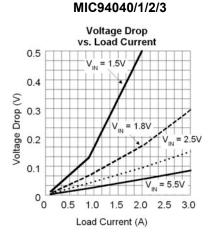
Electrical Characteristics (Dynamic)

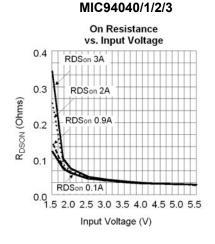
 T_A = 25°C, bold values indicate -40°C $\leq T_A \leq +85$ °C, unless noted.

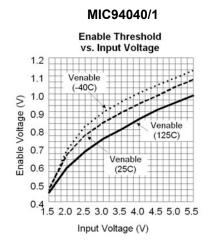
Symbol	Parameter	Condition	Min	Тур	Max	Units
t _{ON_DLY}	Turn-On Delay Time	V_{IN} = +3.6V, I_D = -100mA, V_{EN} = 1.5V MIC94040, MIC94041		0.97	1.5	μs
		V_{IN} = +3.6V, I_D = -100mA, V_{EN} = 1.5V MIC94042, MIC94043	50	106	185	μs
ton_rise	Turn-On Rise Time	V_{IN} = +3.6V, I_D = -100mA, V_{EN} = 1.5V MIC94040, MIC94041	0.5	0.9	5	μs
		V_{IN} = +3.6V, I_D = -100mA, V_{EN} = 1.5V MIC94042, MIC94043	50	116	200	μs
t _{OFF_DLY}	Turn-Off Delay Time	$V_{IN} = +3.6V$, $I_D = -100$ mA, $V_{EN} = 0$ V		100	200	ns
toff_fall	Turn-Off Fall Time	V_{IN} = +3.6V, I_D = -100mA, V_{EN} = 0V		20	100	ns

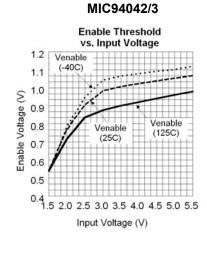
Typical Characteristics

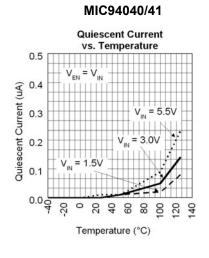
MIC94040/1/2/3 R_{DSON} Variance vs. Temperature 1.4 1.2 1.0 0.80 0.7 0.7 0.80 0.80 0.7 1.9 Temperature (°C)

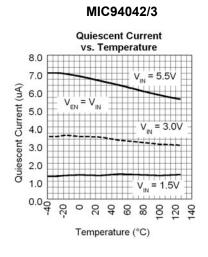


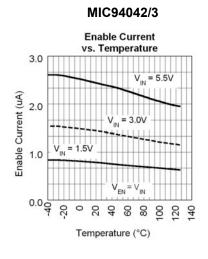


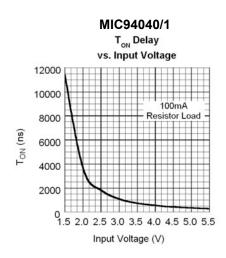




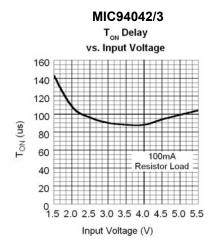


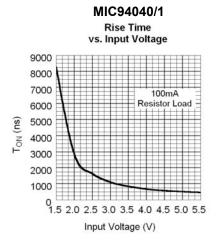


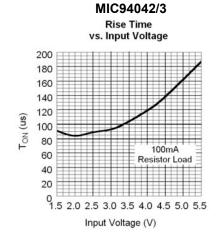


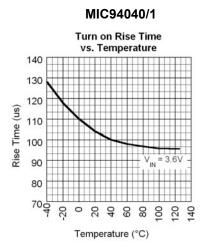


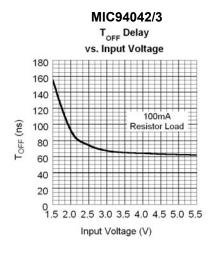
Typical Characteristics

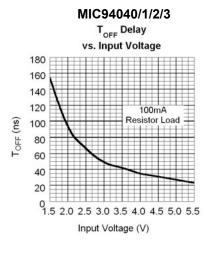


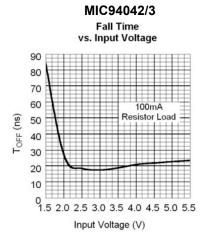




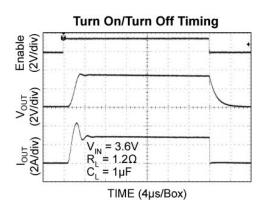


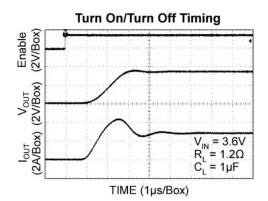


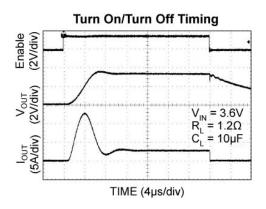


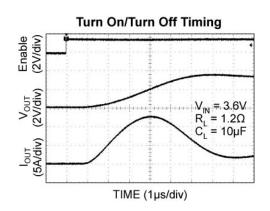


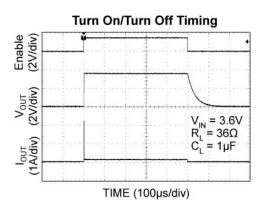
Functional Characteristics

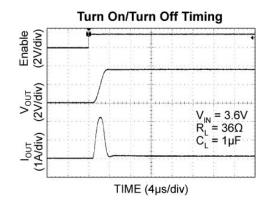


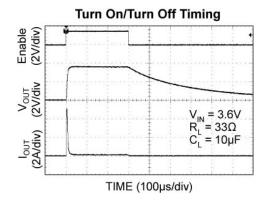


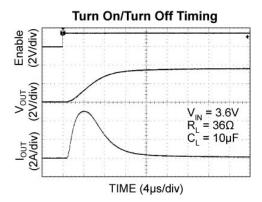


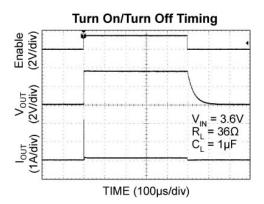


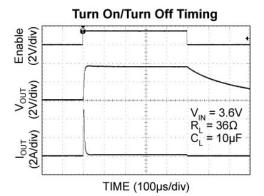


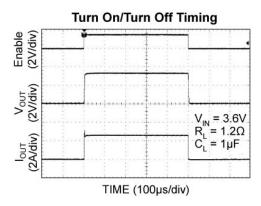


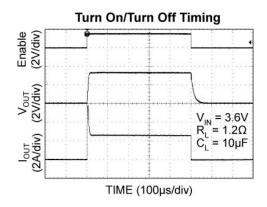


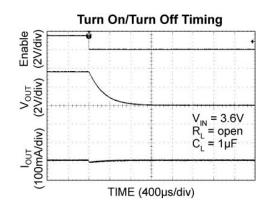


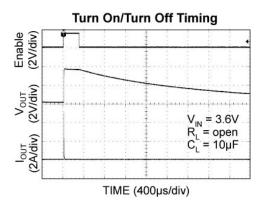


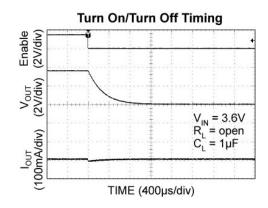


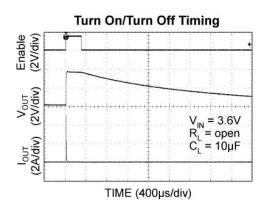


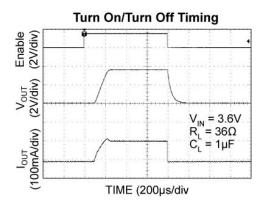


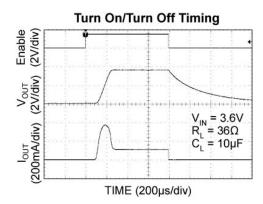


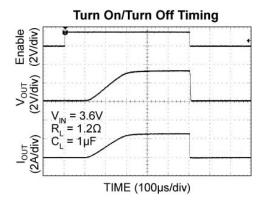


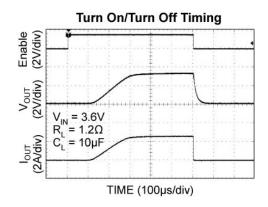


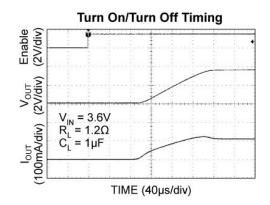


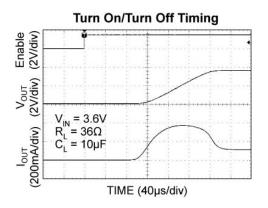


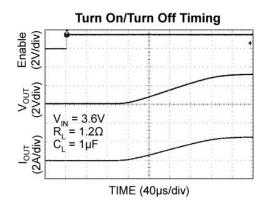


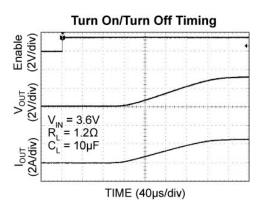


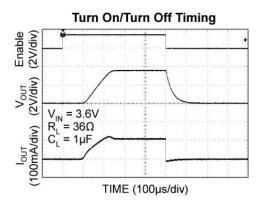


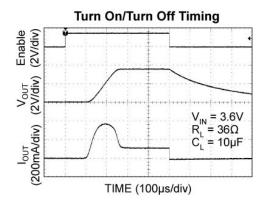


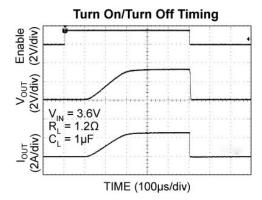


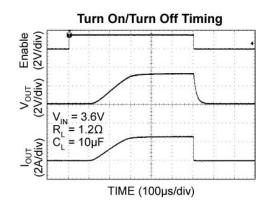


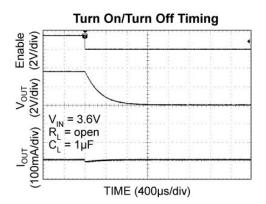


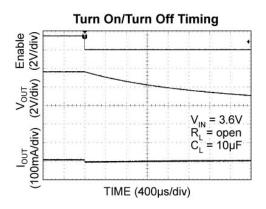


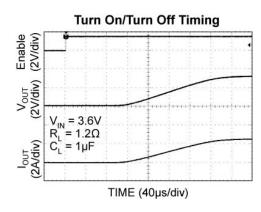


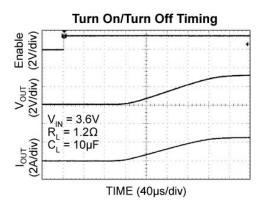












Application Information

Power Dissipation Considerations

As with all power switches, the current rating of the switch is limited mostly by the thermal properties of the package and the PCB it is mounted on. There is a simple ohms law type relationship between thermal resistance, power dissipation and temperature, which are analogous to an electrical circuit:

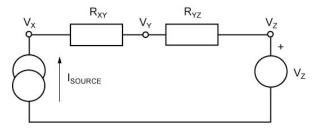


Figure 1. Simple Electrical Circuit

From this simple circuit we can calculate Vx if we know Isource, Vz and the resistor values, Rxy and Ryz using the equation:

$$Vx = Isource \cdot (Rxy + Ryz) + Vz$$

Thermal circuits can be considered using these same rules and can be drawn similarly by replacing current sources with power dissipation (in Watts), resistance with thermal resistance (in °C/W) and voltage sources with temperature (in °C).

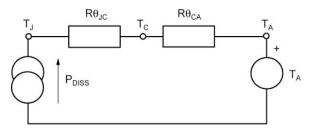


Figure 2. Simple Thermal Circuit

Now replacing the variables in the equation for Vx, we can find the junction temperature (T_J) from power dissipation, ambient temperature and the known thermal resistance of the PCB $(R\theta_{CA})$ and the package $(R\theta_{JC})$.

$$T_J = P_{DISS} x (R\theta_{JC} + R\theta_{CA}) + T_A$$

 P_{DISS} is calculated as $I_{\text{SWITCH}}^2 \times R_{\text{SWmax}}.$ $R\theta_{\text{JC}}$ is found in the operating ratings section of the datasheet and $R\theta_{\text{CA}}$ (the PCB thermal resistance) values for various PCB copper areas is discussed in the document "Designing with Low Dropout Voltage Regulators" available from the Micrel website (LDO Application Hints).

Example:

A switch is intended to drive a 2A load and is placed on a printed circuit board which has a ground plane area of at least 25mm by 25mm (625mm²). The Voltage source is a Li-ion battery with a lower operating threshold of 3V and the ambient temperature of the assembly can be up to 50°C.

Summary of variables:

 $I_{SW} = 2A$ $V_{IN} = 3V \text{ to } 4.2V$ $T_{\Delta} = 50^{\circ}\text{C}$

 $R\theta_{JC} = 90^{\circ}C/W$ from Datasheet

 $R\theta_{CA} = 53^{\circ}C/W$ Read from Graph in Figure 3

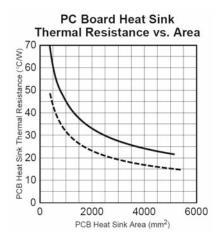


Figure 3. Excerpt from the LDO Book

 $P_{DISS} = I_{SW}^2 x R_{SWmax}$

The worst case switch resistance (R_{SWmax}) at the lowest V_{IN} of 3V is not available in the datasheet, so the next lower value of V_{IN} is used.

$$R_{SWmax}$$
 @ 2.5v = $90m\Omega$

If this were a figure for worst case R_{SWmax} for $25^{\circ}C$, an additional consideration is to allow for the maximum junction temperature of $125^{\circ}C$, the actual worst case resistance in this case can be 30% higher (See R_{DSON} variance vs. temperature graph). However, $90m\Omega$ is the maximum over temperature.

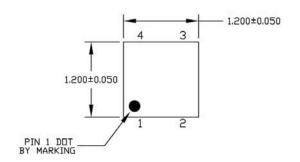
Therefore:

$$T_J = 2^2 \times 0.090 \times (90+53) + 50$$

 $T_J = 101^{\circ}C$

This is below the maximum 125°C.

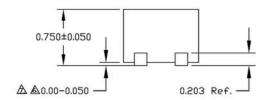
Package Information



0.200±0.050 BSC PIN #1 IDENTIFICATION CHAMFER 0.100X45*

TOP VIEW

BOTTOM VIEW



NOTE:

1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. MAX. PACKAGE WARPAGE IS 0.05 mm.
3. MAXIMUM ALLOWABE BURRS IS 0.076 mm IN ALL DIRECTIONS.
4. PIN #1 ID ON TOP WILL BE LASER/INK MARKED.

DIMENSION APPLIES TO METALIZED TERMINAL AND IS MEASURED BETWEEN 0.20 AND 0.25 mm FROM TERMINAL TIP.

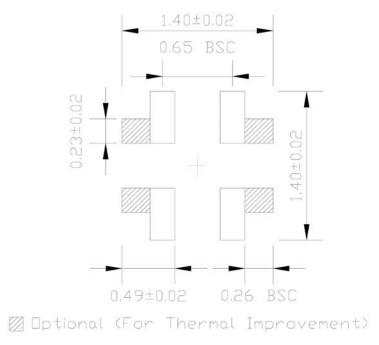
APPLIED DNLY FOR TERMINALS.

APPLIED FOR EXPOSED PAD AND TERMINALS.

SIDE VIEW

4-Pin (1.2mm x 1.2mm) MLF®

All units are in mm Tolerance ± 0.05 if not noted



Disclaimer: This is only a recommendation based on information available to Micrel from its suppliers. Actual land pattern may have to be significantly different due to various materials and processes used in PCB assembly. Micrel makes no representation or warranty of performance based on the recommended land pattern."

Suggested Landing Pattern for 4 Pin (1.2mm x 1.2mm) MLF®

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