



**TRIPLE DIFFERENTIAL  
2:1 MULTIPLEXER**

**SY10E457  
SY100E457**

**FEATURES**

- Differential D and Q
- Extended 100E VEE range of -4.2V to -5.5V
- VBB output for single-ended use
- 700ps max. propagation delay
- High frequency outputs
- Separate and common select
- Internal 75KΩ input pulldown resistors
- Available in 28-pin PLCC package

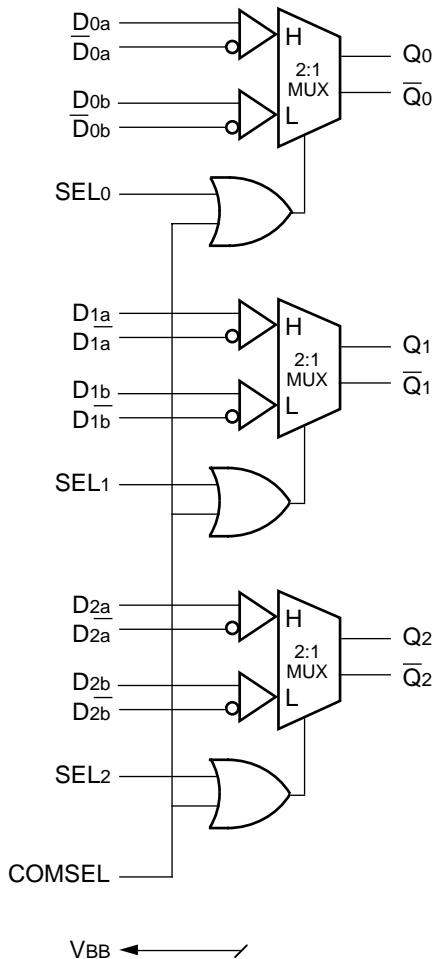
**DESCRIPTION**

The SY10/100E457 are 3-bit differential 2:1 multiplexers. The fully differential data path makes the devices ideal for multiplexing low skew clock or other skew sensitive signals. Multiple VBB pins are provided to ease AC coupling input signals.

The higher frequency outputs provide the device with a >1.0GHz bandwidth to meet the needs of the most demanding system clock.

Both separate selects and a common select are provided to make the device well suited for both data path and random logic applications.

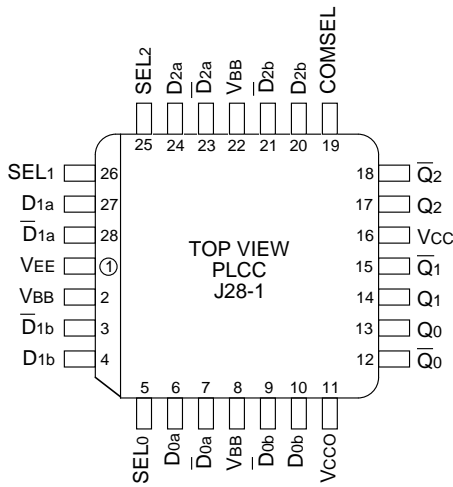
**BLOCK DIAGRAM**



**PIN NAMES**

| Pin                                     | Function                  |
|---|---------------------------|
| D <sub>n</sub> [0:2], $\bar{D}_n$ [0:2] | Differential Data Inputs  |
| SEL                                     | Individual Select Input   |
| COMSEL                                  | Common Select Input       |
| VBB                                     | VBB Reference Output      |
| Q[0:2], $\bar{Q}$ [0:2]                 | Differential Data Outputs |
| Vcco                                    | Vcc to Output             |

**PACKAGE/ORDERING INFORMATION**



**28-Pin PLCC (J28-1)**

**Ordering Information<sup>(1)</sup>**

| Part Number                     | Package Type | Operating Range | Package Marking                             | Lead Finish |
|---------------------------------|--------------|-----------------|---|-------------|
| SY10E457JC                      | J28-1        | Commercial      | SY10E457JC                                  | Sn-Pb       |
| SY10E457JCTR <sup>(2)</sup>     | J28-1        | Commercial      | SY10E457JC                                  | Sn-Pb       |
| SY100E457JC                     | J28-1        | Commercial      | SY100E457JC                                 | Sn-Pb       |
| SY100E457JCTR <sup>(2)</sup>    | J28-1        | Commercial      | SY100E457JC                                 | Sn-Pb       |
| SY10E457JZ <sup>(3)</sup>       | J28-1        | Commercial      | SY10E457JZ with Pb-Free bar-line indicator  | Matte-Sn    |
| SY10E457JZTR <sup>(2, 3)</sup>  | J28-1        | Commercial      | SY10E457JZ with Pb-Free bar-line indicator  | Matte-Sn    |
| SY100E457JZ <sup>(3)</sup>      | J28-1        | Commercial      | SY100E457JZ with Pb-Free bar-line indicator | Matte-Sn    |
| SY100E457JZTR <sup>(2, 3)</sup> | J28-1        | Commercial      | SY100E457JZ with Pb-Free bar-line indicator | Matte-Sn    |

**Notes:**

1. Contact factory for die availability. Dice are guaranteed at T<sub>A</sub> = 25°C, DC Electricals only.
2. Tape and Reel.
3. Pb-Free package is recommended for new designs.

**DC ELECTRICAL CHARACTERISTICS**

VEE = VEE (Min.) to VEE (Max.); VCC = VCCO = GND

| Symbol               | Parameter                | TA = 0°C |      |       | TA = +25°C |      |       | TA = +85°C |      |       | Unit | Condition |
|----------------------|--------------------------|----------|------|-------|------------|------|-------|------------|------|-------|------|-----------|
|                      |                          | Min.     | Typ. | Max.  | Min.       | Typ. | Max.  | Min.       | Typ. | Max.  |      |           |
| VBB                  | Output Reference Voltage |          |      |       |            |      |       |            |      |       | V    | —         |
|                      | 10E                      | -1.38    | —    | -1.27 | -1.35      | —    | -1.25 | -1.31      | —    | -1.19 |      |           |
|                      | 100E                     | -1.38    | —    | -1.26 | -1.38      | —    | -1.26 | -1.38      | —    | -1.26 |      |           |
| I <sub>IH</sub>      | Input HIGH Current       | —        | —    | 150   | —          | —    | 150   | —          | —    | 150   | μA   | —         |
| I <sub>EE</sub>      | Power Supply Current     |          |      |       |            |      |       |            |      |       | mA   | —         |
|                      | 10E                      | —        | 92   | 110   | —          | 92   | 110   | —          | 92   | 110   |      |           |
|                      | 100E                     | —        | 92   | 110   | —          | 92   | 110   | —          | 106  | 127   |      |           |
| V <sub>PP</sub> (DC) | Input Sensitivity        | 50       | —    | —     | 50         | —    | —     | 50         | —    | —     | mV   | 1         |
| V <sub>CMR</sub>     | Common Mode Range        | -1.5     | —    | 0     | -1.5       | —    | 0     | -1.5       | —    | 0     | V    | 2         |

**Notes:**

1. Differential input voltage required to obtain a full ECL swing on the outputs.
2. V<sub>CMR</sub> is referenced to the most positive side of the differential input signal. Normal operation is obtained when the input signals are within the V<sub>CMR</sub> range and the input swing is greater than V<sub>PP</sub> (min.) and <1V.

**AC ELECTRICAL CHARACTERISTICS**

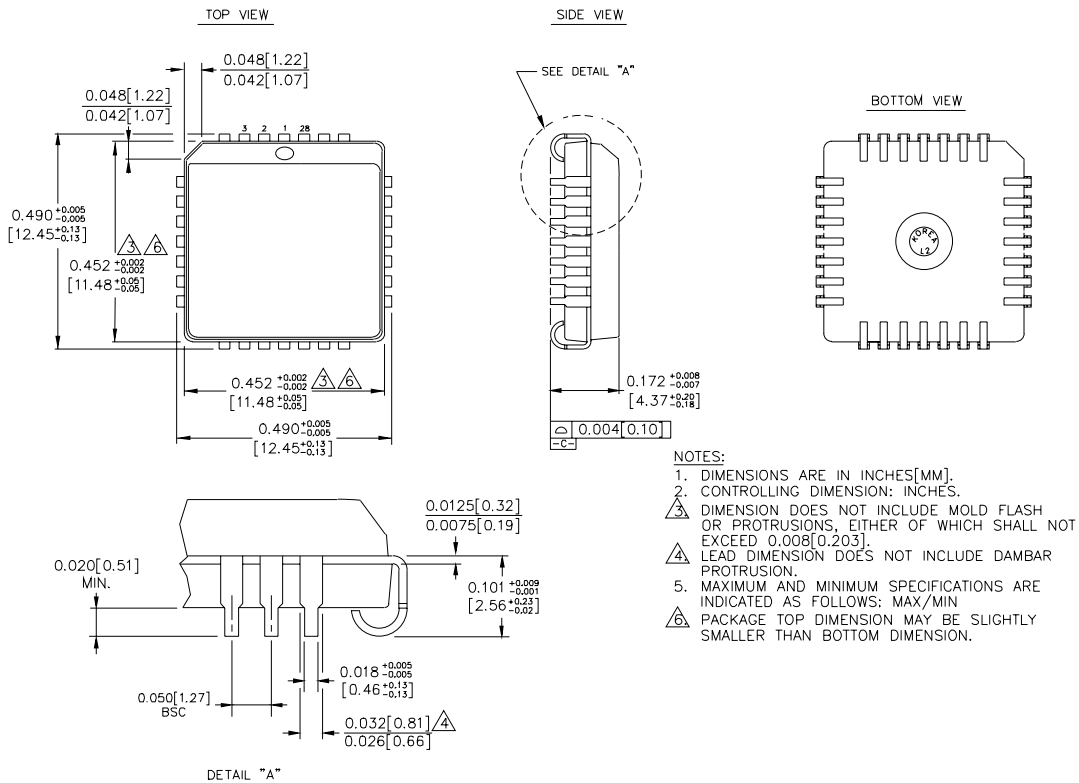
VEE = VEE (Min.) to VEE (Max.); VCC = VCCO = GND

| Symbol               | Parameter  | TA = 0°C |      |      | TA = +25°C |      |      | TA = +85°C |      |      | Unit | Condition |
|----------------------|--|----------|------|------|------------|------|------|------------|------|------|------|-----------|
|                      |  | Min.     | Typ. | Max. | Min.       | Typ. | Max. | Min.       | Typ. | Max. |      |           |
| t <sub>PD</sub>      | Propagation Delay to Output                        |          |      |      |            |      |      |            |      |      | ps   | —         |
|                      | D (Diff)   | 375      | 475  | 650  | 375        | 475  | 650  | 375        | 475  | 650  |      |           |
|                      | D  | 325      | 475  | 700  | 325        | 475  | 700  | 325        | 475  | 700  |      |           |
|                      | SEL  | 350      | 500  | 725  | 350        | 500  | 725  | 350        | 500  | 725  |      |           |
|                      | COMSEL   | 375      | 525  | 750  | 375        | 525  | 750  | 375        | 525  | 750  |      |           |
| t <sub>skew</sub>    | Within-Device Skew                                 | —        | 40   | —    | —          | 40   | —    | —          | 40   | —    | ps   | 1         |
| t <sub>skew</sub>    | Duty Cycle Skew t <sub>PLH</sub> –t <sub>PHL</sub> | —        | ±10  | —    | —          | ±10  | —    | —          | ±10  | —    | ps   | 2         |
| V <sub>PP</sub> (AC) | Minimum Input Swing                                | 150      | —    | —    | 150        | —    | —    | 150        | —    | —    | mV   | 3         |
| t <sub>r</sub>       | Rise/Fall Time                                     | 150      | 275  | 450  | 150        | 275  | 450  | 150        | 275  | 450  | ps   | —         |
| t <sub>f</sub>       | 20–80%   |          |      |      |            |      |      |            |      |      |      |           |

**Notes:**

1. Within-device skew is defined as identical transitions on similar paths through a device.
2. Duty cycle skew guarantee holds only for differential operation when the delays are measured from the cross point of the inputs to the cross point of the outputs.
3. Minimum input swing for which AC parameters are guaranteed.

**28-PIN PLCC (J28-1)**



Rev. 03

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