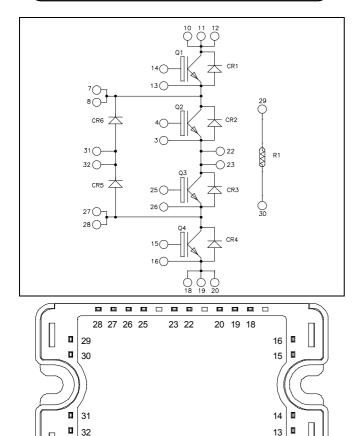
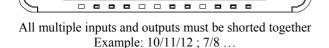


Three level inverter Trench + Field Stop IGBT **Power Module**





7 8

Q1 to Q4 Absolute maximum ratings

2 3 4

APTGT50TL60T3G

$V_{CES} = 600V$ $I_{\rm C} = 50 {\rm A}$ (*a*) ${\rm Tc} = 80^{\circ}{\rm C}$

Application

- Solar converter
- Uninterruptible Power Supplies •

Features .

- Trench + Field Stop IGBT Technology
 - Low voltage drop _
 - Low tail current
 - Switching frequency up to 20 kHz
 - Soft recovery parallel diodes
 - Low diode VF
 - Low leakage current
 - RBSOA and SCSOA rated
- Kelvin emitter for easy drive
- Very low stray inductance
- High level of integration
- Internal thermistor for temperature monitoring

Benefits

- Stable temperature behavior •
- Very rugged
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Easy paralleling due to positive TC of VCEsat
- Low profile •
- **RoHS** Compliant

Symbol	Parameter		Max ratings	Unit
V _{CES}	Collector - Emitter Breakdown Voltage		600	V
I _C	Continuous Collector Current	$T_C = 25^{\circ}C$	80	
		$T_C = 80^{\circ}C$	50	А
I _{CM}	Pulsed Collector Current	$T_C = 25^{\circ}C$	100	
V _{GE}	Gate – Emitter Voltage		±20	V
P _D	Maximum Power Dissipation	$T_C = 25^{\circ}C$	176	W
RBSOA	Reverse Bias Safe Operating Area	$T_{\rm J} = 150^{\circ}{\rm C}$	100A @ 550V	

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

13

10 11 12

APTGT50TL60T3G-Rev1 October, 2012

Γ



All ratings (a) $T_j = 25^{\circ}C$ unless otherwise specified

Q1 to Q4 Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
I _{CES}	Zero Gate Voltage Collector Current	$V_{GE} = 0V, V_{CE} = 600V$				250	μA
V	Collector Emitter Saturation Voltage	$V_{GE} = 15V$	$T_j = 25^{\circ}C$		1.5	1.9	V
V _{CE(sat)}		$I_C = 50A$	$T_{j} = 150^{\circ}C$		1.7		v
V _{GE(th)}	Gate Threshold Voltage	$V_{GE} = V_{CE}, I_C = 600 \mu A$		5.0	5.8	6.5	V
I _{GES}	Gate – Emitter Leakage Current	$V_{GE} = 20V, V_{CE}$	= 0V			600	nA

Q1 to Q4 Dynamic Characteristics

-	Characteristic	Test Conditions	1	Min	Тур	Max	Unit
Cies	Input Capacitance	$V_{GE} = 0V$			3150		
C _{oes}	Output Capacitance	$V_{CE} = 25V$			200		pF
C _{res}	Reverse Transfer Capacitance	f = 1 MHz			95		
Q _G	Gate charge	V _{GE} =±15V, I _C =50A V _{CE} =300V			0.5		μC
T _{d(on)}	Turn-on Delay Time	Inductive Switching (25°C)		110		
T _r	Rise Time	$V_{GE} = \pm 15V$			45		m .c
T _{d(off)}	Turn-off Delay Time	$V_{Bus} = 300V$ $I_{C} = 50A$			200		ns
T _f	Fall Time	$R_G = 8.2\Omega$			40		
T _{d(on)}	Turn-on Delay Time	Inductive Switching (150°C)		120		
T _r	Rise Time	$V_{GE} = \pm 15V$ $V_{Bus} = 300V$ $I_C = 50A$			50		
T _{d(off)}	Turn-off Delay Time				250		ns
T _f	Fall Time	$R_G = 8.2\Omega$			60		
Eon	Turn-on Switching Energy		= 25°C		0.3		mJ
Lon	Turn-on Switching Energy		= 150°C		0.43		IIIJ
E _{off}	Turn-off Switching Energy		= 25°C		1.35		mJ
011			= 150°C		1.75		
I _{sc}	Short Circuit data	$V_{GE} \le 15V$; $V_{Bus} = 360V$ $t_p \le 6\mu s$; $T_1 = 150^{\circ}C$			250		А
R _{thJC}	Junction to Case Thermal Resistance					0.85	°C/W



APTGT50TL60T3G

CR1 to CR4 diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit	
V _{RRM}	Maximum Peak Repetitive Reverse Voltage			600			V	
I _{RM}	Maximum Reverse Leakage Current	V _R =600V	$T_i = 25^{\circ}C$ $T_i = 150^{\circ}C$			150 350	μΑ	
I _F	DC Forward Current		$Tc = 80^{\circ}C$		30		А	
V	V_F Diode Forward Voltage $I_F = 30A$ $V_{GE} = 0V$	$I_F = 30A$	$T_i = 25^{\circ}C$		1.6	2	V	
v _F		$T_{i} = 150^{\circ}C$		1.5		v		
+	Payaraa Paaayary Timaa		$T_j = 25^{\circ}C$		100		na	
t _{rr}	Reverse Recovery Time		$T_{j} = 150^{\circ}C$		150		ns	
0	Pavana Paaavan Changa	$I_{\rm F} = 30A$ $V_{\rm R} = 300V$ $di/dt = 1800A/\mu s$	$T_j = 25^{\circ}C$		1.5			
Q _{rr}	Reverse Recovery Charge			$v_{\rm R} = 300 v$ di/dt =1800A/µs $T_{\rm j} = 150$	$T_{i} = 150^{\circ}C$		3.1	
Б			$T_j = 25^{\circ}C$		0.34		mI	
E _{rr}	Reverse Recovery Energy		$T_i = 150^{\circ}$	$T_{j} = 150^{\circ}C$		0.75		mJ
R _{thJC}	Junction to Case Thermal Resistance					2.45	°C/W	

CR5 & CR6 diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
V _{RRM}	Maximum Peak Repetitive Reverse Voltage			600			V
I _{RM}	Maximum Reverse Leakage Current	V _R =600V	$T_j = 25^{\circ}C$ $T_j = 150^{\circ}C$			150 350	μA
I _F	DC Forward current		$T_{j} = 130 \text{ C}$ $T_{c} = 80^{\circ}\text{C}$		50	350	А
V _F	Diode Forward Voltage	$I_{\rm F} = 50 A$ $V_{\rm GE} = 0 V$	$T_{j} = 25^{\circ}C$ $T_{i} = 150^{\circ}C$		1.6 1.5	2	V
t _{rr}	Reverse Recovery Time	$I_F = 50A$ $V_R = 300V$ $di/dt = 1800A/\mu s$	$T_j = 25^{\circ}C$ $T_j = 150^{\circ}C$		100 150		ns
Q _{rr}	Reverse Recovery Charge		$T_j = 25^{\circ}C$ $T_i = 150^{\circ}C$		2.6 5.4		μC
E _{rr}	Reverse Recovery Energy		$T_{i} = 25^{\circ}C$ $T_{i} = 150^{\circ}C$		0.60		mJ
R _{thJC}	Junction to Case Thermal Resistance		• *			1.42	°C/W

Temperature sensor NTC (see application note APT0406 on www.microsemi.com for more information). Symbol Characteristic Min Turn

Symbol	Characteristic		Min	Тур	Max	Unit
R ₂₅	Resistance @ 25°C			50		kΩ
$\Delta R_{25}/R_{25}$				5		%
B _{25/85}	$T_{25} = 298.15 \text{ K}$			3952		K
$\Delta B/B$		T _C =100°C		4		%

$$R_{T} = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$

T: Thermistor temperature

R_T: Thermistor value at T

APTGT50TL60T3G-Rev1 October, 2012

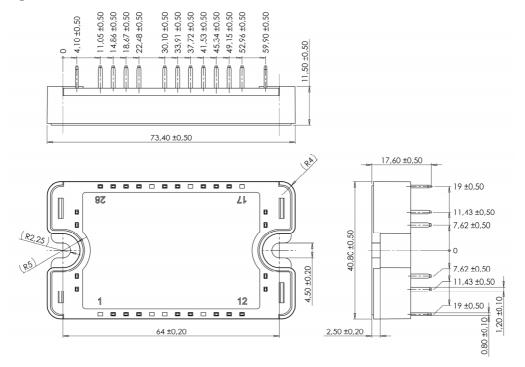


APTGT50TL60T3G

Thermal and package characteristics

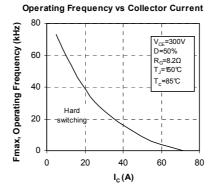
Symbol	Characteristic			Min	Тур	Max	Unit
V _{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000			V
TJ	Operating junction temperature range			-40		175	
T _{STG}	Storage Temperature Range			-40		125	°C
T _C	Operating Case Temperature					100	
Torque	Mounting torque	To heatsink	M4	2		3	N.m
Wt	Package Weight					110	g

SP3 Package outline (dimensions in mm)



See application note 1901 - Mounting Instructions for SP3 Power Modules on www.microsemi.com

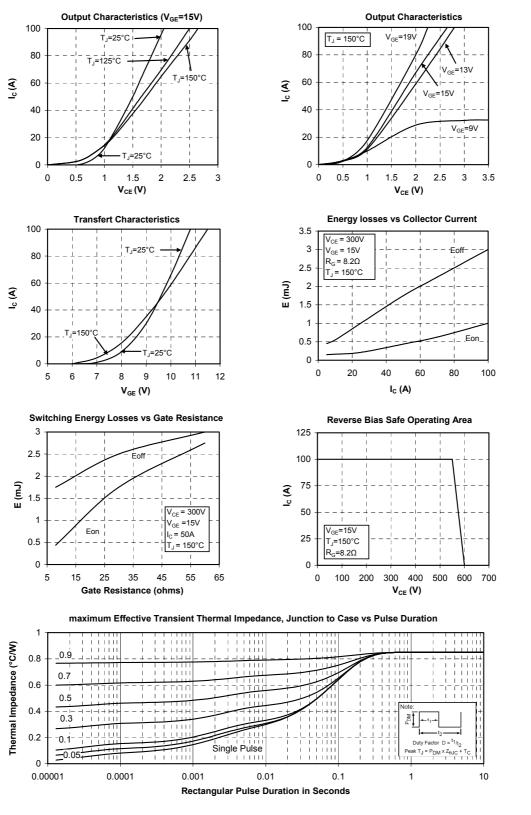
Q1 to Q4 Typical performance curve



APTGT50TL60T3G - Rev1 October, 2012



APTGT50TL60T3G



5 - 8



CR1 to CR4 Typical performance curve

0

0

10

20

30

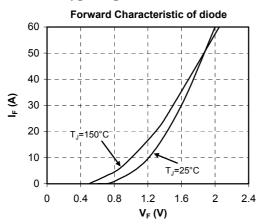
Gate Resistance (ohms)

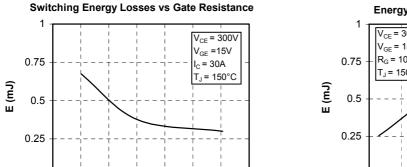
40

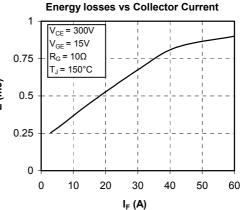
50

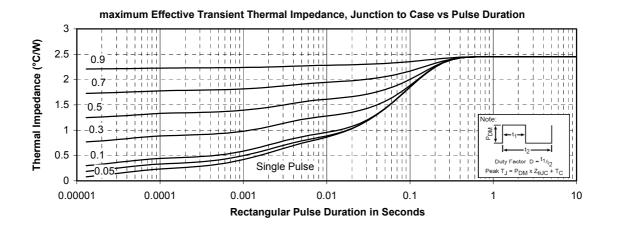
60

70



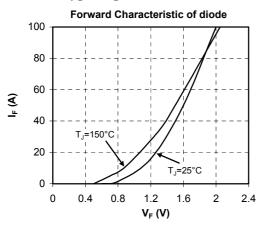


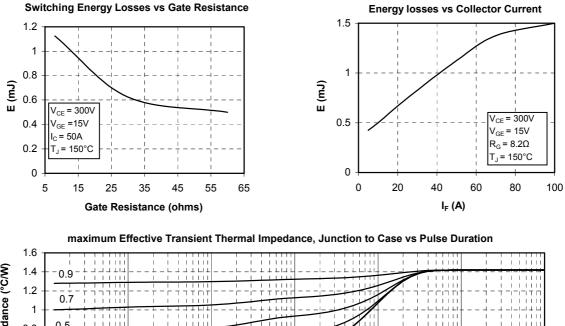


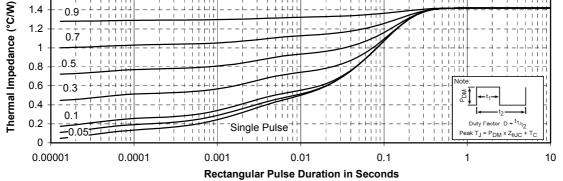




CR5 & CR6 Typical performance curve







7 - 8



DISCLAIMER

The information contained in the document (unless it is publicly available on the Web without access restrictions) is PROPRIETARY AND CONFIDENTIAL information of Microsemi and cannot be copied, published, uploaded, posted, transmitted, distributed or disclosed or used without the express duly signed written consent of Microsemi. If the recipient of this document has entered into a disclosure agreement with Microsemi, then the terms of such Agreement will also apply. This document and the information contained herein may not be modified, by any person other than authorized personnel of Microsemi. No license under any patent, copyright, trade secret or other intellectual property right is granted to or conferred upon you by disclosure or delivery of the information, either expressly, by implication, inducement, estoppels or otherwise. Any license under such intellectual property rights must be approved by Microsemi in writing signed by an officer of Microsemi.

Microsemi reserves the right to change the configuration, functionality and performance of its products at anytime without any notice. This product has been subject to limited testing and should not be used in conjunction with lifesupport or other mission-critical equipment or applications. Microsemi assumes no liability whatsoever, and Microsemi disclaims any express or implied warranty, relating to sale and/or use of Microsemi products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Any performance specifications believed to be reliable but are not verified and customer or user must conduct and complete all performance and other testing of this product as well as any user or customers final application. User or customer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the customer's and user's responsibility to independently determine suitability of any Microsemi product and to test and verify the same. The information contained herein is provided "AS IS, WHERE IS" and with all faults, and the entire risk associated with such information is entirely with the User. Microsemi specifically disclaims any liability of any kind including for consequential, incidental and punitive damages as well as lost profit. The product is subject to other terms and conditions which can be located on the web at http://www.microsemi.com/legal/tnc.asp

Life Support Application

Seller's Products are not designed, intended, or authorized for use as components in systems intended for space, aviation, surgical implant into the body, in other applications intended to support or sustain life, or for any other application in which the failure of the Seller's Product could create a situation where personal injury, death or property damage or loss may occur (collectively "Life Support Applications").

Buyer agrees not to use Products in any Life Support Applications and to the extent it does it shall conduct extensive testing of the Product in such applications and further agrees to indemnify and hold Seller, and its officers, employees, subsidiaries, affiliates, agents, sales representatives and distributors harmless against all claims, costs, damages and expenses, and attorneys' fees and costs arising, directly or directly, out of any claims of personal injury, death, damage or otherwise associated with the use of the goods in Life Support Applications, even if such claim includes allegations that Seller was negligent regarding the design or manufacture of the goods.

Buyer must notify Seller in writing before using Seller's Products in Life Support Applications. Seller will study with Buyer alternative solutions to meet Buyer application specification based on Sellers sales conditions applicable for the new proposed specific part.