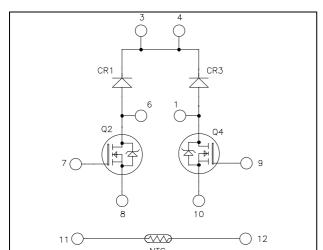
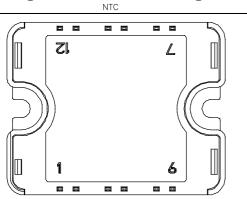


Dual boost chopper Super Junction MOSFET Power Module





Pins 3/4 must be shorted together

### APTC60DDAM45T1G

### $V_{DSS} = 600V$ $R_{DSon} = 45m\Omega \text{ max} @ \text{Tj} = 25^{\circ}\text{C}$ $I_D = 49A @ \text{Tc} = 25^{\circ}\text{C}$

#### Application

- AC and DC motor control
- Switched Mode Power Supplies
- Power Factor Correction

#### Features

#### 

- Ultra low R<sub>DSon</sub>
- Low Miller capacitance
- Ultra low gate charge
- Avalanche energy rated
- Very rugged
- Very low stray inductance
  Symmetrical design
- Internal thermistor for temperature monitoring
- High level of integration

#### Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Each leg can be easily paralleled to achieve a single boost of twice the current capability
- Low profile
- RoHS Compliant

#### Absolute maximum ratings

Symbol	Parameter		Max ratings	Unit
V <sub>DSS</sub>	Drain - Source Breakdown Voltage		600	V
т	Continuous Drain Current	$T_c = 25^{\circ}C$	49	
I <sub>D</sub>	Continuous Drain Current	$T_c = 80^{\circ}C$	38	Α
I <sub>DM</sub>	Pulsed Drain current		130	
V <sub>GS</sub>	Gate - Source Voltage		±20	V
R <sub>DSon</sub>	Drain - Source ON Resistance		45	mΩ
P <sub>D</sub>	Maximum Power Dissipation	$T_c = 25^{\circ}C$	250	W
I <sub>AR</sub>	Avalanche current (repetitive and non repetitive)		15	Α
E <sub>AR</sub>	Repetitive Avalanche Energy		3	mJ
E <sub>AS</sub>	Single Pulse Avalanche Energy		1900	1115

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

www.microsemi.com

1 - 8

APTC60DDAM45T1G-Rev 2 October, 2012



#### All ratings (a) $T_j = 25^{\circ}C$ unless otherwise specified

### **Electrical Characteristics**

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 600V$ $T_j = 25^{\circ}C$			250	
		$V_{GS} = 0V, V_{DS} = 600V$ $T_j = 125^{\circ}C$			500	μA
R <sub>DS(on)</sub>	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 24.5A$		40	45	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 3mA$	2.1	3	3.9	V
I <sub>GSS</sub>	Gate – Source Leakage Current	$V_{GS} = \pm 20 V, V_{DS} = 0V$			100	nA

#### **Dynamic Characteristics**

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
C <sub>iss</sub>	Input Capacitance	$V_{GS} = 0V$ ; $V_{DS} = 25V$		7.2		nF
C <sub>oss</sub>	Output Capacitance	f=1MHz		8.5		m
Qg	Total gate Charge	$V_{GS} = 10V$		150		
$Q_{gs}$	Gate – Source Charge	$V_{Bus} = 300V$		34		nC
$Q_{gd}$	Gate – Drain Charge	$I_D = 49A$		51		
T <sub>d(on)</sub>	Turn-on Delay Time	Inductive Switching (125°C)		21		
Tr	Rise Time	$V_{GS} = 10V$		30		ns
T <sub>d(off)</sub>	Turn-off Delay Time	$V_{Bus} = 400V$ $I_D = 49A$		100		
$T_{\rm f}$	Fall Time	$R_G = 5\Omega$		45		
Eon	Turn-on Switching Energy	Inductive switching @ $25^{\circ}C$ $V_{GS} = 10V$ ; $V_{Bus} = 400V$		675		μJ
E <sub>off</sub>	Turn-off Switching Energy	$I_D = 49A; R_G = 5\Omega$		520		μι
Eon	Turn-on Switching Energy	Inductive switching (a) $125^{\circ}C$		1100		1
$\mathrm{E}_{\mathrm{off}}$	Turn-off Switching Energy	$ V_{GS} = 10V; V_{Bus} = 400V  I_D = 49A; R_G = 5\Omega $		635		μJ

#### Chopper diode ratings and characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit	
V <sub>RRM</sub>	Maximum Peak Repetitive Reverse Voltage			600			V
I <sub>RM</sub>	Maximum Reverse Leakage Current	V <sub>R</sub> =600V	$T_j = 25^{\circ}C$ $T_j = 125^{\circ}C$			25 500	μΑ
I <sub>F</sub>	DC Forward Current		$Tc = 80^{\circ}C$		60		А
	Diode Forward Voltage	$I_F = 60A$			1.7	2.3	
$V_{\rm F}$		$I_{\rm F} = 120 {\rm A}$		2		V	
		$I_F = 60A$	$T_{j} = 125^{\circ}C$		1.4		
ť	Reverse Recovery Time	$I_F = 60A$ $V_R = 400V$	$T_j = 25^{\circ}C$		70		ns
t <sub>rr</sub>			$T_{j} = 125^{\circ}C$		140		115
Q <sub>rr</sub>	Reverse Recovery Charge	$di/dt = 200 A/\mu s$	$T_j = 25^{\circ}C$		100		nC
			$T_{j} = 125^{\circ}C$		690		



#### Thermal and package characteristics

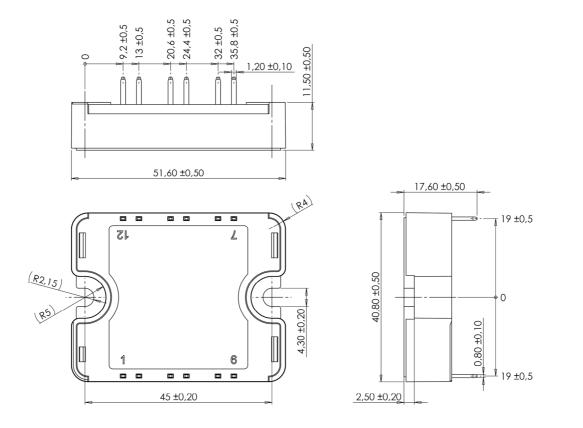
Symbol	Characteristic				Min	Тур	Max	Unit
р	Junction to Case Thermal Resistance		Cooll	MOS			0.5	°C/W
R <sub>thJC</sub>		Diode	e			0.85	C/ w	
V <sub>ISOL</sub>	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz				4000			V
T <sub>J</sub>	Operating junction temperature range				-40		150	
T <sub>STG</sub>	Storage Temperature Range				-40		125	°C
T <sub>C</sub>	Operating Case Temperature						100	
Torque	Mounting torque	To heats	sink	M4	2		3	N.m
Wt	Package Weight						80	g

Temperature sensor NTC (see application note APT0406 on www.microsemi.com for more information).

Symbol	Characteristic		Min	Тур	Max	Unit
R <sub>25</sub>	Resistance @ 25°C			50		kΩ
$\Delta R_{25}/R_{25}$				5		%
B <sub>25/85</sub>	$T_{25} = 298.15 \text{ K}$			3952		Κ
$\Delta B/B$		$T_C = 100^{\circ}C$		4		%

$$R_{T} = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$
 T: Thermistor temperature R<sub>T</sub>: Thermistor value at T

#### SP1 Package outline (dimensions in mm)



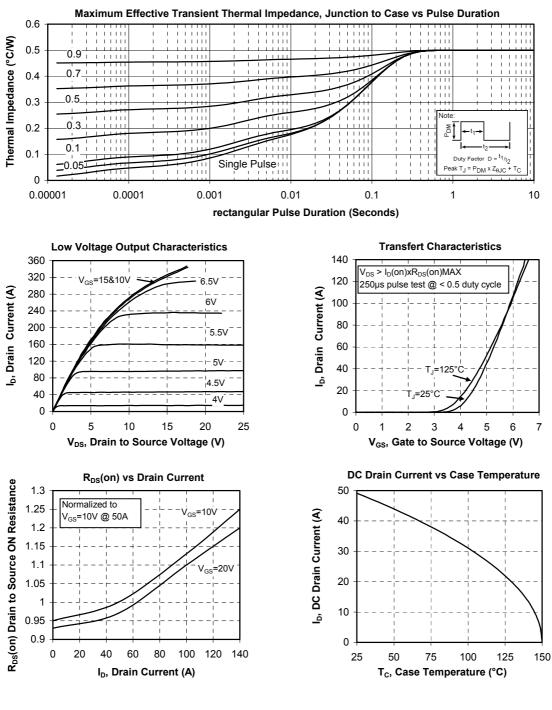
See application note 1904 - Mounting Instructions for SP1 Power Modules on www.microsemi.com

www.microsemi.com

3 - 8



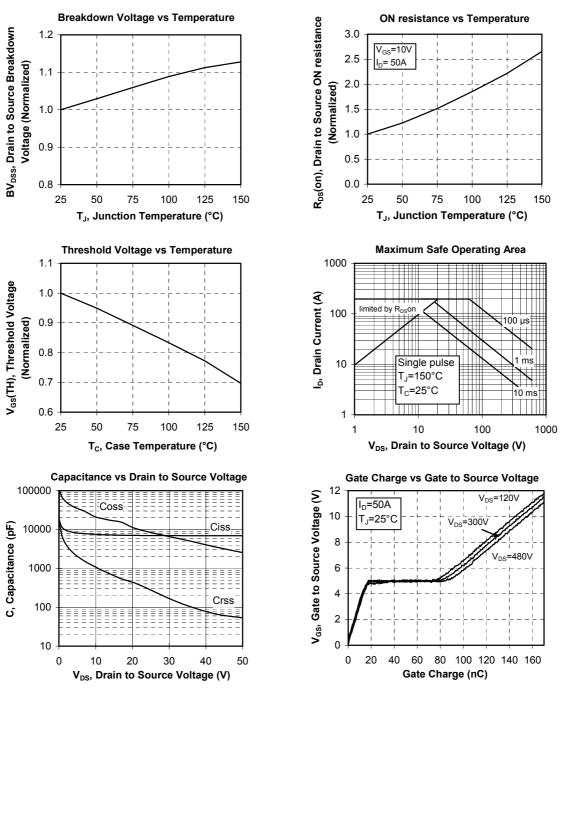
#### **Typical CoolMOS Performance Curve**



APTC60DDAM45T1G-Rev 2 October, 2012

4 - 8

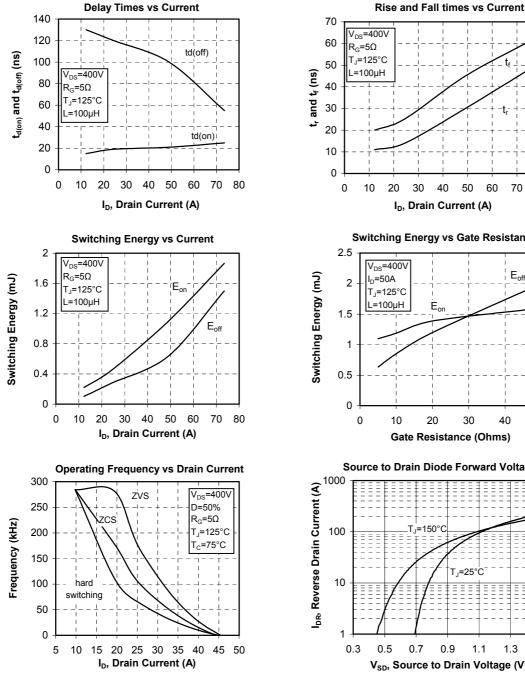




APTC60DDAM45T1G-Rev 2 October, 2012

5 - 8



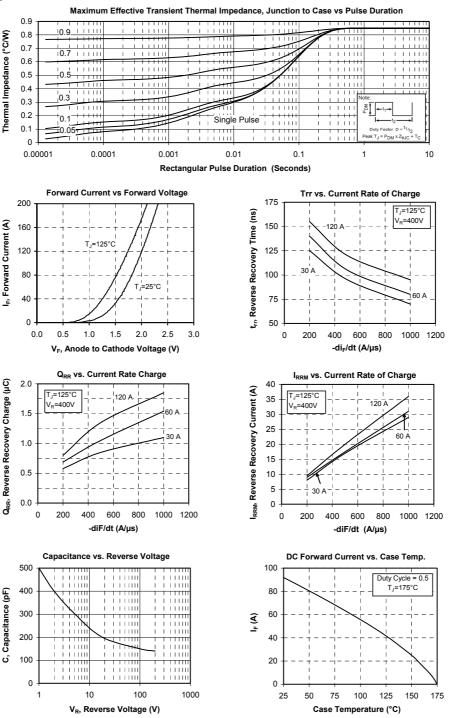


T\_=125°C L=100µH 20 30 40 50 70 80 60 I<sub>D</sub>, Drain Current (A) Switching Energy vs Gate Resistance V<sub>DS</sub>=400V  $\mathsf{E}_{\mathsf{off}}$ I<sub>D</sub>=50A T<sub>J</sub>=125°C L=100µH 20 30 40 50 10 Gate Resistance (Ohms) Source to Drain Diode Forward Voltage =150°C T<sub>J</sub>=25°॑C 0.5 0.9 1.5 0.7 1.1 1.3 V<sub>SD</sub>, Source to Drain Voltage (V)

6 - 8



#### **Typical chopper diode Performance Curve**



"COOLMOS™ comprise a new family of transistors developed by Infineon Technologies AG. "COOLMOS" is a trademark of Infineon Technologies AG".

7 - 8



#### DISCLAIMER

The information contained in the document (unless it is publicly available on the Web without access restrictions) is PROPRIETARY AND CONFIDENTIAL information of Microsemi and cannot be copied, published, uploaded, posted, transmitted, distributed or disclosed or used without the express duly signed written consent of Microsemi. If the recipient of this document has entered into a disclosure agreement with Microsemi, then the terms of such Agreement will also apply. This document and the information contained herein may not be modified, by any person other than authorized personnel of Microsemi. No license under any patent, copyright, trade secret or other intellectual property right is granted to or conferred upon you by disclosure or delivery of the information, either expressly, by implication, inducement, estoppels or otherwise. Any license under such intellectual property rights must be approved by Microsemi in writing signed by an officer of Microsemi.

Microsemi reserves the right to change the configuration, functionality and performance of its products at anytime without any notice. This product has been subject to limited testing and should not be used in conjunction with lifesupport or other mission-critical equipment or applications. Microsemi assumes no liability whatsoever, and Microsemi disclaims any express or implied warranty, relating to sale and/or use of Microsemi products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Any performance specifications believed to be reliable but are not verified and customer or user must conduct and complete all performance and other testing of this product as well as any user or customers final application. User or customer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the customer's and user's responsibility to independently determine suitability of any Microsemi product and to test and verify the same. The information contained herein is provided "AS IS, WHERE IS" and with all faults, and the entire risk associated with such information is entirely with the User. Microsemi specifically disclaims any liability of any kind including for consequential, incidental and punitive damages as well as lost profit. The product is subject to other terms and conditions which can be located on the web at <a href="http://www.microsemi.com/legal/tnc.asp">http://www.microsemi.com/legal/tnc.asp</a>

#### Life Support Application

Seller's Products are not designed, intended, or authorized for use as components in systems intended for space, aviation, surgical implant into the body, in other applications intended to support or sustain life, or for any other application in which the failure of the Seller's Product could create a situation where personal injury, death or property damage or loss may occur (collectively "Life Support Applications").

Buyer agrees not to use Products in any Life Support Applications and to the extent it does it shall conduct extensive testing of the Product in such applications and further agrees to indemnify and hold Seller, and its officers, employees, subsidiaries, affiliates, agents, sales representatives and distributors harmless against all claims, costs, damages and expenses, and attorneys' fees and costs arising, directly or directly, out of any claims of personal injury, death, damage or otherwise associated with the use of the goods in Life Support Applications, even if such claim includes allegations that Seller was negligent regarding the design or manufacture of the goods.

Buyer must notify Seller in writing before using Seller's Products in Life Support Applications. Seller will study with Buyer alternative solutions to meet Buyer application specification based on Sellers sales conditions applicable for the new proposed specific part.