Linear, Fixed Constant Current LED Driver

Features

- ▶ 25mA ±10% constant current drive
- ▶ 1.0V dropout
- ▶ 90V rating for transient immunity
- Temperature compensated
- ▶ 4.75 90V supply range

Applications

- Specialty lighting
- Low voltage signage

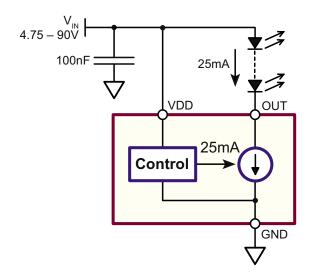
General Description

The CL525 is a fixed, linear current regulator designed for driving LEDs at 25mA. With a maximum rating of 90V, it is able to withstand transients without the need for additional transient protection circuitry. It is ideally suited for applications employing single or multiple LEDs.

The minimum dropout voltage of 1.0V accommodates extra LEDs, permits lower supply voltages, and provides more efficient operation.

The CL525 is offered in TO-252(D-PAK) and TO-92 packages.

Typical Application Circuit



Ordering Information

	Package Options						
Device	TO-252 (D-PAK)	TO-92					
CL525	CL525K4-G	CL525N3-G					

-G indicates package is RoHS compliant ('Green')



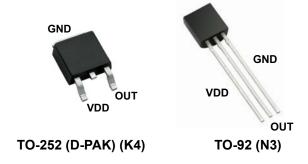


Absolute Maximum Ratings

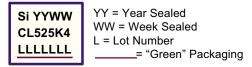
Parameter	Value
Supply voltage, V _{DD}	-0.5V to +100V
Output voltage, V _{OUT}	-0.5V to +100V
Operating junction temperature	-40°C to +125°C
Storage temperature	-65°C to +150°C

Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these conditions is not implied. Continuous operation of the device at the absolute rating level may affect device reliability. All voltages are referenced to device ground..

Pin Configurations

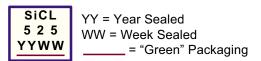


Product Marking



Package may or may not include the following marks: Si or 🍿

TO-252 (D-PAK) (K4)



Package may or may not include the following marks: Si or **TO-92 (N3)**

Recommended Operating Conditions (all voltages with respect to GND pin)

Sym	Parameter	Min	Тур	Max	Units	Conditions
$V_{_{\mathrm{DD}}}$	Supply voltage	4.75	-	90	V	
V _{out}	Voltage at OUT pin ¹	1.0	-	90	V	
T_{J}	Junction temperature	-40	-	125	°C	
C _{DD}	V _{DD} bypass capacitor	100	-	-	nF	

Thermal Characteristics

Sym	Parameter		Min	Тур	Max	Units	Conditions
$ heta_{jc}$	D-PAK		-	30	-	°C/W	
	Thermal resistance, junction to case	-	N/A	-	°C/VV		
	Thormal registers of investigate ambient	D-PAK	-	81	-	00/14/	
θ_{ja}	Thermal resistance, junction to ambient	TO-92	-	132	-	°C/W	

Notes:

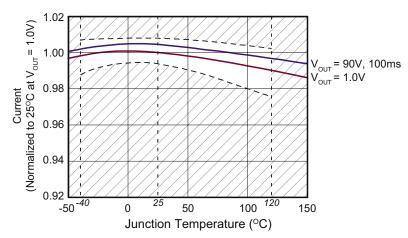
1. Thermal considerations may limit voltage to less than 90V.

Electrical Characteristics

(Over recommended operating conditions. T_A = 25°C unless otherwise specified. All voltages with respect to GND pin)

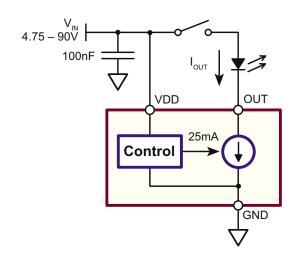
Sym	Parameter	Min	Тур	Max	Units	Conditions
I _{DD}	Current into VDD pin	-	-	1.0	mA	
	0		25	27.5	mΛ	1.0V < V _{OUT} < 90V
OUT	Current into OUT pin	-	-	27.5	mA	V _{OUT} <1.0V
l _{OUT(OFF)}	Current into OUT pin with VDD pin open	-	-	10	μA	V _{DD} = open
V _{DD(OFF)}	Voltage at VDD to shut off LED current	-	-	1.0	V	I _{OUT} < 10μA
t _{on}	VDD applied on time	-	-	100	μs	
t _{OFF}	VDD removed off time	-	-	100	μs	

Temperature Effects

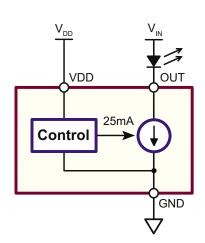


Application Circuits

Switched LED

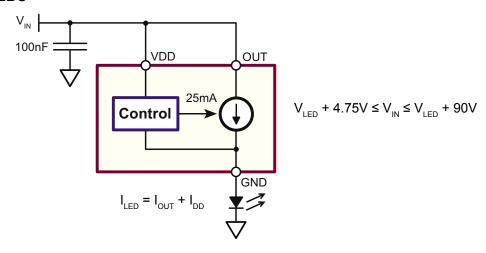


Seperate LED Supply $(V_{OUT}$ may be higher or lower than V_{DD} .)



Application Circuits

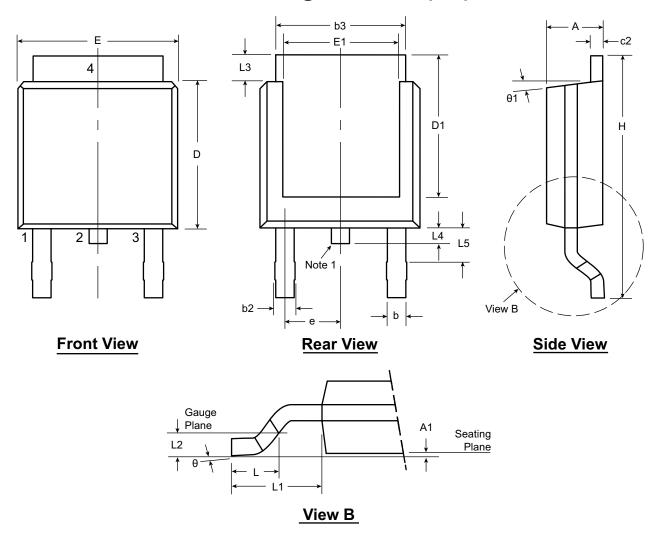
Ground Referenced LEDs



Pin Description

Pi	n #		
TO-252 D-PAK (K4)	TO-92 (N3)	Name	Description
1	1	VDD	Supply voltage for the CL525. Bypass locally with a 100nF capacitor to ground.
3	2	OUT	Constant current output (sink).
4	3	GND	Circuit common.

3-Lead TO-252 D-PAK Package Outline (K4)



Note:

1. Although 4 terminal locations are shown, only 3 are functional. Lead number 2 was removed.

Symb	ol	A	A1	b	b2	b3	c2	D	D1	Е	E1	е	Н	L	L1	L2	L3	L4	L5	θ	θ1
Dimen- sion (inches)	MIN	.086	.000*	.025	.030	.195	.018	.235	.205	.250	.170		.370	.055			.035	.025*	.045	00	00
	NOM	-	-	-	-	-	-	.240	-	-	-	.090 BSC	-	.060	.108 REF	.020 BSC	-	-	-	-	-
	MAX	.094	.005	.035	.045	.215	.035	.245	.217*	.265	.182*		.410	.070			.050	.040	.060	10º	15º

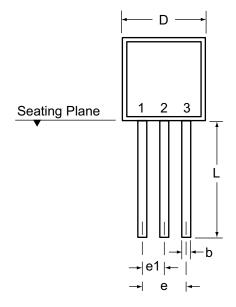
JEDEC Registration TO-252, Variation AA, Issue E, June 2004.

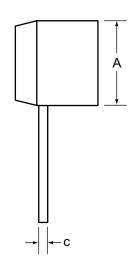
Drawings not to scale.

Supertex Doc. #: DSPD-3TO252K4, Version E041309.

^{*} This dimension is not specified in the JEDEC drawing.

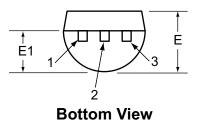
3-Lead TO-92 Package Outline (N3)





Front View

Side View



Symbol		Α	b	С	D	E	E1	е	e1	L
	MIN	.170	.014 [†]	.014 [†]	.175	.125	.080	.095	.045	.500
Dimensions (inches)	NOM	-	-	-	-	-	-	-	-	-
	MAX	.210	.022 [†]	.022 [†]	.205	.165	.105	.105	.055	.610*

JEDEC Registration TO-92.

Drawings not to scale.

Supertex Doc.#: DSPD-3TO92N3, Version E041009.

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to http://www.supertex.com/packaging.html.)

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^{*} This dimension is not specified in the JEDEC drawing.

[†] This dimension differs from the JEDEC drawing.