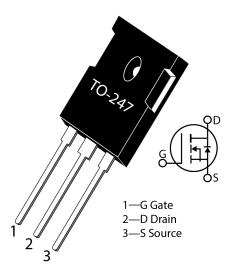


MSC040SMA120B Silicon Carbide N-Channel Power MOSFET

1 Product Overview

The silicon carbide (SiC) power MOSFET product line from Microsemi increases the performance over silicon MOSFET and silicon IGBT solutions while lowering the total cost of ownership for high-voltage applications. The MSC040SMA120B device is a 1200 V, 40 m Ω SiC MOSFET in a TO-247 package.



1.1 Features

The following are key features of the MSC040SMA120B device:

- Low capacitances and low gate charge
- Fast switching speed due to low internal gate resistance (ESR)
- Stable operation at high junction temperature, T_J(max) = 175°C
- Fast and reliable body diode
- Superior avalanche ruggedness
- RoHS compliant

1.2 Benefits

The following are benefits of the MSC040SMA120B device:

- High efficiency to enable lighter, more compact system
- Simple to drive and easy to parallel
- Improved thermal capabilities and lower switching losses
- Eliminates the need for external freewheeling diode
- Lower system cost of ownership

1.3 Applications

The MSC040SMA120B device is designed for the following applications:

- PV inverter, converter, and industrial motor drives
- Smart grid transmission and distribution
- Induction heating and welding
- H/EV powertrain and EV charger
- Power supply and distribution



2 Device Specifications

This section shows the specifications for the MSC040SMA120B device.

2.1 Absolute Maximum Ratings

The following table shows the absolute maximum ratings for the MSC040SMA120B device.

Table 1 • Absolute Maximum Ratings

Symbol	Parameter	Ratings	Unit
VDSS	Drain source voltage	1200	V
ID	Continuous drain current at Tc = 25 °C	66	Α
	Continuous drain current at Tc = 100 °C	46	_
Ірм	Pulsed drain current ¹	105	_
V _G s	Gate-source voltage	23 to -10	V
P _D	Total power dissipation at Tc = 25 °C	323	W
	Linear derating factor	2.15	W/°C

Note:

1. Repetitive Rating: Pulse width and case temperature limited by maximum junction temperature.

The following table shows the thermal and mechanical characteristics for the MSC040SMA120B device.

Table 2 • Thermal and Mechanical Characteristics

Symbol	Characteristic	Min	Тур	Max	Unit
Rejc	Junction-to-case thermal resistance		0.31	0.47	°C/W
Tı	Operating junction temperature	-55		175	°C
Тѕтс	Storage temperature	- 55		150	_
Tι	Soldering temperature for 10 seconds (1.6 mm from case)			260	_
	Mounting torque, 6-32 or M3 screw			10	lbf-in
				1.1	N-m
Wt	Package weight		0.22		oz
			6.2		g



2.2 Electrical Performance

The following table shows the static characteristics for the MSC040SMA120B device. $T_1 = 25$ °C unless otherwise specified.

Table 3 • Static Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
V(BR) DSS	Drain-source breakdown voltage	V_{GS} = 0 V, I_D = 100 μA	1200			V
R _{DS(on)}	Drain-source on resistance 1	$V_{GS} = 20 \text{ V}, I_D = 40 \text{ A}$		40	50	mΩ
V _{GS(th)}	Gate-source threshold voltage	$V_{GS} = V_{DS}$, $I_D = 2 \text{ mA}$	1.8	2.7		V
$\Delta V_{GS(th)}$ / ΔT_J	Threshold voltage coefficient	$V_{GS} = V_{DS}$, $I_D = 2 \text{ mA}$		-4.7		mV/°C
loss	Zero gate voltage drain current	V _{DS} = 1200 V, V _{GS} = 0 V			100	μΑ
		V _{DS} = 1200 V, V _{GS} = 0 V T _J = 125 °C			500	_
Igss	Gate-source leakage current	V _{GS} = 20 V / -10 V			±100	nA

Note:

1. Pulse test: pulse width $< 380 \mu s$, duty cycle < 2%.

The following table shows the dynamic characteristics for the MSC040SMA120B device. $T_1 = 25$ °C unless otherwise specified.

Table 4 • Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
Ciss	Input capacitance	$V_{GS} = 0 V$		1990		pF
Crss	Reverse transfer capacitance	V _{DD} = 1000 V		17		-
Coss	Output capacitance	Output capacitance $V_{AC} = 25 \text{ mV}$ $f = 1 \text{ MHz}$		156		-
Qg	Total gate charge	V _{GS} = -5 V/20 V	137			nC
Qgs	Gate-source charge	$V_{DD} = 800 \text{ V}$ $I_D = 40 \text{ A}$	29		-	
Qgd	Gate-drain charge	10 - 40 A	-	31		=
t _{d(on)}	Turn-on delay time	V _{DD} = 800 V		24		ns
tr	Current rise time	V _{GS} = -5 V/20 V I _D = 40 A		13		-
td(off)	Turn-off delay time	$R_{G (ext)} = 4 \Omega^{1}$		46		_
tf	Current fall time	Freewheeling diode =		13		-
Eon	Turn-on switching energy ²	MSC040SMA120B (V _{GS} = -5 V)		560		μЈ
Eoff	Turn-off switching energy			82		-
t _{d(on)}	Turn-on delay time	V _{DD} = 800 V		23		ns
tr	Current rise time	V _{GS} = -5 V/20 V I _D = 40 A		10		-
t _{d(off)}	Turn-off delay time	$R_{G \text{ (ext)}} = 4 \Omega^{1}$		44		-
tf	Current fall time	Freewheeling diode =		11		-
Eon	Turn-on switching energy ²	— MSC015SDA120B		275		μЈ
Eoff	Turn-off switching energy	<u> </u>		83		-
ESR	Equivalent series resistance	f = 1 MHz, 25 mV, drain short		1.2		Ω
SCWT	Short circuit withstand time	V _{DS} = 960 V, V _{GS} = 20 V, T _C = 25 °C		3		μs



Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
Eas	Avalanche energy, single pulse	V _{DS} = 145 V, V _{GS} = 20 V, I _D = 40 A, T _C = 25 °C		2000		mJ

Notes:

- 1. R_G is total gate resistance excluding internal gate driver impedance.
- 2. Eon includes energy of freewheeling diode.

The following table shows the body diode characteristics for the MSC040SMA120B device. $T_J = 25$ °C unless otherwise specified.

Table 5 • Body Diode Characteristics

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
V _{SD}	Diode forward voltage	I _{SD} = 40 A, V _{GS} = 0 V		3.9		V
		I _{SD} = 40 A, V _{GS} = -5 V		4.1		V
trr	Reverse recovery time	$I_{SD} = 40 \text{ A}, V_{GS} = -5 \text{ V}$		31		ns
Qrr	Reverse recovery charge	V _{DD} = 800 V dl/dt = −1800 A/μs		610		nC
IRRM	Reverse recovery current	αη ατ 1500 / η μ5		40		Α

2.3 Typical Performance Curves

This section shows the typical performance curves for the MSC040SMA120B device.

Figure 1 • Drain Current vs. Drain-to-Source Voltage

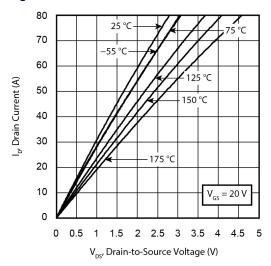


Figure 2 • Drain Current vs. Drain-to-Source Voltage.

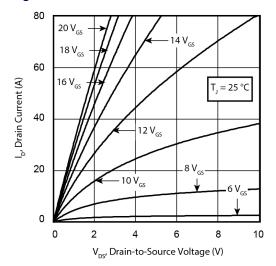




Figure 3 • Drain Current vs. Drain-to-Source Voltage

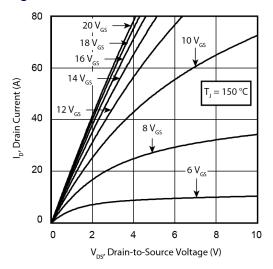


Figure 5 • RDS(on) vs. Junction Temperature

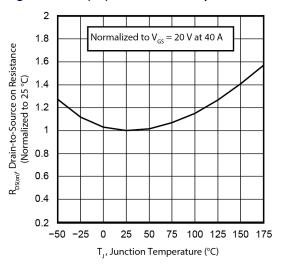


Figure 7 • Capacitance vs. Drain-to-Source Voltage

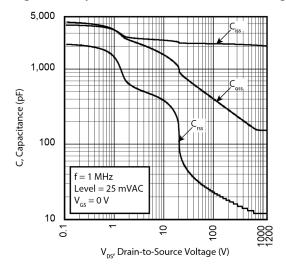


Figure 4 • Drain Current vs. Drain-to-Source Voltage

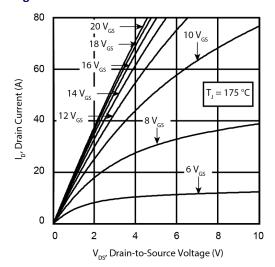


Figure 6 • Gate Charge Characteristics

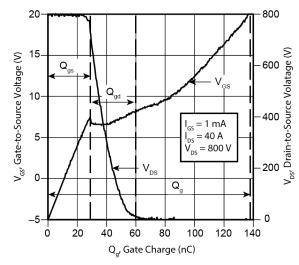


Figure 8 • IDM vs. Gate-to-Source Voltage

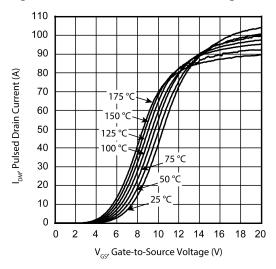




Figure 9 • IDM vs. VDS Third Quadrant Conduction

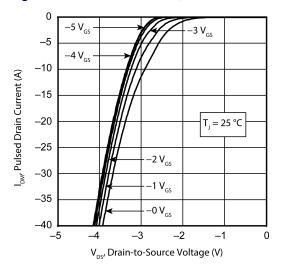


Figure 11 • VGS(th) vs. Junction Temperature

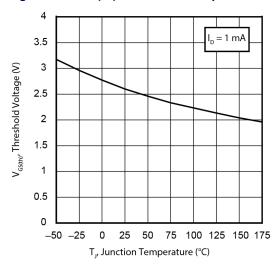


Figure 10 • IDM vs. VDS Third Quadrant Conduction

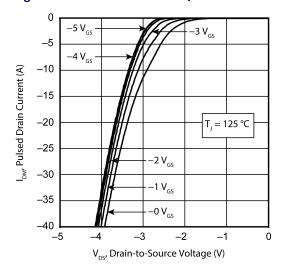


Figure 12 • Forward Safe Operating Area

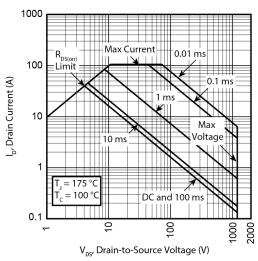
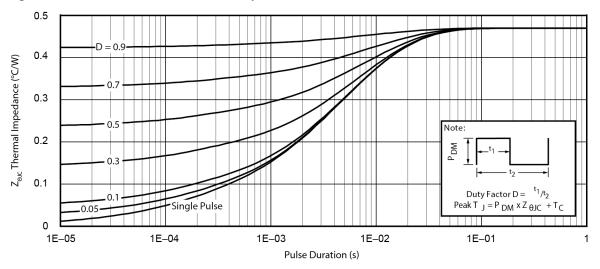


Figure 13 • Maximum Transient Thermal Impedance





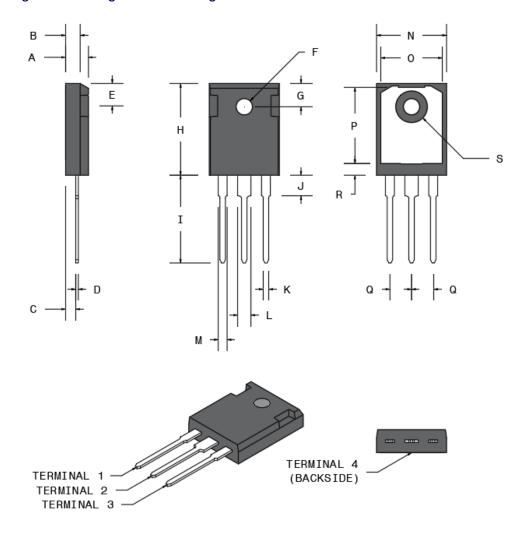
3 Package Specification

This section shows the package specification for the MSC040SMA120B device.

3.1 Package Outline Drawing

The following figure illustrates the TO-247 package drawing for the MSC040SMA120B device. The dimensions in the figure below are in millimeters and (inches).

Figure 14 • Package Outline Drawing





The following table shows the MSC040SMA120B dimensions and should be used in conjunction with the package outline drawing.

Table 6 • TO-247 Dimensions

Symbol	Min (mm)	Max (mm)	Min (in.)	Max (in.)
Α	4.69	5.31	0.185	0.209
В	1.49	2.49	0.059	0.098
С	2.21	2.59	0.087	0.102
D	0.40	0.79	0.016	0.031
E	5.38	6.20	0.212	0.244
F	3.50	3.81	0.138	0.150
G	6.15 BSC		0.242 BSC	
Н	20.80	21.46	0.819	0.845
I	19.81	20.32	0.780	0.800
J	4.00	4.50	0.157	0.177
K	1.01	1.40	0.040	0.055
L	2.87	3.12	0.113	0.123
М	1.65	2.13	0.065	0.084
N	15.49	16.26	0.610	0.640
0	13.50	14.50	0.531	0.571
Р	16.50	17.50	0.650	0.689
Q	5.45 BSC		0.215 BSC	
R	2.00	2.75	0.079	0.108
S	7.10	7.50	0.280	0.295
Terminal 1	Gate			
Terminal 2	Drain			
Terminal 3	Source			
Terminal 4	Drain			





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