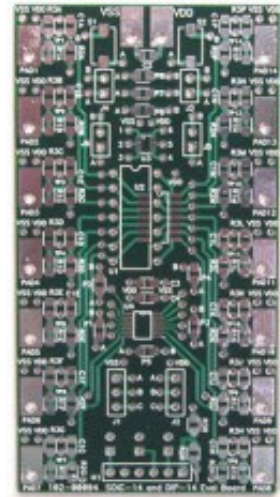# 14-pin SOIC/TSSOP/DIP Evaluation Board
Part Number: SOIC14EV

BUY IT NOW

Share

The 14-Lead SOIC/TSSOP/DIP Evaluation Board allows the system designer to quickly evaluate the operation of Microchip Technology's devices in either SOIC, DIP, or TSSOP packages.

Features   Package Contents   Specifications

- Connection Terminals may be either surface mount or through hole

- Three 14-pin footprints are supported (SOIC, DIP, and TSSOP)

- Footprints for optional passive components and other devices for:

  - Power Supply Filtering

  - Device bypass capacitors

  - Output filtering

  - Output pull-up resistor

  - Output pull-down resistor

  - Output loading resistor

  - Output series resistor

  - Up to four additional passive components

  - Jumper Headers

  - SOT-23-6 device

- Silk screen area to write specifics of implemented circuit (on back of PCB)

- PICmicro MCU Baseline Flash Microcontroller Programmer (BFMP) Header

- Can be used for SOIC-14 or TSSOP-14 to DIP-14 converter

Documentation & Software

Documents	Last Updated	Size	
Digital Potentiometer Design Guide	8/3/2016 10:23:58 AM	4MB	
14-pin SOIC/TSSOP/DIP Evaluation Board Gerbers	2/6/2006 4:17:18 PM	215KB	
14-Pin SOIC/DIP/TSSOP Evaluation Board User's Guide	1/31/2006 3:00:21 PM	1MB	

Products | Applications | Design Support | Training | Sample and Buy | About Us | Contact Us | Legal | Investors | Careers | Support