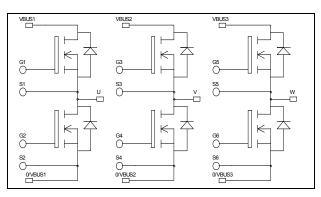


Triple phase leg MOSFET Power Module



VBUS1 VBUS2 VBUS3 • G1 • G3 • G5 • S1 • S3 • S5 • O/VBUS1 • O/VBUS2 • O/VBUS3 • S2 • S4 • S6 • G2 • G4 • G6

Absolute maximum ratings

$V_{\rm DSS} = 500V$

$R_{DSon} = 65m\Omega \text{ typ} @ Tj = 25^{\circ}C$ $I_D = 51A @ Tc = 25^{\circ}C$

Application

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

Features

• Power MOS 7[®] FREDFETs

- Low R_{DSon}
 - Low input and Miller capacitance
- Low gate charge
- Fast intrinsic reverse diode
- Avalanche energy rated
- Very rugged
- Kelvin source for easy drive
- Very low stray inductance
 - Symmetrical design
 - Lead frames for power connections
- High level of integration

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Very low (12mm) profile
- Each leg can be easily paralleled to achieve a phase leg of three times the current capability
- Module can be configured as a three phase bridge
- Module can be configured as a boost followed by a full bridge
- RoHS Compliant

Symbol	Parameter		Max ratings	Unit
V _{DSS}	Drain - Source Breakdown Voltage		500	V
I _D	Continuous Drain Current	$T_c = 25^{\circ}C$	51	
	Continuous Drain Current	$T_c = 80^{\circ}C$	38	А
I _{DM}	Pulsed Drain current	204		
V _{GS}	Gate - Source Voltage		±30	V
R _{DSon}	Drain - Source ON Resistance		78	mΩ
P _D	Maximum Power Dissipation $T_c = 25^{\circ}C$		390	W
I _{AR}	Avalanche current (repetitive and non repetitive)		51	Α
E _{AR}	Repetitive Avalanche Energy		50	I III
E _{AS}	Single Pulse Avalanche Energy		3000	mJ

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

www.microsemi.com

APTM50TAM65FPG - Rev 2 October, 2012





All ratings (a) $T_j = 25^{\circ}C$ unless otherwise specified

Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
I _{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 500V$ $T_j = 25^{\circ}C$			100	
		$V_{GS} = 0V, V_{DS} = 400V$ $T_j = 125^{\circ}C$			500	μA
R _{DS(on)}	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 25.5A$		65	78	mΩ
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 2.5 \text{mA}$	3		5	V
I _{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
C _{iss}	Input Capacitance	$V_{GS} = 0V$		7000		
Coss	Output Capacitance	$V_{\rm DS} = 25 V$		1400		pF
C _{rss}	Reverse Transfer Capacitance	f=1MHz		90		
Q_{g}	Total gate Charge	$V_{GS} = 10V$		140		
Q_{gs}	Gate – Source Charge	$V_{Bus} = 250V$		40		nC
Q_{gd}	Gate – Drain Charge	$I_D = 51A$		70		
T _{d(on)}	Turn-on Delay Time	Inductive switching @ 125°C		21		
T _r	Rise Time	$V_{GS} = 15V$ $V_{Bus} = 333V$ $I_D = 51A$ $R_G = 3\Omega$		38		ns
T _{d(off)}	Turn-off Delay Time			75		
$T_{\rm f}$	Fall Time			93		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C		1035		T
E _{off}	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 333V$ $I_D = 51A, R_G = 3\Omega$		845		μJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C $V_{GS} = 15V$, $V_{Bus} = 333V$ $I_D = 51A$, $R_G = 3\Omega$		1556		Ŧ
E _{off}	Turn-off Switching Energy			1013		μJ

Source - Drain diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
Is	Continuous Source current		$Tc = 25^{\circ}C$			51	
	(Body diode)		$Tc = 80^{\circ}C$			38	A
V_{SD}	Diode Forward Voltage	$V_{GS} = 0V, I_S = -51A$	4			1.3	V
dv/dt	Peak Diode Recovery O					15	V/ns
t _{rr}	Poverse Peeevery Time		$T_j = 25^{\circ}C$			270	
	Reverse Recovery Time	$I_{\rm S} = -51A$ $V_{\rm R} = 333V$	$T_{j} = 125^{\circ}C$			540	ns
Q _{rr}	Poverse Recovery Charge	$v_R = 333 v$ $di_s/dt = 100 A/\mu s$	$T_j = 25^{\circ}C$		2.6		
	Q _{rr} Reverse Recovery Charge	~ '	$T_{j} = 125^{\circ}C$		9.6		μC

• dv/dt numbers reflect the limitations of the circuit rather than the device itself. $I \leq 51A = di/dt \leq 700A/us = V \leq V = T \leq 150^{\circ}C$

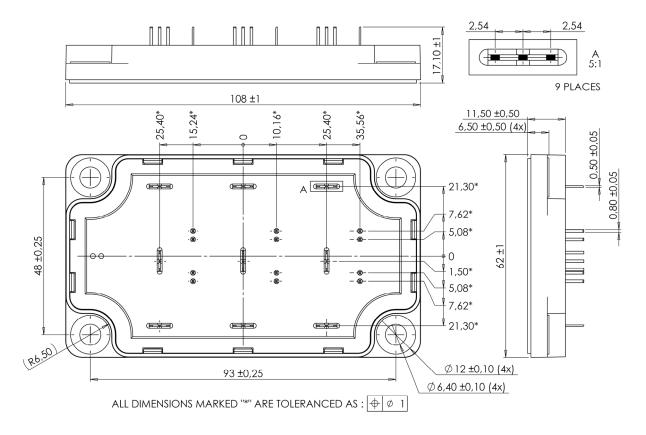
$$I_{\rm S} \le -51$$
 di/dt ≤ 700 A/µs $V_{\rm R} \le V_{\rm DSS}$ $T_{\rm j} \le 150^{\circ}$ C



Thermal and package characteristics

Symbol	Characteristic			Min	Тур	Max	Unit
R _{thJC}	Junction to Case Thermal Resistance IGBT				0.32	°C/W	
V _{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000			V
T _J	Operating junction temperature range			-40		150	
T _{STG}	Storage Temperature Range			-40		125	°C
T _C	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M6	3		5	N.m
Wt	Package Weight					250	g

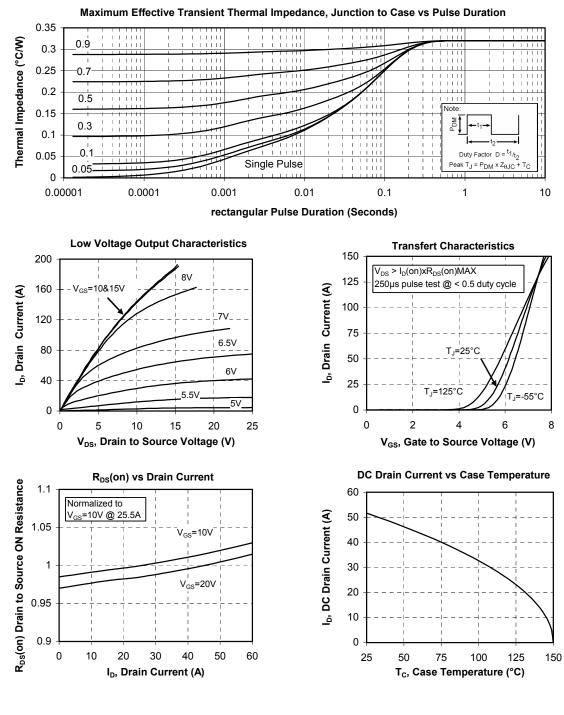
SP6-P Package outline (dimensions in mm)



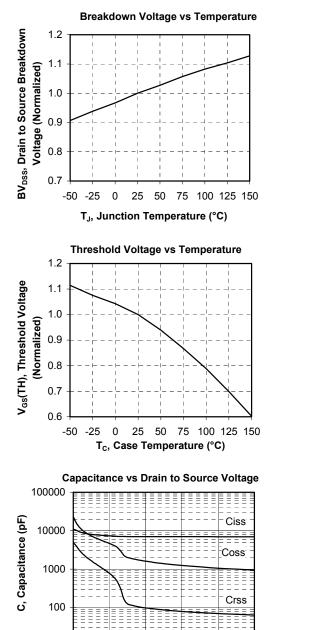
See application note 1902 - Mounting Instructions for SP6-P (12mm) Power Modules on www.microsemi.com

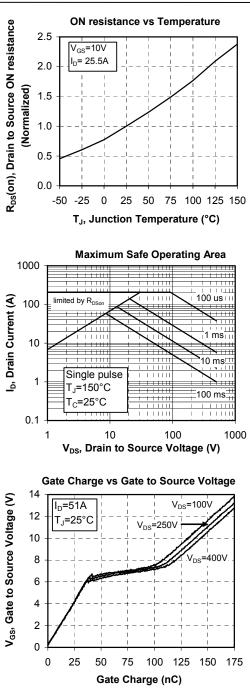


Typical Performance Curve









5 - 7

www.microsemi.com

10

0

10

20

V_{DS}, Drain to Source Voltage (V)

30

40

50



Delay Times vs Current

APTM50TAM65FPG Rise and Fall times vs Current

70 80

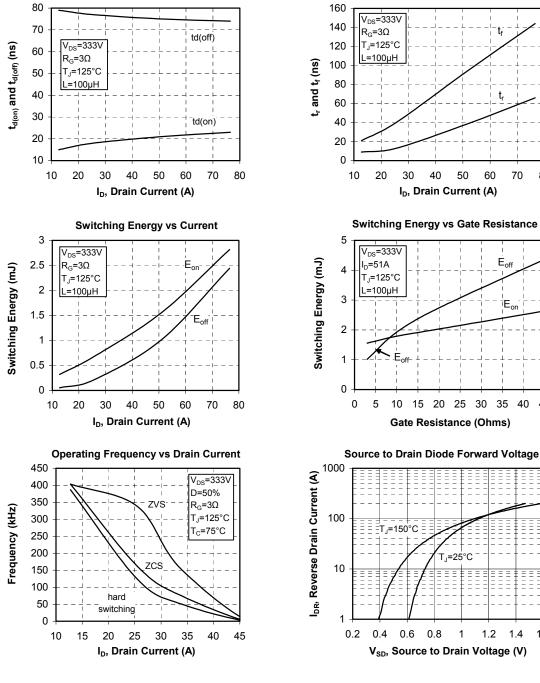
E_{off}

Eon

40 45

1.4

1.6



APTM50TAM65FPG-Rev 2 October, 2012

6 - 7



DISCLAIMER

The information contained in the document (unless it is publicly available on the Web without access restrictions) is PROPRIETARY AND CONFIDENTIAL information of Microsemi and cannot be copied, published, uploaded, posted, transmitted, distributed or disclosed or used without the express duly signed written consent of Microsemi. If the recipient of this document has entered into a disclosure agreement with Microsemi, then the terms of such Agreement will also apply. This document and the information contained herein may not be modified, by any person other than authorized personnel of Microsemi. No license under any patent, copyright, trade secret or other intellectual property right is granted to or conferred upon you by disclosure or delivery of the information, either expressly, by implication, inducement, estoppels or otherwise. Any license under such intellectual property rights must be approved by Microsemi in writing signed by an officer of Microsemi.

Microsemi reserves the right to change the configuration, functionality and performance of its products at anytime without any notice. This product has been subject to limited testing and should not be used in conjunction with lifesupport or other mission-critical equipment or applications. Microsemi assumes no liability whatsoever, and Microsemi disclaims any express or implied warranty, relating to sale and/or use of Microsemi products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Any performance specifications believed to be reliable but are not verified and customer or user must conduct and complete all performance and other testing of this product as well as any user or customers final application. User or customer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the customer's and user's responsibility to independently determine suitability of any Microsemi product and to test and verify the same. The information contained herein is provided "AS IS, WHERE IS" and with all faults, and the entire risk associated with such information is entirely with the User. Microsemi specifically disclaims any liability of any kind including for consequential, incidental and punitive damages as well as lost profit. The product is subject to other terms and conditions which can be located on the web at http://www.microsemi.com/legal/tnc.asp

Life Support Application

Seller's Products are not designed, intended, or authorized for use as components in systems intended for space, aviation, surgical implant into the body, in other applications intended to support or sustain life, or for any other application in which the failure of the Seller's Product could create a situation where personal injury, death or property damage or loss may occur (collectively "Life Support Applications").

Buyer agrees not to use Products in any Life Support Applications and to the extent it does it shall conduct extensive testing of the Product in such applications and further agrees to indemnify and hold Seller, and its officers, employees, subsidiaries, affiliates, agents, sales representatives and distributors harmless against all claims, costs, damages and expenses, and attorneys' fees and costs arising, directly or directly, out of any claims of personal injury, death, damage or otherwise associated with the use of the goods in Life Support Applications, even if such claim includes allegations that Seller was negligent regarding the design or manufacture of the goods.

Buyer must notify Seller in writing before using Seller's Products in Life Support Applications. Seller will study with Buyer alternative solutions to meet Buyer application specification based on Sellers sales conditions applicable for the new proposed specific part.