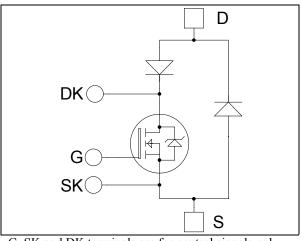
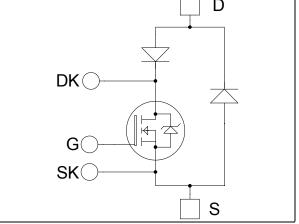


### Single switch Series & SiC parallel diodes **MOSFET Power Module**

 $V_{DSS} = 1200V$  $R_{DSon} = 100 \text{m}\Omega \text{ typ } @ \text{Tj} = 25^{\circ}\text{C}$  $I_D = 116A$  @ Tc = 25°C



G, SK and DK terminals are for control signals only (not for power)



**Application** 

**Features** Power MOS 7® MOSFETs

Welding converters

Switched Mode Power Supplies Uninterruptible Power Supplies

 $Low\;R_{DSon}$ 

Motor control

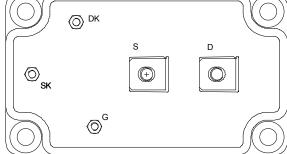
- Low input and Miller capacitance
- Low gate charge
- Avalanche energy rated
- Very rugged

#### SiC Parallel Schottky Diode

- Zero reverse recovery
- Zero forward recovery
- Temperature Independent switching behavior
- Positive temperature coefficient on VF
- Kelvin source for easy drive
- Kelvin drain for voltage monitoring
- Very low stray inductance
  - Symmetrical design
  - M5 power connectors
  - M3 power connectors
- High level of integration
- AlN substrate for improved MOSFET thermal performance



- Outstanding performance high frequency at operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Low profile
- **RoHS Compliant**



### All ratings @ $T_i = 25^{\circ}C$ unless otherwise specified

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

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Absolute maximum ratings

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		1200	V
T	Continuous Drain Current T <sub>c</sub>	$T_c = 25^{\circ}C$	116	
$I_{D}$	Continuous Diam Current	$T_c = 80$ °C	86	Α
$I_{DM}$	Pulsed Drain current		464	
$V_{GS}$	Gate - Source Voltage	±30	V	
$R_{DSon}$	Drain - Source ON Resistance	120	$m\Omega$	
$P_{D}$	Maximum Power Dissipation	3290	W	
$I_{AR}$	Avalanche current (repetitive and non repetitive)	24	Α	
$E_{AR}$	Repetitive Avalanche Energy		50	I
$E_{AS}$	Single Pulse Avalanche Energy		3200	mJ

**Electrical Characteristics** 

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit	
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 1200V$ $T_j = 25^{\circ}C$	;		1	4	
		$V_{GS} = 0V, V_{DS} = 1000V$ $T_j = 125^{\circ}$	C		3	mA	
R <sub>DS(on)</sub>	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 58A$		100	120	mΩ	
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 20$ mA	3		5	V	
$I_{GSS}$	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$			±400	nA	

**Dynamic Characteristics** 

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
$C_{iss}$	Input Capacitance	$V_{GS} = 0V$		28.9		
$C_{oss}$	Output Capacitance	$V_{DS} = 25V$		4.4		nF
$C_{rss}$	Reverse Transfer Capacitance	f=1MHz		0.8		
$Q_{g}$	Total gate Charge	$V_{GS} = 10V$		1100		
$Q_{gs}$	Gate – Source Charge	$V_{\text{Bus}} = 600 \text{V}$		128		nC
$Q_{gd}$	Gate – Drain Charge	$I_D = 116A$		716		
$T_{d(on)}$	Turn-on Delay Time	Inductive switching @ 125°C		20		
$T_{\rm r}$	Rise Time	$V_{GS} = 15V$		17		ns
$T_{d(off)}$	Turn-off Delay Time	$V_{\text{Bus}} = 800V$ $I_{\text{D}} = 116A$		245		
$T_{\mathrm{f}}$	Fall Time	$R_G = 1.2\Omega$		62		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C		3		I
E <sub>off</sub>	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 800V$ $I_D = 116A, R_G = 1.2\Omega$		4.6		mJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C $V_{GS} = 15V$ , $V_{Bus} = 800V$ $I_D = 116A$ , $R_G = 1.2\Omega$		5.5		mJ
$E_{\text{off}}$	Turn-off Switching Energy			5.6		1113
$R_{thJC}$	Junction to Case Thermal Resistance	ce			0.038	°C/W



Series diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
$V_{RRM}$	Maximum Peak Repetitive Reverse Voltage			1000			V
$I_{RM}$	Maximum Reverse Leakage Current	V <sub>R</sub> =1000V				500	μΑ
$I_{\mathrm{F}}$	DC Forward Current		$T_c = 100^{\circ}C$		240		A
		$I_F = 240A$			1.9	2.5	
$V_{\mathrm{F}}$	Diode Forward Voltage	$I_F = 480A$			2.2		V
		$I_F = 240A$	$T_{j} = 125^{\circ}C$		1.7		
+	Reverse Recovery Time		$T_j = 25$ °C		280		nc
$t_{rr}$		$I_F = 240A$ $V_R = 667V$	$T_{j} = 125^{\circ}C$		350		ns
Q <sub>rr</sub>	Reverse Recovery Charge	$di/dt = 800A/\mu s$	$T_j = 25$ °C		3		μС
			$T_{j} = 125^{\circ}C$		14.4		μ
$R_{thJC}$	Junction to Case Thermal Resistance					0.19	°C/W

SiC Parallel diode ratings and characteristics

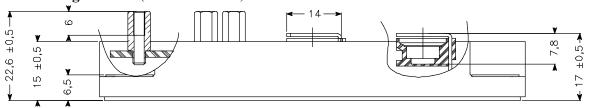
Symbol	Characteristic	Test Conditions			Тур	Max	Unit
$V_{RRM}$	Maximum Peak Repetitive Reverse Voltage			1200			V
T	Inv.   Maximum Reverse Leakage Current   Vp=1200V	V -1200V	$T_j = 25^{\circ}C$		288	1800	4
$\mathbf{I}_{\mathrm{RM}}$		$T_j = 175$ °C		504	9000	μΑ	
$I_F$	DC Forward Current	$Tc = 100^{\circ}C$			90		Α
V	Diode Forward Voltage	$I_F = 90A$	$T_i = 25$ °C	1.0	1.6	1.8	V
$V_{\mathrm{F}}$	Diode Forward Voltage	1 <sub>F</sub> – 90A	$T_{i} = 175^{\circ}C$		2.3	3	V
$Q_{\rm C}$	Total Capacitive Charge		$I_F = 90A, V_R = 1200V$		720		пC
QC	Total Capacitive Charge	$di/dt = 4500A/\mu s$			720		пс
С	Total Conscitance	$f = 1 MHz, V_R = 200V$		86	864		рF
	Total Capacitance	$f = 1 MHz, V_R = 400 V$			621		þΓ
$R_{thJC}$	Junction to Case Thermal Resistance					0.22	°C/W

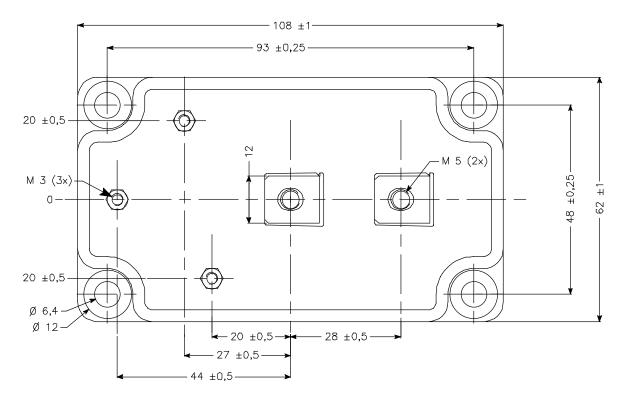
Thermal and package characteristics

Symbol	Characteristic			Min	Max	Unit			
$V_{ISOL}$	RMS Isolation Voltage, any terminal to ca	se $t = 1 \min_{0.5} \frac{50}{60}$	Hz	4000		V			
$T_{\rm J}$	Operating junction temperature range	Operating junction temperature range							
$T_{JOP}$	Recommended junction temperature under	switching conditi	ons	-40	T <sub>J</sub> max -25	°C			
$T_{STG}$	Storage Temperature Range			-40	125	1			
$T_{\rm C}$	Operating Case Temperature				100				
		To heatsink	M6	3	5				
Torque	Mounting torque	For terminals	M5	2	3.5	N.m			
	roi terminais M3		M3	1	1.5				
Wt	Package Weight				300	g			



### $SP6\ Package\ outline\ ({\rm dimensions\ in\ mm})$

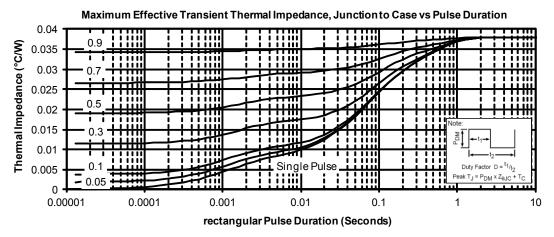


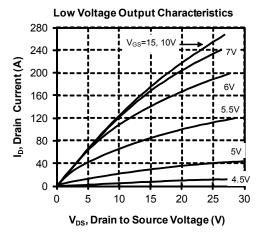


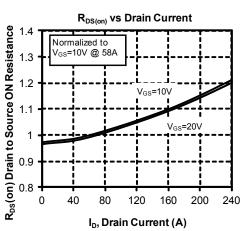
See application note APT0601 - Mounting Instructions for SP6 Power Modules on www.microsemi.com

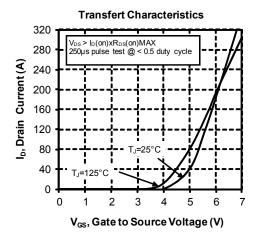


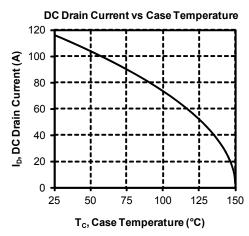
#### **Typical MOSFET Performance Curve**



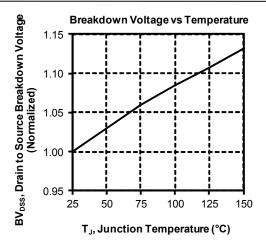


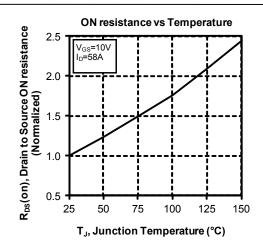


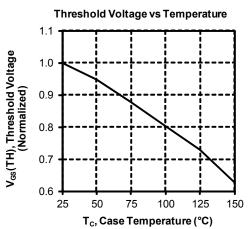


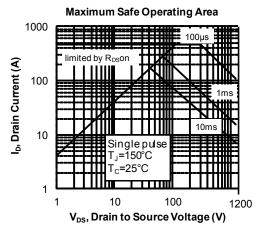


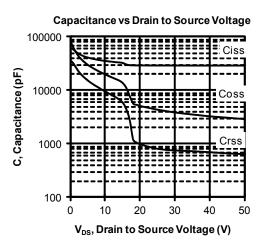


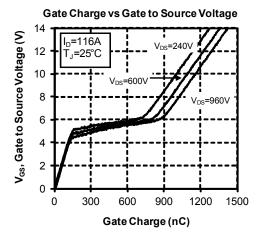




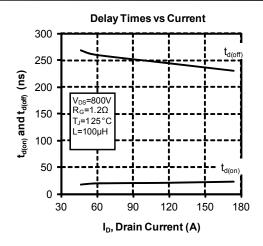


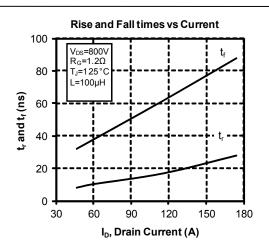


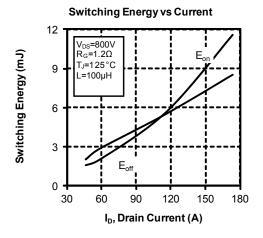


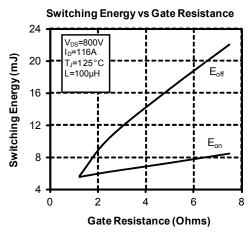


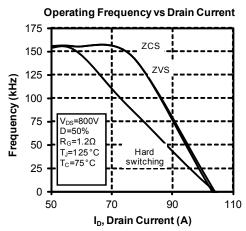


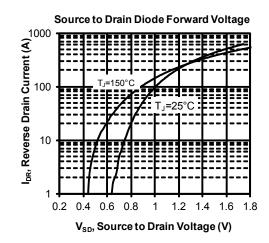








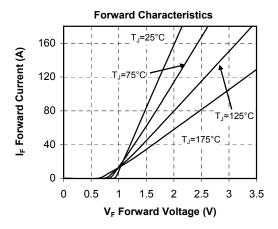


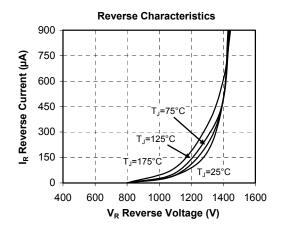


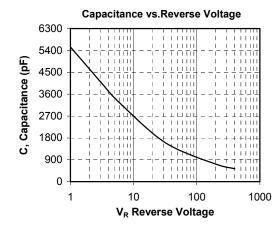


### **SiC Typical Performance Curve**

#### Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration 0.24 0.9 Thermal Impedance (°C/W) 0.2 0.7 0.16 0.5 0.12 0.3 0.08 0.04 Single Pulse 0.05 0 0.00001 0.0001 0.001 0.01 10 **Rectangular Pulse Duration (Seconds)**









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