



# Switching Diode Array Steering Diode TVS Array<sup>™</sup>

## DESCRIPTION

These low capacitance diode arrays are multiple, discrete, isolated junctions fabricated by a planar process and mounted in a 16-Pin DIP package for use as steering diodes protecting up to eight I/O ports from ESD, EFT, or surge by directing them either to the positive side of the power supply line or to ground (see figure 1). An external TVS diode may be added between the positive supply line and ground to prevent overvoltage on the supply rail. They may also be used in fast switching core-driver applications. This includes computers and peripheral equipment such as magnetic cores, thin-film memories, plated-wire memories, etc., as well as decoding or encoding applications. These arrays offer many advantages of integrated circuits such as high-density packaging and improved reliability. This is a result of fewer pick and place operations, smaller footprint, smaller weight, and elimination of various discrete packages that may not be as user friendly in PC board mounting. They are available with either tin-Lead plating terminations or as RoHS compliant with annealed matte-tin finish.

Important: For the latest information, visit our website http://www.microsemi.com.

FEATURES

- 8 diode arrays protects 8 lines
- Molded 16-Pin dual-in-line package
- UL 94V-0 flammability classification
- Low capacitance 1.5 pF per diode
- Switching speeds less than 5 ns
- Discrete diode elements offer optimum isolation
  - IEC 61000-4 compatible: 61000-4-2 (ESD): Air 15 kV, contact – 8 kV 61000-4-4 (EFT): 40 A – 5/50 ns 61000-4-5 (surge): 12 A, 8/20 µs
- RoHS compliant device is available

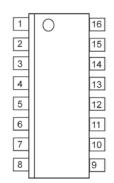
#### **APPLICATIONS / BENEFITS**

- Low capacitance steering diode protection for high frequency data lines
- Ideal for:

RS-232 & RS-422 Interface Networks. Ethernet: 10 Base T Computer I/O Ports LAN Switching Core Drivers



# 16 Pin Package



#### Top Viewing Pin Layout

#### MSC – Lawrence

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#### MSC – Ireland

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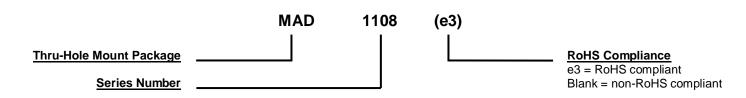
## MAXIMUM RATINGS

Parameters/Test Conditions		Symbol	Value	Unit
Junction and Storage Temperature		$T_{\rm J}$ and $T_{\rm STG}$	-55 to +150	°C/W
Peak Working Reverse Voltage		V <sub>RWM</sub>	75	V
Repetitive Peak Forward Current (one diode)		I <sub>FRM</sub>	400	mA
Forward Surge Current	@ 8.3 ms	I <sub>FSM</sub>	2	Α
	@ 8/20 μs		12	
Rated Average Power dissipation (total)		P <sub>M(AV)</sub>	1500	mW
Solder Temperature @ 10 s		T <sub>SP</sub>	260	°C

## **MECHANICAL and PACKAGING**

- CASE: Void-free transfer molded thermosetting epoxy body meeting UL94V-0 flammability classification
- TERMINALS: Tin-lead or RoHS compliant annealed matte-tin plating solderable per MIL-STD-750 method 2026
- MARKING: MSC logo, MAD1108 or MAD1108e3 and date code. Pin #1 is to the left of the dot or indent on top of package.
- DELIVERY OPTION: Carrier tube. Consult factory for quantities
- WEIGHT: Approximately 0.997 grams
- See Package Dimensions on last page.

## PART NOMENCLATURE



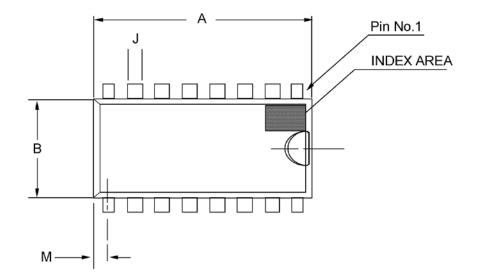
SYMBOLS & DEFINITIONS				
Symbol	Definition			
Ст	Total Capacitance: The total small signal capacitance between the diode terminals of a complete device.			
I <sub>R</sub>	Reverse Current: The dc current flowing from the external circuit into the cathode terminal at the specified voltage V <sub>R</sub> .			
V <sub>(BR)</sub>	Breakdown Voltage: The voltage across the device at a specified current I(BR) in the breakdown region.			
VF	Forward Voltage: A positive dc anode-cathode voltage the device will exhibit at a specified forward current.			
V <sub>RWM</sub>	Working Peak Reverse Voltage: The peak voltage excluding all transient voltages (ref JESD282-B). Also sometimes known historically as PIV.			

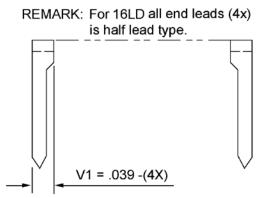
## ELECTRICAL CHARACTERISTICS @ 25 °C unless otherwise stated

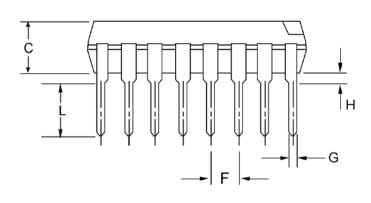
PART NUMBER	BREAKDOWN VOLTAGE V <sub>(BR)</sub> @ Ι <sub>(BR)</sub> =100 μΑ	LEAK CURR I <sub>F</sub> T <sub>A</sub> = 2	RENT	LEAK CURF I T <sub>A</sub> = 1	RENT	TOTAL CAPACITANCE C <sub>T</sub> @ 0 V	REVERSE RECOVERY TIME t <sub>rr</sub>	FORWARD VOLTAGE V <sub>F</sub> I <sub>F</sub> = 10 mA	FORWARD VOLTAGE V <sub>F</sub> I <sub>F</sub> = 100 mA
	V	μ/	A	μ	Α	pF	ns	v	V
	MIN	MAX	@V <sub>R</sub>	MAX	@V <sub>R</sub>	ТҮР	MAX	MAX	MAX

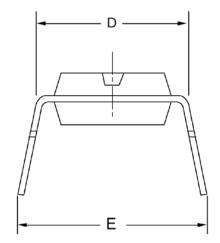


## PACKAGE DIMENSIONS





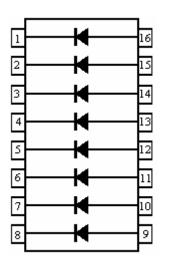




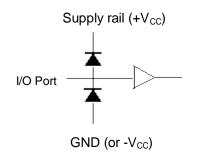
	Dimensions						
Ref.	lr	nch	Millimeters				
	Min	Max	Min	Max			
Α	0.746	0.754	18.95	19.15			
В	0.246	0.254	6.248	6.452			
С	0.126	0.134	3.200	3.404			
D	0.300	0.325	7.619	8.255			
Е	0.320	0.370	8.128	9.398			
F	0.098	0.102	2.489	2.591			
G	0.016	0.020	0.406	0.508			
Н	0.015	0.040	0.381	1.016			
J	0.058	0.062	1.473	1.575			
L	0.121	0.129	3.073	3.277			
М	0.25	REF	0.63	5 REF			



## PAD LAYOUT



CONFIGURATION



## STEERING DIODE APPLICATION